

Address Break	Plate	Function	Variable	Instruction/Data	Open Data	Array
		Register Transition				
		Pre-Comment				
		Label				XRef Heade
+	Address	Bytes	...	Mnemonic	Operands	
		PCode				
		Post-Comment				
Space						
<div> <div>LAB_140003374</div> <div>XREF[1]:</div> <div> 140003374 41 83 c6 0c ADD R14D,0xc 140003378 45 8b fe MOV R15D,R14D 14000337b 4b 8d 0c 36 LEA RCX,[R14 + R14*0x1] 14000337f e8 44 7c CALL _malloc_base 03 00 140003384 48 8b f0 MOV RSI,RAX 140003387 48 85 c0 TEST RAX,RAX 14000338a 75 10 JNZ LAB_14000339c 14000338c 48 8d 15 LEA RDX,[u_A_memory_allocation_failed_14006f5f0 5d c2 06 00 140003393 8d 48 68 LEA ECX,[RAX + 0x68] 140003396 e8 c5 df CALL FUN_140001360 ff ff 14000339b cc INT3 </div> </div>						

		LAB_1400031a5	XREF[1]
1400031a5	41 b8 22	MOV	R8D,0x22
	00 00 00		
1400031ab	b8 20 00	MOV	EAX,0x20
	00 00		
1400031b0	66 41 3b d0	CMP	DX,R8W
1400031b4	66 41 0f	CMOVZ	AX,R8W
	44 c0		
1400031b9	0f b7 d0	MOVZX	EDX,AX
1400031bc	e8 bf 16	CALL	wcschr
	00 00		
1400031c1	48 85 c0	TEST	RAX,RAX
1400031c4	75 09	JNZ	LAB_1400031cf
1400031c6	4c 8d 25	LEA	R12,[DAT_14006e838]
	6b b6 06 00		
1400031cd	eb 2c	JMP	LAB_1400031fb
		LAB_1400031cf	XREF[1]
1400031cf	4c 8d 60 02	LEA	R12,[RAX + 0x2]
1400031d3	0f b7 40 02	MOVZX	EAX,word ptr [RAX + 0x2]
1400031d7	66 85 c0	TEST	AX,AX
1400031da	74 1f	JZ	LAB_1400031fb
1400031dc	0f 1f 40 00	NOP	dword ptr [RAX]