COAL Assignment 1

(NI In real address made, 16-bit segment registers indicate base addresses of plu assisters had pointed to segment descriptor tables, which describe the barton, to segment descriptor tables, which describe the barton, to be length and access rights of the memory segment. Q1) IT As a suk, merory access takes more weeking a cycles be cause stending a single value from morning involves four steps.

1 Place the allows of the value of on the allows sus as Asserot (change) the value of the processor's 10 (pd) 3. Drit one clock cycle for merory chips to suspend of Copy date from data bus to destination appeared The contrast, register acress requires fever clock cycle.

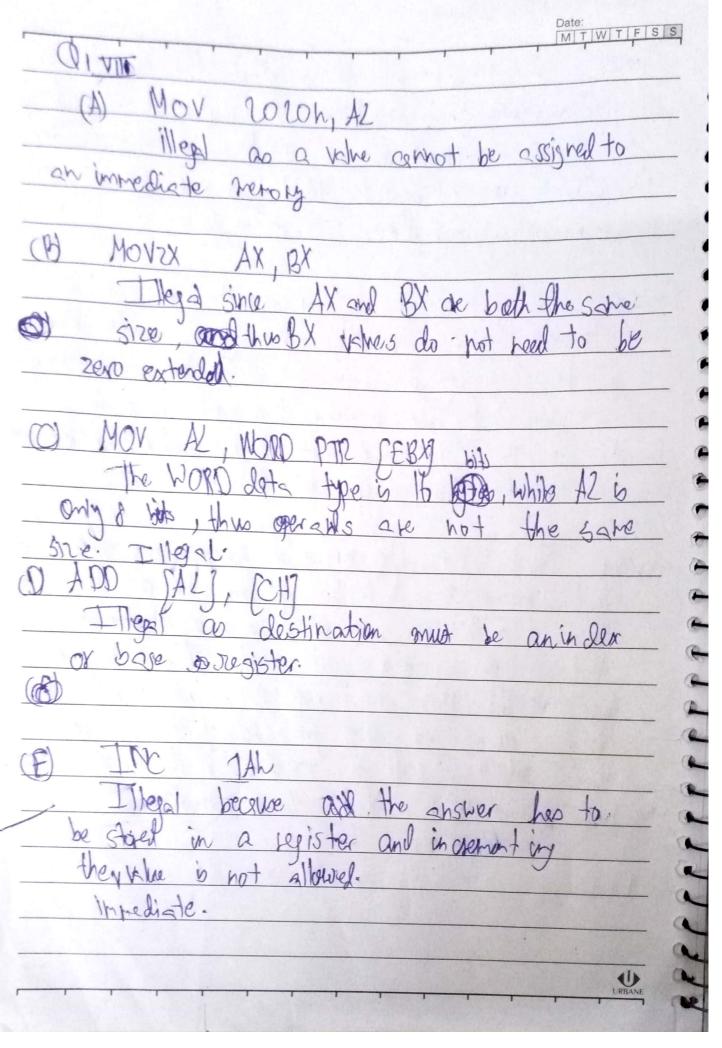
(Objuct) Industrial Robots
(2) Gaming Consoles

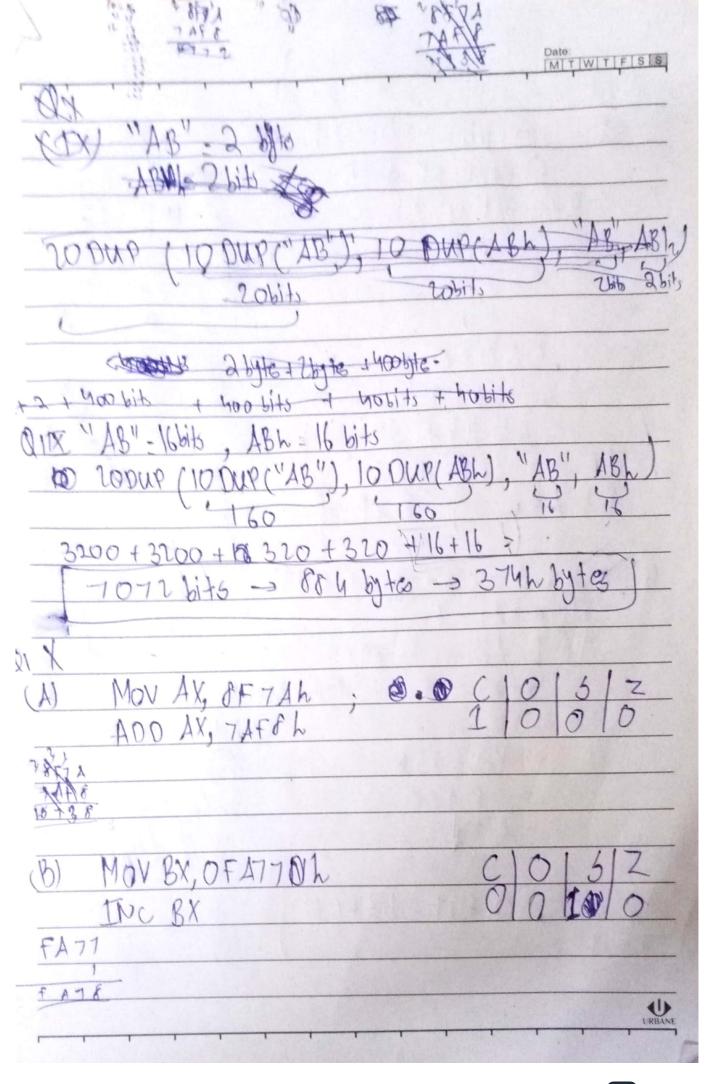
LEBANE

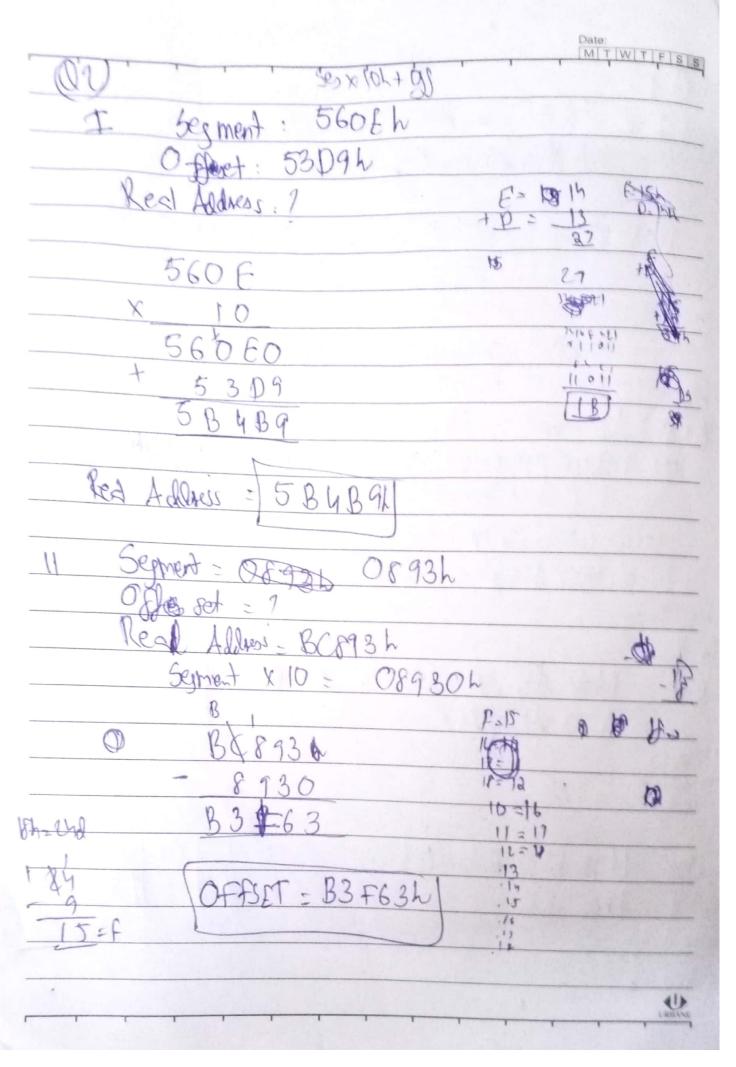
Date: 0 CIVITY Executing a machine instanction is known as an instruction execution cycle The Instruction Pointer (IP) holls the address of the instruction to be a executed I The All fetches the instanction from the instruction greate and then increments points to the next astruction CPU decodes the instruction by looking at Lihary bit pattern, which could never oper ands etc. operands are involved, the All fetches operands from registers (general purpose registers like base, counter etc) and sometimes performs calculations (done of in the ogic list or ALU) CPU executes the instantions and status flags such as zero flag, Corry flag ok If the output operand was part of the instruction. The All stores the result of the instruction in the operand. instructions are decoded using the instruction de coder, which coordinateste All and thorting pointuit Reaching program instructions from memory requires addresses to be paced on the address bus. bus coordinates transfer of data between different units of the CPU.

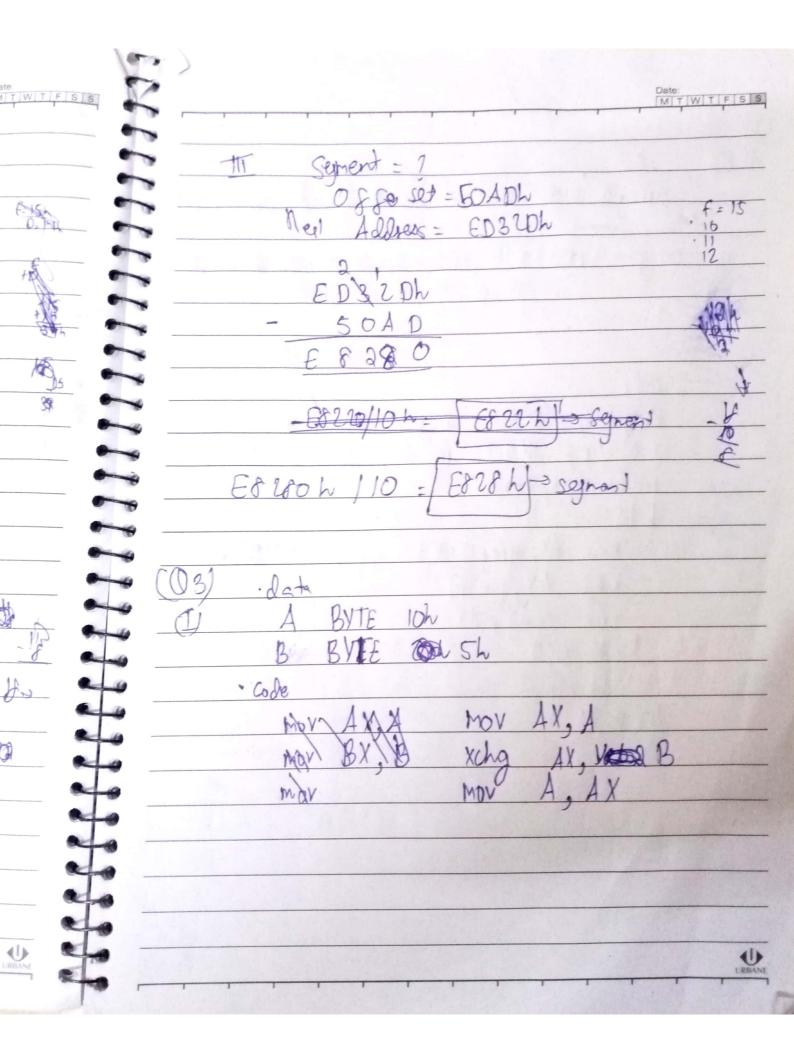
implements an instruction. An instruction cycle of process of in the computer is given a program. anderstands it and execute at it from transmy. An instruction and contain multiple machine agales. QUII This is because a program written in Jana is translated into Java byte call by a compiler which
be executed by appropriate from as a Java Virtual Machine. (IVM)

Since Java byte code is always the some and is always
executed by the JVM on a almost every computer, the streetcode is nearly machine independent. On The Whon the computer recognizes the QI(XII) alless 12FCBDIDL, a puts the value at one of All's input noisters, put on the other value on at the other input register. There parallal autput. Result is stored in a destination register 12FCBDION points to a register.









Date |M|T|W|T|F|8[8] Q3TT · lata array 1 WORD 20,19,18,17,16,15,14,18,12,11,10,9,8,7,6,5,4,3,41 array 2 WORD 12 Th, 2 h, 3h, 4h, 5h, 6h, 7h, 8h, 9h, 10h, 1142h, 15h, 14h, 15h, Ken, 17h, 8th, 19h, 20h · Code MOV esi, OFFSET amay 1 mov elop & OFSET any 2 move ecr, 20 AX, any Sesij MoV mov fesi, AX add esi, 2 add ebp, 2 g

