# KEA Driver Introduction

Contents

[PERIPHERAL DRIVERS 1](#_Toc403483896)

[EXAMPLES 2](#_Toc403483897)

[Appendix Ⅰ 3](#_Toc403483898)

[ICS API Specification 3](#_Toc403483899)

[OSC API Specification 3](#_Toc403483900)

[SIM API Specification 4](#_Toc403483901)

[WDOG API Specification 12](#_Toc403483902)

[BME API Specification 13](#_Toc403483903)

[Flextimer API Specification 15](#_Toc403483904)

[KBI API List 19](#_Toc403483905)

[PMC API List 21](#_Toc403483906)

[ACMP API Specification 22](#_Toc403483907)

[ADC API Specification 24](#_Toc403483908)

[NVM API Specification 27](#_Toc403483909)

[I2C API Specification 29](#_Toc403483910)

[SPI API Specification 32](#_Toc403483911)

[PIT API Specification 35](#_Toc403483912)

[RTC API Specification 36](#_Toc403483913)

[Cyclic Redundancy Check API Specification 37](#_Toc403483914)

[UART API Specification 37](#_Toc403483915)

[GPIO API Specification 40](#_Toc403483916)

[PWT API Specification 41](#_Toc403483917)

[BME API Specification 42](#_Toc403483918)

[BitBand API Specification 44](#_Toc403483919)

[CAN APIs Specification 44](#_Toc403483920)

## PERIPHERAL DRIVERS

Freescale is making automotive development easy with Kinetis EA series MCUs. Kinetis EA series MCUs for automotive allow fast time to market with simple tools, an extensive development environment and automotive grade qualification across -40 to 125 °C temperature ranges. This series of MCUs includes a broad set of documentation, hardware and software tools and application notes to help speed development and reduce design costs.

Kinetis EA series MCUs for automotive provide an automotive-grade, scalable, 32-bit portfolio with a broad ARM ecosystem for a wide range of automotive applications. The EA series include a powerful array of analog, communication and timing and control peripherals with specific Flash memory size and pin counts. This MCU series offers low-power, built on the ARM®-Cortex® M0+ core, high-robustness and cost-efficiency to provide automotive developers with an optimized ARM-based solution. This series was designed with EMC/ESD in mind to ensure strong noise immunity and low radiated emissions. Supported peripheral drivers are listed in alphabetical order:

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **F** | **G** | **I** | **K** | **N** | **O** | **P** | **R** | **S** | **U** | **W** |
| [ACMP](#_ACMP_API_Specification) | [BME](#_BME_API_Specification) | [CRC](#_Cyclic_Redundancy_Check) | [FTM](#_Flextimer_API_Specification) | [GPIO](#_GPIO_API_Specification) | [ICS](#_ICS_API_Specification) | [KBI](#_KBI_API_List) | [NVM](#_NVM_API_Specification) | [OSC](#_OSC_API_Specification) | [PIT](#_PIT_API_Specification) | [RTC](#_RTC_API_Specification) | [SIM](#_SIM_API_Specification) | [UART](#_UART_API_Specification) | [WDOG](#_WDOG_API_Specification) |
| [ADC](#_ADC_API_Specification) | [BIT\_BAND](#_BitBand_API_Specification) | [CAN](#_CAN_APIs_Specification) |  |  | [I2C](#_I2C_API_Specification) |  |  |  | [PMC](#_PMC_API_List) |  | [SPI](#_SPI_API_Specification) |  |  |
|  |  |  |  |  |  |  |  |  | [PWT](#_PWT_API_Specification) |  |  |  |  |

## EXAMPLES

Software examples demonstrate correct use of the Kinetis EA peripheral drivers in an application. Each peripheral driver is complemented by one or more software examples depending on the driver complexity and number of features to be demonstrated.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Peripheral** | **First Example** | **Second Example** | **Third Example** | **Fourth Example** |
| [ADC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__drv.html) | [ADC FIFO Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__fifo__demo.html) | [ADC Interrupt Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__int__demo.html) | [ADC Poll Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__poll__demo.html) |  |
| [ACMP](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__drv.html) | [ACMP Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__demo.html) |  |  |  |
| [BME](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__bme__drv.html) | [BME Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__bme__demo.html) |  |  |  |
| [BIT\_BAND](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__bitband__drv.html) | [BitBand Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__bitband__demo.html) |  |  |  |
| [CAN](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__can__drv.html) | [CAN Example Node1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__can__node1__demo.html) | [CAN Example Node2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__can__node2__demo.html) | [CAN transmitting interrupt Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__can__node1withint__demo.html) |  |
| [CRC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__drv.html) | [CRC Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___c_r_c__demo.html) |  |  |  |
| [FTM](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__drv.html) | [FTM Combine Mode Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___f_t_m___combine__demo.html) | [Dual Edge Capture Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___f_t_m___dual_edge_capture__demo.html) | [Edge Align PWM Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___f_t_m___e_p_w_m__demo.html) | [Output Compare mode Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___f_t_m___output_compare__demo.html) |
| [GPIO](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__drv.html) | [GPIO demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__demo.html) | [Fast gpio demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__onecycle__demo.html) |  |  |
| [ICS](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__drv.html) | [ICS FEE to FEI Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__feefei__demo.html) |  |  |  |
| [I2C](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__drv.html) | [I2C Master Interrupt Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___i2_c___master_int__demo.html) | [I2C Master Poll Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___i2_c___master_poll__demo.html) | [I2C Slave Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___i2_c___slave__demo.html) |  |
| [KBI](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__drv.html) | [KBI Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__demo.html) |  |  |  |
| [NVM](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__drv.html) | [Flash demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___flash__demo.html) |  |  |  |
| [PIT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__drv.html) | [PIT Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___p_i_t__demo.html) |  |  |  |
| [PMC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__drv.html) | [PMC Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__demo.html) |  |  |  |
| [PWT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__drv.html) | [PWT Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___p_w_t__demo.html) |  |  |  |
| [RTC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__drv.html) | [RTC Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___r_t_c__demo.html) |  |  |  |
| [SIM](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__drv.html) | [RTC Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___r_t_c__demo.html) |  |  |  |
| [SPI](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__drv.html) | [SPI Master Polling Mode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___s_p_i__mpoll__demo.html) | [SPI Master Interrupt Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___s_p_i___master_int__demo.html) | [SPI slave Mode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___s_p_i___slave__demo.html) |  |
| [UART](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__drv.html) | [UART Polling Mode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__demo.html) | [UART Interrupt Mode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uartisr__demo.html) | [UART Loopback Demo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uartlb__demo.html) |  |
| [WDOG](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__drv.html) | [WDOG Feed Example](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__demo.html) |  |  |  |

## Appendix Ⅰ (Refer to the source files for more details)

### ICS API Specification

|  |  |
| --- | --- |
| void | [ICS\_Trim](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga6324833a0b759c61806b2e2e83961463) (uint16\_t u16TrimValue) |
|  | trim internal clock (IRC). |
|  | |
| void | [ICS\_SetClkDivider](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#gaee7a03142594bbfea2a6a8d71b9f857a) (uint32\_t u32ClkFreqKHz) |
|  | set clock divider so that the reference clock for FLL/PLL is within spec. |
|  | |
| void | [ICS\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga18aeac6aa349832111fe4d4ddec8fe5a) ([ICS\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__config__type.html#struct_i_c_s___config_type) \*pConfig) |
|  | initialize ICS to the desired clock as defined in BUS\_CLK\_HZ. |
|  | |
| void | [ICS\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga178e9ccc02ef310f72028c3a54f4513c) (void) |
|  | initialize ICS to the default state. |
|  | |
| \_\_STATIC\_INLINE void | [ICS\_EnableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga60fd7c9f2f9ed247bd7b08ef52149189) (void) |
|  | enable interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [ICS\_DisableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga9f34251572accc9858257e338d9d1b55) (void) |
|  | disable interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [ICS\_EnableClockMonitor](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga19b7d468c79c7ad531b24a0a5989e0c0) (void) |
|  | enable clock monitor. |
|  | |
| \_\_STATIC\_INLINE void | [ICS\_DisableClockMonitor](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#gabbe36e41db98db796d54745f1c79d86b) (void) |
|  | disable clock monitor. |
|  | |
| \_\_STATIC\_INLINE void | [ICS\_SetBusDivider](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ics__api__list.html#ga0df12575b0025f86f2eb78c65b3601b0) (uint8\_t u8BusDivide) |
|  | set bus divider BDIV bit field. |

### OSC API Specification

|  |  |
| --- | --- |
| void | [OSC\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gacddf7fea20d81b353dd74b58aa07a7a3) ([OSC\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__config__type.html#struct_o_s_c___config_type) \*pConfig) |
|  | initialize XOSC with given parameters: GAIN, RANGE in control structure. |
|  | |
| void | [OSC\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gaedda4109b30ef08ef6ac708cdc7bdab8) (void) |
|  | initialize OSC to the default state. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gae1f7ac89affbb272e0f41060b9b07589) (void) |
|  | enable OSC. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gafbb333ef0d678528623e1499ec5bc767) (void) |
|  | disable OSC. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SetLowRange](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#ga5edb1285f6dcd3b654eb536dc2e00dca) (void) |
|  | set low range of oscillator. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SetHighRange](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gaa8d16aeba979a0acb60153a97086f1da) (void) |
|  | set high range of oscillator |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SetHighGain](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#ga25378b63a2ee5f10e2e41e616953f465) (void) |
|  | set high gain of oscillator. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SetLowGain](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gac12dbe19684d98d89851cfe42faae460) (void) |
|  | set low gain of oscillator. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SelectCrystal](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gacbd545bccf566cf4e5cf90600f055502) (void) |
|  | select crystal as clock source. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_SelectClock](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gaf36965f6ada0e396c346929ea6dbf0a8) (void) |
|  | select active clock as clock source |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_ActiveInStop](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#ga50257b80270d38ef4638a9bd2cad653e) (void) |
|  | enable OSC in stop mode. |
|  | |
| \_\_STATIC\_INLINE void | [OSC\_InactiveInStop](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__osc__api__list.html#gafddce93e9a84fe6aa89b9863fbd4a465) (void) |
|  | disable OSC in stop mode. |

### SIM API Specification

|  |  |
| --- | --- |
| void | [SIM\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga1f4ba9422c823b28bc37e5d310744d6f) ([SIM\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__config__type.html#struct_s_i_m___config_type) \*pConfig) |
|  | initialize SIM registers. |
|  | |
| void | [SIM\_SetClockGating](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gacd93b05cfe7dac2d8dc35990750c097b) (uint32\_t u32PeripheralMask, uint8\_t u8GateOn) |
|  | set SIM clock gating registers to enable or disable peripheral clocks. |
|  | |
| uint32\_t | [SIM\_GetStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga3ea84e9d7a96323f408ac5e69a036120) (uint32\_t u32StatusMask) |
|  | read the corresponding status flags. |
|  | |
| uint8\_t | [SIM\_ReadID](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaadb595dc7218afb30121a3d51dbe64c2) ([IDType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__id__types.html#gaa0a690a2c08f2c4e8568b767107275a2) sID) |
|  | read the corresponding ID. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_DelayFTM2Trig2ADC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9cd0593d03900ffb6969c6e420201e10) (uint8\_t u8Delay) |
|  | delay FTM2 triggering ADC for u8Delay bus clock output divide. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableClockOutput](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga3700c5ba0929188dcdbfed64409fedda) (void) |
|  | enable clock output. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_DisableClockOutput](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga26b5a4c26b48c20ef1a9a75c5414f3a6) (void) |
|  | disable clock output. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetClockOutputDivide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad646e520bde215f190be9da13fed369d) (uint8\_t u8Divide) |
|  | set bus clock output divide. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableUART0RXDConnectFTMOCH1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf81f45651b2de4b9d97e5cf5fd7204f5) (void) |
|  | enable UART0 RXD connect with UART0 module and FTM0 channel 1.. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableUART0Modulation](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga41af1c171539a623bf1691f8ed583168) (void) |
|  | enable UART0 TX modulation. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_DisableUART0Modulation](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf77250e2f52fc60810dcd208df47edfd) (void) |
|  | disable UART0 TX modulation. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_GenerateSoftwareTrig2FTM2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gabc45aa2b5cec02639e7257edee95a13e) (void) |
|  | generate a softare sync trigger to FTM2 module (trigger). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetPWTIN3InputFromUART0Rx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga2a05b0d641902ef69ce23da3a8f2ad49) (void) |
|  | select PWTIN3 input signal from UART0RX. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetPWTIN3InputFromUART1Rx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga42214b81b7a1f24c2c36e3a81a68a08d) (void) |
|  | select PWTIN3 input signal from UART1RX. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetPWTIN3InputFromUART2Rx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga52ba28af45e4933e2f32694822453734) (void) |
|  | select PWTIN3 input signal from UART2RX. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetPWTIN2InputFromACMP0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga17e35c91ec352e4f2fd457a17b38edc8) (void) |
|  | select PWTIN2 input signal from ACMP0\_OUT. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetPWTIN2InputFromACMP1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga6711b4e15b618eb30a4804c8cd3541b9) (void) |
|  | select PWTIN2 input signal from ACMP1\_OUT. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableI2C0OuputInvertion](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga98a0d6a95e4b89c436f90cc1002ae4fd) (void) |
|  | enable invertion of the I2C output. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_DisableI2C0OuputInvertion](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga32abbfc354624491ab335b631018048f) (void) |
|  | disable invertion of the I2C output. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_Enable4WireI2C0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga44da02a72f745e50fd7db00acf354b96) (void) |
|  | enable 4-wire I2C. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_Disable4WireI2C0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga354c7014a9c90b6a2bc9381d5ecdbe9c) (void) |
|  | disable 4-wire I2C.. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableUART0Filter](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga7f9f34baed3f6ec10b966b0fd4d2f3f6) (void) |
|  | UART0 RXD input signal is connected to UART0 module directly. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableUART0FilterByACMP0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga90edf24b88bf87199a28c9e3639c4535) (void) |
|  | UART0 RXD input signal is filtered by ACMP0, then injected to UART0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableUART0FilterByACMP1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga5911d14504add411b892a04c7d1367cf) (void) |
|  | UART0 RXD input signal is filtered by ACMP1, then injected to UART0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByRTC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gabd0e79d73f2f2ad1750ca37a91da1e77) (void) |
|  | set ADC hardware trigger source to RTC overflow. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByPIT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gae2a8e631c354645e6e942770558978c0) (void) |
|  | set ADC hardware trigger source to PIT . |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByFTM2Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga7f532c9f031fa966935177f37b2ca210) (void) |
|  | set ADC hardware trigger source to FTM2 init trigger with 8-bit programmable delay. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByFTM2Match](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga58e8c585af60363b3d1b05339d321c53) (void) |
|  | set ADC hardware trigger source to FTM2 match trigger with 8-bit programmable delay. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByPITCH0Overflow](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac2dcd301236eaf92ea392a7faabb2667) (void) |
|  | set ADC hardware trigger source to PIT channel0 overflow. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByPITChannel1Overflow](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga078fd0aeae5c2a9b97d4207cf2722d8c) (void) |
|  | set ADC hardware trigger source to PIT channel1 overflow. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByACMP0Out](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga88f97d447e3dac540f4837de6883ba1e) (void) |
|  | set ADC hardware trigger source to ACMP0 out. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_TriggerADCByACMP1Out](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga704486ad21abeb908723b604d66100dc) (void) |
|  | set ADC hardware trigger source to ACMP1 out. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelACMP0AsFTM2Trigger0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gabee51077e2401e6307583e7c2e3fbee6) (void) |
|  | Select ACMP0 output as the trigger0 input of FTM2. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelACMP1AsFTM2Trigger0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga52314d321951cd4779a4bcbaf801f6b9) (void) |
|  | Select ACMP1 output as the trigger0 input of FTM2. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableRTCCapture](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga2971c09da2006757d8c361c2fb7fd5ec) (void) |
|  | enable RTC capture to FTM1 input channel1. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_EnableACMP0InputCapture](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gab8427dacb10823ca9c3c8c9fdecd9120) (void) |
|  | enable ACMP0 input capture to FTM1 input channel0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK0ForPWT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga7000731519d50b1ee6251de78052efee) (void) |
|  | select TCLK0 for PWT module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK1ForPWT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga78940688bf379fbfc4a1983667225f04) (void) |
|  | select TCLK1 for PWT module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK2ForPWT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9810b3eee38c6f3f627ea09c06ed3a52) (void) |
|  | select TCLK2 for PWT module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK0ForFTM2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga56c3f93f3c827596cb973486495bdddb) (void) |
|  | select TCLK0 for FTM2 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK1ForFTM2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga06202dc34320a5c950d87c98e6bfb030) (void) |
|  | select TCLK1 for FTM2 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK2ForFTM2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gae6c7cc29cc11fbef93c02d19b7e075ec) (void) |
|  | select TCLK2 for FTM2 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK0ForFTM1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaa5375556705e7319ff3ca8dc228291f4) (void) |
|  | select TCLK0 for FTM1 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK1ForFTM1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga1edc15573b3040168d68ce54bf7ef421) (void) |
|  | select TCLK1 for FTM1 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK2ForFTM1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gae61f1e61cf41a9e6cea480590749a9ef) (void) |
|  | select TCLK2 for FTM1 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK0ForFTM0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga66060b31150fdb226155c183b287b36b) (void) |
|  | select TCLK0 for FTM0 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK1ForFTM0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga87221cd4be3e5205c898dc4ce08880f3) (void) |
|  | select TCLK1 for FTM0 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SelectTCLK2ForFTM0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga771204e2436c7454390df2abc5067d6a) (void) |
|  | select TCLK2 for FTM0 module. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM1CH1ToPTC5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac11d7ddef0a73158f24f3b27e8dea32a) (void) |
|  | remap FTM1CH1 to pin PTC5 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM1CH1ToPTE7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad93664f1ee4d7ebe52db9a4077ba66c1) (void) |
|  | remap FTM1CH1 to pin PTE7. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM1CH0ToPTC4](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga6b6fd198f86beae46b1c43aabf8119bf) (void) |
|  | remap FTM1CH0 to pin PTC4 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM1CH0ToPTH2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga19a91fdfa02a2854555903f36cbac469) (void) |
|  | remap FTM1CH0 to pin PTH2. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM0CH1ToPTA1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9cc3baa17f7f810042a2b0175af74b9d) (void) |
|  | remap FTM0CH1 to pin PTA1 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM0CH1ToPTB3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga78ca11f6f8ef438c81f9bafc70d299c3) (void) |
|  | remap FTM0CH1 to pin PTB3. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM0CH0ToPTA1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9f6ca442e8f4a59d9ebf6c09432890f7) (void) |
|  | remap FTM0CH0 to pin PTA0 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM0CH0ToPTB3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf4d4820070fc39d60a8bfe54458455e6) (void) |
|  | remap FTM0CH0 to pin PTB2. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART0ToPTB\_0\_1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga3592d1fd3b1ef89155f8fb396d6873ac) (void) |
|  | remap UART0 to pin PTB0/1 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART0ToPTA\_2\_3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga73017a784dd60e30b814c3324500911e) (void) |
|  | remap UART0 to pin PTA2/3. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapSPI0ToPTB\_2\_3\_4\_5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9122f5e985af649a69382452948fa0dd) (void) |
|  | remap SPI0 to pin PTB2/3/4/5 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapSPI0ToPTE\_0\_12\_3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf67351266232ec49c9af44b86668d210) (void) |
|  | remap SPI0 to pin PTE01/2/3. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapI2CToPTA\_2\_3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf18474c6f4c79ccaf9fedd3000ecd1e6) (void) |
|  | remap I2C to pin PTA2/3 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapI2CToPTB\_6\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad7815606dd420222653e29e10ac50eaa) (void) |
|  | remap I2C to pin PTB6/7 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapRTCToPTC4](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9db2592245b3983d2f8f4ef3a2e9ece2) (void) |
|  | remap RTC to pin PTC4 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapRTCToPTC5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gab6cf5127f97acc75493c6cd74741801c) (void) |
|  | remap RTC to pin PTC5. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapMSCANToPTC\_6\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga1c0082e40397acdc4e48d8537bd7b0ac) (void) |
|  | remap MSCAN to pin PTC6/7 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapMSCANToPTH\_2\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga241b7e58b5aebe33709fc5e66c5fbab2) (void) |
|  | remap MSCAN to pin PTH2/7. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapPWTIN1ToPTB0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf27c284a191683eaf94d2d1367bb1ac3) (void) |
|  | remap PWTIN1 to pin PTB0 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapPWTIN1ToPTH7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac279188ccd583a09e05db1ab6cd24d40) (void) |
|  | remap PWTIN1 to pin PTH7. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapPWTIN0ToPTD5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga1005f196250602411fbf589be49e84e9) (void) |
|  | remap PWTIN0 to pin PTD5 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapPWTIN0ToPTE2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaf34b65367f66a900ed0e99edcdca515d) (void) |
|  | remap PWTIN0 to pin PTE2. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART2ToPTD\_6\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga7310062eb82093c0ac43ffca388d5d25) (void) |
|  | remap UART2 to pin PTD6/7 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART2ToPTI\_0\_1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga91053598230f9ba7526975a18e07be29) (void) |
|  | remap UART2 to pin PTI0/1. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART1ToPTC\_6\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga58d9c25646cd305ef90b89638bb45ba7) (void) |
|  | remap UART1 to pin PTC6/7 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapUART1ToPTF\_2\_3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga1b68cec58984911055e0fe57811cd720) (void) |
|  | remap UART1 to pin PTF2/3. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapSPI1ToPTD\_0\_1\_2\_3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaecbf4ef8d7d4023bc312e8e394e41185) (void) |
|  | remap SPI1 to pin PTD0/1/2/3 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapSPI1ToPTG\_4\_5\_6\_7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad993605d9ac2213bb0bc4966b6936e0a) (void) |
|  | remap SPI1 to pin PTG4/5/6/7. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapI2C1ToPTE\_0\_1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad921f8e0cf35d24359142e1df8a0e92a) (void) |
|  | remap I2C1 to pin PTE0/1 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapI2C1ToPTH\_3\_4](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga3ec555244352bed6ca803be28d9cceb2) (void) |
|  | remap I2C1 to pin PTH3/4. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH5ToPTB5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga9c00788d18aef82383313ae1cf3531a4) (void) |
|  | remap FTM2CH5 to pin PTB5 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH5ToPTG7](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gacbd5bb84eea7f3de80d34c9a4e77bd54) (void) |
|  | remap FTM2CH5 to pin PTG7. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH4ToPTB4](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga618fa4efbfe0d1c2c16b79c981c5e381) (void) |
|  | remap FTM2CH4 to pin PTB4 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH4ToPTG6](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gad8a669e5bbd922384046d27c48b093cc) (void) |
|  | remap FTM2CH4 to pin PTG6. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH3ToPTC3](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gafc0305d34522c99c258c423eb60d05c6) (void) |
|  | remap FTM2CH3 to pin PTC3 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH3ToPTD1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga61c6435860e64b576155744099354f39) (void) |
|  | remap FTM2CH3 to pin PTD1. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH3ToPTG5](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga0f4656c6ee6ba4c8e5c6baa5f2adf31d) (void) |
|  | remap FTM2CH3 to pin PTG5. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH2ToPTC2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac43e1c2e85c3e256b2f6f8d8294882bd) (void) |
|  | remap FTM2CH2 to pin PTC2 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH2ToPTD0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga88a837c16c2e7692a81c0e73a0780078) (void) |
|  | remap FTM2CH2 to pin PTD0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH2ToPTG4](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac7f2e65563dfccf10b14809be722a079) (void) |
|  | remap FTM2CH2 to pin PTG4. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH1ToPTC1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gaa5fe9bd5d208a267dfac1759f0872a34) (void) |
|  | remap FTM2CH1 to pin PTC1 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH1ToPTH1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga46a828f12c3965ac194dab734b5ee727) (void) |
|  | remap FTM2CH1 to pin PTH1. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH1ToPTF1](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga6631ff17f4ef422b068643a5394f0e0d) (void) |
|  | remap FTM2CH1 to pin PTF1. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH0ToPTC0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga0b7c003ceb6174ef4e3e5e24bd82e7c3) (void) |
|  | remap FTM2CH0 to pin PTC0 (default). |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH0ToPTH0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga58658354d0b0718f39aec757ef4a7aa8) (void) |
|  | remap FTM2CH0 to pin PTH0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_RemapFTM2CH0ToPTF0](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gac6cc7dba48998ed08655894c910d1082) (void) |
|  | remap FTM2CH0 to pin PTF0. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetClock3Divide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga16f48914ffd1605066fb70ae15f211a8) (void) |
|  | set clock3 divide CLKDIV. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetClock2Divide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga89f7dbe0b4949ed003fef7d941b06d8b) (void) |
|  | set clock2 divide CLKDIV. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetClock1Divide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#gab1190cded8be49b7530ca96991dfa439) (uint8\_t u8divide) |
|  | set clock1 divide CLKDIV. |
|  | |
| \_\_STATIC\_INLINE void | [SIM\_SetBusDivide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__sim__api__list.html#ga14b6768516984080a9825b06746a3e83) (uint8\_t u8Divide) |
|  | set bus divide BUSDIV. |

### WDOG API Specification

|  |  |
| --- | --- |
| void | [WDOG\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga097fb423605c7cd92592686aaecac037) (void) |
|  | Watchdog timer disable routine. |
|  | |
| void | [WDOG\_DisableWDOGEnableUpdate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga5884076d6008447d11545ea5d3e1fa20) (void) |
|  | Watchdog timer disable routine with update enabled. |
|  | |
| void | [WDOG\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga32f6a8aa8f05de0acc1dfe97f276d7f2) (void) |
|  | Watchdog timer enable routine. |
|  | |
| void | [WDOG\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#gade82a00a2fec6c56f559809e36180cb7) ([WDOG\_ConfigPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__config__type.html#ga41e0808e23bd41d8bb97c2d9e5ea54d8) pConfig) |
|  | initialize watchdog. |
|  | |
| void | [WDOG\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga2005aba06e85ac1d31597ce9b65bbcbe) (void) |
|  | initialize watchdog to the default state. |
|  | |
| void | [WDOG\_Feed](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#gabf4cfa0c00f36eb71896f2ce1e884d27) (void) |
|  | feed/refresh watchdog. |
|  | |
| void | [WDOG\_EnableUpdate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#gac3fd70dd7e8361e8c81da6c624668e49) (void) |
|  | enable update of WDOG. |
|  | |
| void | [WDOG\_DisableUpdate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga0ce5ceb5bd364d2016ae422339ecc634) (void) |
|  | disable update of WDOG. |
|  | |
| \_\_STATIC\_INLINE void | [WDOG\_SetTimeOut](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#gab26680d80838a0b3f6ccabb3937b700f) (uint16\_t u16TimeOut) |
|  | set time out value for WDOG. |
|  | |
| \_\_STATIC\_INLINE void | [WDOG\_SetWindow](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#ga59e689f8e9039a827ccfe1b0ebb1460f) (uint16\_t u16WinTime) |
|  | set window value for WDOG. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [WDOG\_IsReset](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__wdog__api__list.html#gafcb5d4ff29d34d87e165a5610d3d4f98) (void) |
|  | check if watchdog reset occurs. |

### BME API Specification

|  |  |
| --- | --- |
| #define | [BME\_AND](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1aa902312bef07dd995b8f2de5a94461)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_OR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gae647428045f10813c9adf5b4705385f3)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_XOR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga5363f0afb2bbc2673a069509e1bac1e6)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BIT\_CLEAR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gac6cd56c73903d423100506ecaa66cbd3)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1748e7c9c7fa55745a87292931eef9fe)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BITFIELD\_INSERT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga83b580edd411d979714b7b289f54c060)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga137adf383dbe3b4b851c0e769b089016)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_AND\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gaa9ba136dd0b51048e2bc2e93d4929589)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_OR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga5db86a62bc2d981fcd541f692ea9d74e)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_XOR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gae85e1942e2b95f7a1a4cc6d083f5ee52)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BIT\_CLEAR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga4a144977bf06634c568655f9a7a0ff05)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga84100d47a0f13ab2e527c0db19ce76b9)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BITFIELD\_INSERT\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gab4b15fff26eda50d0b93a834c5367634)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gac27561cbff63967157b8d56e02f343ad)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_AND\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga0feed195dc2eb8d813a49236d8d5e74e)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_OR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gada49ceacf4be9a3f3760803cbb9ce920)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_XOR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1017565a07d0554b314bf073e67d1d18)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BIT\_CLEAR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gaa3f3269ccb4258e434ab4bf0bf6753b0)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga20a106dda348cf1541830a0f78880703)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BITFIELD\_INSERT\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga4254ebadcaba31e730669ae8fd7dcd35)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1fb14e4c123fe034f259371cf4abbb6f)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address) for 16-bit data. |

### Flextimer API Specification

|  |  |
| --- | --- |
| void | [FTM\_ClockSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga686a2b9444f07cd64a37ea0955363f07) (FTM\_Type \*pFTM, uint8\_t u8ClockSource, uint8\_t u8ClockPrescale) |
|  | set the ftm moule clock source and prescale. |
|  | |
| void | [FTM\_PWMInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gab1bd3347f9e38ea3f6241d2acc0bbe14) (FTM\_Type \*pFTM, uint8\_t u8PWMModeSelect, uint8\_t u8PWMEdgeSelect) |
|  | general configuration to FTM\_No to high ture EPWM mode, clock soure is the system clock, MOD, CnV are the initial value. |
|  | |
| void | [FTM\_InputCaptureInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gacd826aa3f9c67feb739c0a664f6b8bd7) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8CaptureMode) |
|  | general configuration to FTM\_No to input capture mode, enable interrupt. |
|  | |
| void | [FTM\_DualEdgeCaptureInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga6aaa0a37fe7d890e48bbe44183515da7) (FTM\_Type \*pFTM, uint8\_t u8ChannelPair, uint8\_t u8CaptureMode, uint8\_t u8Channel\_N\_Edge, uint8\_t u8Channel\_Np1\_Edge) |
|  | general configuration to FTM\_No to Dual Edge Capture mode to measure the width or the period of a pulse. |
|  | |
| void | [FTM\_OutputCompareInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga35d5be01d92b035a9f3fd9ba638f4655) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8CompareMode) |
|  | general configuration to FTM\_No to input capture mode. |
|  | |
| void | [FTM\_SoftwareSync](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gaac28027e84854a9c0b12b47699380f3f) (FTM\_Type \*pFTM) |
|  | general configuration to FTM2 to start software synchronization. |
|  | |
| void | [FTM\_HardwareSync](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga71dd8c3e353ffb9a0e5787a098ff1eaa) (FTM\_Type \*pFTM, uint8\_t u8TriggerN) |
|  | general configuration to FTM to enable hardware synchronization. |
|  | |
| void | [FTM\_HardwareSyncCombine](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga016bd38bcb66443dfbdf085ca3609c45) (FTM\_Type \*pFTM, uint8\_t u8TriggerMask) |
|  | general configuration to FTM to enable hardware synchronization, more then 1 trigger. |
|  | |
| void | [FTM\_GenerateTrig2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gae498c4126ad76750476854aeb5a4787c) (FTM\_Type \*pFTM) |
|  | generate FTM2 hardware trigger 2,Note: please call FTM\_HardwareSyncCombine first. |
|  | |
| void | [FTM\_PWMDeadtimeSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga7c14fa84f3ee2dc52d39ba3d6519e233) (FTM\_Type \*pFTM, uint8\_t u8PrescalerValue, uint8\_t u8DeadtimeValue) |
|  | general configuration to FTM\_No to start software synchronization. |
|  | |
| void | [FTM\_OutputMaskSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gab5b1c57ceb45f7dbe6b7139f6478c5d8) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | set output mask. |
|  | |
| void | [FTM\_SWOutputControlSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga9ffb02e21683938d4fb33720da278835) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8ChannelValue) |
|  | general configuration to FTM\_No to start software synchronization. |
|  | |
| void | [FTM\_PolaritySet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga078ff0b4edb6ec267c31c74cc12f98ea) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8ActiveValue) |
|  | set PWM polarity. |
|  | |
| void | [FTM\_SetDebugModeBehavior](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gafbe8f588a0be559e3fafcded38ee8a99) (FTM\_Type \*pFTM, uint8\_t u8DebugMode) |
|  | set FTM behavior in debug mode. |
|  | |
| void | [FTM\_SetTOFFrequency](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gac22426abdd509212a41affb5cc9487aa) (FTM\_Type \*pFTM, uint8\_t u8TOFNUM) |
|  | Selects the ratio between the number of counter overflows to the number of times the TOF bit is set. |
|  | |
| void | [FTM\_InvertChannel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga345e2b0aae4147e350a309be397a6b6e) (FTM\_Type \*pFTM, uint8\_t u8ChannelPair) |
|  | swap the output of CH(n) and CH(n+1). |
|  | |
| void | [FTM\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga1fd8c77962b5a51aa2b8cbcdffda7b37) (FTM\_Type \*pFTM, [FTM\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__configsturct.html#struct_f_t_m___config_type) \*pConfig) |
|  | configure the FTM as specified control parameters, CnSC and CnV not included. |
|  | |
| void | [FTM\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga4e65c1d296b9a6045b09276d87b40e74) (FTM\_Type \*pFTM) |
|  | close the FTM moudle. |
|  | |
| void | [FTM\_ChannelInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga53a56fc6429a8364e33606030e2ee0d6) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, [FTM\_ChParamsType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__chconfigsturct.html#struct_f_t_m___ch_params_type) \*pTFTMCH\_Params) |
|  | configure the FTM channels, CnSC and CnV are included. |
|  | |
| void | [FTM\_SetDutyCycleCombine](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga13dbfbc5688540723773e3c5728967ad) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8DutyCycle) |
|  | set the FTM channel value register per duty cycle and modulo for combine mode odd channel no must be provided and even channel value register is not changed. |
|  | |
| void | [FTM\_SyncConfigActivate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga9da81bfddbbfcf1476b47cf216fbe2d6) (FTM\_Type \*pFTM, uint32\_t u32ConfigValue) |
|  | configure the FTMx\_SYNCONF register including SW and HW Sync selection. |
|  | |
| void | [FTM\_SyncConfigDeactivate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga16d0d608b3da85e2b2fd8c0c618dc80d) (FTM\_Type \*pFTM, uint32\_t u32ConfigValue) |
|  | configure the FTMx\_SYNCONF register including SW and HW Sync selection. |
|  | |
| void | [FTM\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga7d74aef0a295de980694ffc7958cb53b) (FTM\_Type \*pFTM, FTM\_CallbackPtr pfnCallback) |
|  | This function sets the callback function. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_EnableOverflowInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga7b3b9be4f2750bdeed98b5bb08f35c62) (FTM\_Type \*pFTM) |
|  | enable the over flow interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_DisableOverflowInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga9c6d0ecfa3311d8f0cfb4854ab32f4eb) (FTM\_Type \*pFTM) |
|  | disable the over flow interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_EnableChannelInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gad39d683806bd13939746783c4a3b6812) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | enable the channel interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_DisableChannelInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gada527b359602910ec488f319332e6278) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | disable the channel interrupt. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetOverFlowFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga233986d50419ba974e874685334b6491) (FTM\_Type \*pFTM) |
|  | get the over flow flag. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_ClrOverFlowFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga79178dac0a9e0949094cd3ceed2da30c) (FTM\_Type \*pFTM) |
|  | clear the over flow flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetChannelFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga54b4ffdc08fa32b51286abd747779bde) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | get the channel flag. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_ClrChannelFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga587eff1fcf7208bed4ceb5b551ed4aa4) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | clear the channel flag. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_WriteProtectionEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga5d1282ed1312a5abcc14c107b8d5079b) (FTM\_Type \*pFTM) |
|  | enable the write protection function.Write protected bits cannot be written. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_WriteProtectionDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga88f644945564f20280fa5513cbf77f30) (FTM\_Type \*pFTM) |
|  | disable the write protection function.Write protected bits can be written. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetFTMEnhanced](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga20b9425f3b4d077e3b2abeccaf46b505) (FTM\_Type \*pFTM) |
|  | set FTMEN bit to enable FTM-specific register. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetFTMBasic](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga64e2ba7ad0aac06f7c6ea2f359243fea) (FTM\_Type \*pFTM) |
|  | clear FTMEN bit to disable FTM-specific registers, only TPM-compatible registers can be used. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetModValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gaba0b419c821efa3d700b1f3e5996d5ec) (FTM\_Type \*pFTM, uint16\_t u16ModValue) |
|  | set the ftm mod value. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetChannelValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gabbad632a7d0265e70b0c789c5b4aa992) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint16\_t u16ChannelValue) |
|  | set the ftm channel value. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetCounterInitValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga577ea129d1ae46431a2d1a5ff857a1cd) (FTM\_Type \*pFTM, uint16\_t u16CounterValue) |
|  | set the ftm channel value. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_MaskChannels](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga977bf8a361607905ed9e8af9206e821b) (FTM\_Type \*pFTM, uint16\_t u16ChMask) |
|  | set the channel output mask value, FTM2 used only. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_UnMaskChannels](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga80c82c02ea068e08e743c191b9a4be55) (FTM\_Type \*pFTM, uint16\_t u16ChMask) |
|  | clear the channel output mask value, FTM2 used only. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetChannelsPolarity](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gab0fa3cd16fdd7ea82daa77cdb35deff7) (FTM\_Type \*pFTM, uint8\_t u8ChsPolValue) |
|  | set FTM channels polarity. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetChannelsPolarity](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gaeccf68887ca85d7b5242ca91e4358480) (FTM\_Type \*pFTM) |
|  | get FTM channels polarity. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_EnableEnhancedSYNCMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga7a2521e6e501a3205e915c1fc95df989) (FTM\_Type \*pFTM) |
|  | select the enhanced SYNC mode. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_DisableEnhancedSYNCMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga358b45e1033fb65e753e6e894c39697a) (FTM\_Type \*pFTM) |
|  | select the legacy SYNC mode. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetExternalTrigger](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gaaad4ebfd2b4ece084d7959f95a5462da) (FTM\_Type \*pFTM, uint8\_t u8TirggerSource) |
|  | set the external trigger source. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetExternalTriggerFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gac9fbffd9982b162439268b540b7b3a64) (FTM\_Type \*pFTM) |
|  | get the external trigger flag. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetLoadEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gab8c03b8ae33d27e79301ca967c96c7c3) (FTM\_Type \*pFTM) |
|  | set LDOK bit. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_SetLoadMatchChannel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga3ed3686d1d5c94fbefe5b8fd8c07688e) (FTM\_Type \*pFTM, uint8\_t u8Matchchannel) |
|  | set the channel in the matching process. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_InputCaptureFilterClr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gaa7331b3b3ed4cfe6a8ae87d2945e2424) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel) |
|  | disable the channel input capture filter. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_InputCaptureFilterSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gad810605e00aceda1a8794bba61d238fa) (FTM\_Type \*pFTM, uint8\_t u8FTM\_Channel, uint8\_t u8FilterValue) |
|  | set the channel input capture filter value. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga65a31cac84dba69c4b4587ec34c091c5) (FTM\_Type \*pFTM, uint8\_t u8FaultPin) |
|  | enable the fault input pin. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga6590ca7f8b0c4e41b0a1203164b7ed17) (FTM\_Type \*pFTM, uint8\_t u8FaultPin) |
|  | disable the fault input pin. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinFilterEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gab85022ef4624e8e9a8f21870cf3a1144) (FTM\_Type \*pFTM, uint8\_t u8FaultPin) |
|  | enable the fault pin filter. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinFilterDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#gad680bd8a72a7fad57bd07743ed06f4a0) (FTM\_Type \*pFTM, uint8\_t u8FaultPin) |
|  | disable the fault pin filter. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinFilterCDisableAll](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga4da12de9000295ec3cdf86dc507c343e) (FTM\_Type \*pFTM) |
|  | disable all the fault pins filter together. |
|  | |
| \_\_STATIC\_INLINE void | [FTM\_FaultPinFilterSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga1bca7c4875e800302398b4e7276b1faa) (FTM\_Type \*pFTM, uint8\_t u8FilterValue) |
|  | set the fault filter value. All channels share the same filter value. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetFaultDetectionLogicORFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga8e65ef31145731ea38c3d2f7d1a48236) (FTM\_Type \*pFTM) |
|  | get the logic OR of all the fault detection flags |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetFaultDetectionFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga5ec9b8b9e3168014fbd02cb5cda56b77) (FTM\_Type \*pFTM, uint8\_t u8FaultPin) |
|  | get the fault detection flag |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [FTM\_GetFaultInputsLogicORValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__ftm__api__list.html#ga7f610e774c16eca212f3008bb31a5691) (FTM\_Type \*pFTM) |
|  | get the logic OR value of the fault inputs |

### KBI API List

|  |  |
| --- | --- |
| void | [KBI\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga106b515b62004796ef9a2ad8f54bdb2e) (KBI\_Type \*pKBI, [KBI\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__configstruct.html#struct_k_b_i___config_type) \*pConfig) |
|  | initialize KBI module. |
|  | |
| void | [KBI\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga181b664d14bb7a1476356fa237047813) (KBI\_Type \*pKBI, KBI\_CallbackType pfnCallback) |
|  | set up KBI callback routine. |
|  | |
| void | [KBI\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga29bac6f5be71495c5357f78bfb5aa59d) (KBI\_Type \*pKBI) |
|  | deinit the kbi module. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_DetectFallingEdge](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga5b9d87eda4c2d322e17c5aebdc9bf667) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | set detect falling edge only. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_DetectRisingEdge](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga4f513ff4c88a182cb46e5f9e6bfb75d8) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | set detect falling edge only. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_DetectRisingEdgeHighLevel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga10b959255f4646e10f652b5c87ad558f) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | set detect falling edge only. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_DetectFallingEdgeLowLevel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga85d366ca8c755676761516a6e611d2ce) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | set detect falling edge only. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga9d37bba3ba1c937943921290746695a5) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | enable the pin specified. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga25b32171bb6e1682bdb25b9dec448891) (KBI\_Type \*pKBI, uint32\_t PinMasks) |
|  | disable the pin specified. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_EnableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#gafb363ec261866aedb7642be0ef20ac6f) (KBI\_Type \*pKBI) |
|  | enable the corresponding interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_DisableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#gaab40c01acc4a4490f319bb1e61475af2) (KBI\_Type \*pKBI) |
|  | disable the corresponding interrupt. |
|  | |
| \_\_STATIC\_INLINE uint32\_t | [KBI\_GetFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#gaa9bc59347852c1207210b9dbb96f4ea3) (KBI\_Type \*pKBI) |
|  | Get the corresponding status flag bits. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_ClrFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga790ca195a16ce315dde857eb506000d5) (KBI\_Type \*pKBI) |
|  | clear the corresponding status flag bits. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_SPEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#gaf7e8a745526fcedc9156aa4ce4ac5910) (KBI\_Type \*pKBI) |
|  | Real KBI\_SP register enable. |
|  | |
| \_\_STATIC\_INLINE uint32\_t | [KBI\_GetSP](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#ga34e54ade0a7ef5fc94a9f08b27af2603) (KBI\_Type \*pKBI) |
|  | Get KBI source pin register fields. |
|  | |
| \_\_STATIC\_INLINE void | [KBI\_RstSP](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__kbi__api__list.html#gaa86ef84f7e346321ed7a1a3a8a088e42) (KBI\_Type \*pKBI) |
|  | Reset KBI\_SP register. |

### PMC API List

|  |  |
| --- | --- |
| void | [PMC\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga97eb3b3caa8701cca68e86457a339a94) (PMC\_Type \*pPMC, [PMC\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__configstruct.html#struct_p_m_c___config_type) \*pPMC\_Config) |
|  | configure PMC with given parameters. |
|  | |
| void | [PMC\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga4251ec275bc4ca8c595f93158b94162d) (PMC\_Type \*pPMC) |
|  | config the pmc register to the default mode. |
|  | |
| void | [PMC\_SetMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga2209a3db0848a12c5ea7edec725a9ff5) (PMC\_Type \*pPMC, uint8\_t u8PmcMode) |
|  | config the pmc mode among run, wait and stop modes. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_EnableLVDInStopMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga0e5ed599a48aea16b31c276138b255ce) (PMC\_Type \*pPMC) |
|  | enable LVD events during stop mode. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_DisableLVDInStopMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#gac259099070598b4c0b01bf4bec4fd739) (PMC\_Type \*pPMC) |
|  | disable LVD events during stop mode. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_EnableLVDRst](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga62c656191f263a8f7666e6cbe9c48a9b) (PMC\_Type \*pPMC) |
|  | enable LVD events to generate a hardware reset, note: write once. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_DisableLVDRst](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#gacaefb259d0177ee92cdc160d203ac2a5) (PMC\_Type \*pPMC) |
|  | disable LVD events to generate a hardware reset, note: write once. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_EnableLVD](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga6c799b764b8f0a530f795bd5ad45a2ce) (PMC\_Type \*pPMC) |
|  | enable low-voltage detect logic, note: write once. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_DisableLVD](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#gaf502f44f4ba66f302125433ead7bd21e) (PMC\_Type \*pPMC) |
|  | disable low-voltage detect logic, note: write once |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_SetLVDTripVolt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga2f5b3f6fb22c9d15e426185ed42e3f77) (PMC\_Type \*pPMC, uint8\_t Trippoint) |
|  | set the low-voltage detect trip point voltage, note: write once. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_SetLVWTripVolt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga221729559fe64c1740aff6b6394bbd3a) (PMC\_Type \*pPMC, uint8\_t Trippoint) |
|  | set the low-voltage warning (LVW) trip point voltage. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_EnableLVWInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga75e186ae6b9fb09a650000870427fa6e) (PMC\_Type \*pPMC) |
|  | Enable hardware interrupt requests for LVWF. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_DisableLVWInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#gafe7691fc7cb20117ca70e390c505e1f4) (PMC\_Type \*pPMC) |
|  | Disable hardware interrupt requests for LVWF. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [PMC\_GetLVWFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga11a0c50f5b79d6c5fea5c8d8fff4b18f) (PMC\_Type \*pPMC) |
|  | get the lvw warning flag. |
|  | |
| \_\_STATIC\_INLINE void | [PMC\_ClrLVWFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pmc__api__list.html#ga451a02a1a1a3b4d6a5682f71879b4ed6) (PMC\_Type \*pPMC) |
|  | clear the lvw warning flag. |

### ACMP API Specification

|  |  |
| --- | --- |
| void | [ACMP\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gabb01ae5fa62bfbb2870ac023f1700f70) (ACMP\_Type \*pACMPx, [ACMP\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__configstruct.html#struct_a_c_m_p___config_type) \*pConfig) |
|  | initialize ACMP as per control field. |
|  | |
| void | [ACMP\_ConfigDAC](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gab0d2cdb2a7762668ca8711385436cc21) (ACMP\_Type \*pACMPx, [ACMP\_DACType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__dacctrlstruct.html#union_a_c_m_p___d_a_c_type) \*pDACConfig) |
|  | write ACMP register bits. |
|  | |
| void | [ACMP\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga93487c17b3253e6b91f5770e14ef5fdd) (ACMP\_Type \*pACMPx) |
|  | deinit ACMP module. |
|  | |
| void | [ACMP\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga6539a923c73dbb69f02d9ec3242edf62) (ACMP\_Type \*pACMPx, ACMP\_CallbackPtr pfnCallback) |
|  | set up ACMP callback routines to be called by interrupt service routine. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga2515272bf2fd6d55262a9db13f5554ec) (ACMP\_Type \*pACMPx) |
|  | enable the acmp module. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga651232de7939571eeae1799626228d0e) (ACMP\_Type \*pACMPx) |
|  | disable the acmp module. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_SelectIntMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gaad58d0f67e5187f88b81f0373e9c48f0) (ACMP\_Type \*pACMPx, uint8\_t u8EdgeSelect) |
|  | select sensitivity modes of the interrupt trigger. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_EnablePinOut](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga0b02fd681f3eea2b8846e4f5772c7bfd) (ACMP\_Type \*pACMPx) |
|  | enable the ACMP module analog comparator output. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DisablePinOut](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga1bd35eda12ea1da00ee11c59d367df0d) (ACMP\_Type \*pACMPx) |
|  | disable the ACMP module analog comparator output. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_SelectHyst](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga7ac00848d2fbf3b6d3d6d389aaf0dc76) (ACMP\_Type \*pACMPx, uint8\_t u8HystSelect) |
|  | select ACMP hystersis. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_EnableInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga4b6d27507b9462bac4d1bbc3f4029b4f) (ACMP\_Type \*pACMPx) |
|  | enable the acmp module interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DisableInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga0589eff4ee4a3280fff6928a2c1e0d25) (ACMP\_Type \*pACMPx) |
|  | disable the acmp module interrupt. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [ACMP\_GetFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga665ed751a29de768cc52cd2a8fcb1617) (ACMP\_Type \*pACMPx) |
|  | get the interrupt flag bit. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_ClrFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gaa242c67e556f58bcfa7f5cbb0aed1e7e) (ACMP\_Type \*pACMPx) |
|  | clear the interrupt flag bit. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_PositiveInputSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga2d6b3b079ff2df3ae71414e35c8c993b) (ACMP\_Type \*pACMPx, uint8\_t u8PosPinSel) |
|  | ACMP Positive Input Select. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_NegativeInputSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gad2f52ba8c0aa0893138694a31f2d2410) (ACMP\_Type \*pACMPx, uint8\_t u8NegPinSel) |
|  | ACMP Negative Input Select. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DacEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gaf529b8ba00007144ac78db75e782e785) (ACMP\_Type \*pACMPx) |
|  | Enable 6 bit DAC in ACMP module. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DacDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga47cb6f55d09305aff26c436741ab029b) (ACMP\_Type \*pACMPx) |
|  | Disable 6 bit DAC in ACMP module. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DacReferenceSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gaa7d8539121807a17886fb2efe44adc9b) (ACMP\_Type \*pACMPx, uint8\_t u8RefSelect) |
|  | ACMP 6 bit DAC Reference Select. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_DacOutputSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gab180150650b75b305e4efc3c950f61b5) (ACMP\_Type \*pACMPx, uint8\_t u8DacValue) |
|  | ACMP 6 bit DAC Output Value Set. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_InputPinEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#ga2f6c3e3853580e122af685fef9355d8c) (ACMP\_Type \*pACMPx, uint8\_t u8InputPin) |
|  | Enable ACMP input pin. |
|  | |
| \_\_STATIC\_INLINE void | [ACMP\_InputPinDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__acmp__api__list.html#gaccb7d9ff134a5aed665444ec78a5fa57) (ACMP\_Type \*pACMPx, uint8\_t u8InputPin) |
|  | Disable ACMP input pin. |

### ADC API Specification

|  |  |
| --- | --- |
| void | [ADC\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga1448b87c741454166aedbd5c481ecfd1) (ADC\_Type \*pADC, [ADC\_ConfigTypePtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__config__type.html#struct_a_d_c___config_type) pADC\_Config) |
|  | initialize ADC module. |
|  | |
| void | [ADC\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga4a4afab4d246cd821ed8c9abab51ba65) (ADC\_Type \*pADC) |
|  | disable ADC module. |
|  | |
| unsigned int | [ADC\_PollRead](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga24870c0acbc56869c33fcc46a0aec125) (ADC\_Type \*pADC, uint8\_t u8Channel) |
|  | start a conversion and get conversion result |
|  | |
| void | [ADC\_SetCallBack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga3b9e02d4309536fb75ff240056cfddb3) ([ADC\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__callback.html#ga575972aa3c3272e900ff94f7dc7d083a) pADC\_CallBack) |
|  | install ADC call back function. |
|  | |
| void | [ADC\_SetChannel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gaed7d00e90358b178a144c9b80ebb2afb) (ADC\_Type \*pADC, uint8\_t u8Channel) |
|  | set ADC channel. |
|  | |
| void | [ADC\_VrefSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga6cabc23faf5a2eca604ea188f685be12) (ADC\_Type \*pADC, uint8\_t u8Vref) |
|  | Voltage Reference Selection. |
|  | |
| void | [ADC\_SelectClockDivide](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga6ef693dc577df90157c002339dd85912) (ADC\_Type \*pADC, uint8\_t u8Div) |
|  | select clock divide |
|  | |
| void | [ADC\_SetMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gaade96370f8127031dd1524c2144b6fdf) (ADC\_Type \*pADC, uint8\_t u8Mode) |
|  | set ADC mode. |
|  | |
| void | [ADC\_SelectClock](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga6fc21daf83ea83e78313eb6aa7c450f7) (ADC\_Type \*pADC, uint8\_t u8Clock) |
|  | Input Clock Select. |
|  | |
| void | [ADC\_SetFifoLevel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga21326cd27d0eb909b8ea53471d0fc101) (ADC\_Type \*pADC, uint8\_t u8FifoLevel) |
|  | FIFO Depth enables. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_IntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga960d056e3289579be14e45d250646c57) (ADC\_Type \*pADC) |
|  | enable ADC interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_IntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga74ad95f15a46b02de43394a9e03b6fc0) (ADC\_Type \*pADC) |
|  | disable ADC interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_ContinuousConversion](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga2e21ff2ebbd89e4224f93d6795099316) (ADC\_Type \*pADC) |
|  | enable ADC continuous conversion. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SingleConversion](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gab8ccebfbf8a666d23c99dc07a7ff30ac) (ADC\_Type \*pADC) |
|  | enable ADC single conversion |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetHardwareTrigger](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga6f104bcd51fdb65889a0cf78c751f004) (ADC\_Type \*pADC) |
|  | set the ADC to hardware trigger. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetSoftwareTrigger](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga1d9104b61a76c94225e68d951822ca7c) (ADC\_Type \*pADC) |
|  | set the ADC to software trigger. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga1da24e904bec8f277d2b009e7a383d45) (ADC\_Type \*pADC) |
|  | enable ADC compare function. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga473920ff266e90eb6908a571f085d9f4) (ADC\_Type \*pADC) |
|  | disable ADC compare function. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareGreaterFunction](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga87990fd655af1a1fa698ee8c60d5d4e8) (ADC\_Type \*pADC) |
|  | enable ADC compare greater function. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareLessFunction](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gabb9d945e7c08e2dcc61263a49c5a6924) (ADC\_Type \*pADC) |
|  | enable ADC compare less function. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetLowPower](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga162587494aa234b45a940ad8ef6677e6) (ADC\_Type \*pADC) |
|  | set ADC to low power configuration. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetHighSpeed](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gad1b4aadbd5e32e13ff717ef267b05ea3) (ADC\_Type \*pADC) |
|  | set ADC to high speed configuration. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetLongSample](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga8abff9a2d96e09c14f63091fcbb790e5) (ADC\_Type \*pADC) |
|  | Long Sample Time Configuration. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetShortSample](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gad371fa3bda57884f3eb98f0a581c83b1) (ADC\_Type \*pADC) |
|  | Short Sample Time Configuration. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_FifoScanModeEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga23e0b8df75111428a6cf12d0c734b247) (ADC\_Type \*pADC) |
|  | FIFO scan mode enable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_FifoScanModeDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga8de9556d517ff7af01f5103d64ba687f) (ADC\_Type \*pADC) |
|  | FIFO scan mode disable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareFifoOr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga37526d7c36b574902c4603b107f41425) (ADC\_Type \*pADC) |
|  | OR all of compare trigger. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_CompareFifoAnd](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gaf0b3046d3e8a9ddbc768a4211795a3fb) (ADC\_Type \*pADC) |
|  | And all of compare trigger. |
|  | |
| \_\_STATIC\_INLINE uint16\_t | [ADC\_ReadResultReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga9936232e0aed44173502ae033c287527) (ADC\_Type \*pADC) |
|  | read ADC result register. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_SetCompareValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga8c609bb00092f17a2e979242c65c14b0) (ADC\_Type \*pADC, uint16\_t u16Compare) |
|  | set ADC compare value. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_PinControlEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gaeca3839137ef81497cde7716b54c2ec1) (ADC\_Type \*pADC, uint16\_t u16PinNumber) |
|  | ADC pin control enable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_PinControlDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga2dc57ebfc527035448eb58cc3c2cfc5d) (ADC\_Type \*pADC, uint16\_t u16PinNumber) |
|  | ADC pin control disable. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [ADC\_IsConversionActiveFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga7d0b69aa4e6a750698c3a5034ecd84a1) (ADC\_Type \*pADC) |
|  | check conversion active status |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [ADC\_IsCOCOFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gabfa0138b6eb36cefeababcc0fd5aaf02) (ADC\_Type \*pADC) |
|  | check COCO flag |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [ADC\_IsFIFOEmptyFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga160ab0572790a822c4336b985de21793) (ADC\_Type \*pADC) |
|  | check Result FIFO empty |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [ADC\_IsFIFOFullFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga560af801467cd5e9d24e4f57a2f6298f) (ADC\_Type \*pADC) |
|  | check Result FIFO full |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerMultiple](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gacd490a026a292a791d188ad4492f0ef3) (ADC\_Type \*pADC) |
|  | Hardware Trigger Multiple Conversion Enable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerSingle](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga5b1c68ef08aa7f18481e7e62a6358f22) (ADC\_Type \*pADC) |
|  | Hardware Trigger Single Conversion. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerMaskEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gac84f16052b4e12b48054a400d4987148) (ADC\_Type \*pADC) |
|  | Hardware Trigger Mask Enable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerMaskDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#ga9d9cc03e6146bbdd27c534d00cf7ded3) (ADC\_Type \*pADC) |
|  | Hardware Trigger Mask Disable. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerMaskAuto](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gaa1739468587a18336ee8fa3190c994fb) (ADC\_Type \*pADC) |
|  | Hardware Trigger Mask Mode Select Automatic Mode. |
|  | |
| \_\_STATIC\_INLINE void | [ADC\_HardwareTriggerMaskNonAuto](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__adc__api__list.html#gac772f890f9767ea23d72f243c38f2ecc) (ADC\_Type \*pADC) |
|  | Hardware Trigger Mask Mode Select to be with HTRGMASKE. |

### NVM API Specification

|  |  |
| --- | --- |
| uint16\_t | [FLASH\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gac21cc6c3eed88d8a179ec956131d9710) (uint32\_t u32BusClock) |
|  | initialize flash driver. |
|  | |
| uint16\_t | [FLASH\_Program](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga9115c40a21ee10b1bdef7af465236980) (uint32\_t u32NVMTargetAddress, uint8\_t \*pData, uint16\_t u16SizeBytes) |
|  | program flash routine. |
|  | |
| uint16\_t | [FLASH\_Program1LongWord](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gac173dba2693be4494f626968ebb3848d) (uint32\_t u32NVMTargetAddress, uint32\_t u32DwData) |
|  | program flash routine, program 1 long word to flash. |
|  | |
| uint16\_t | [FLASH\_Program2LongWords](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gac7c2297f61446620fccfbca384d123ac) (uint32\_t u32NVMTargetAddress, uint32\_t u32DwData0, uint32\_t u32DwData1) |
|  | program flash routine, program 2long word to flash. |
|  | |
| uint16\_t | [FLASH\_EraseSector](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gaafb86cc620be9077606ec7c91d91543d) (uint32\_t u32NVMTargetAddress) |
|  | erase flash sector, each flash sector is of 512 bytes long. |
|  | |
| uint16\_t | [FLASH\_Unsecure](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gae371e0a9e4544ef8e34641ca90d2448b) (void) |
|  | unsecure device with unsecure command. |
|  | |
| uint16\_t | [FLASH\_EraseBlock](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga264d8496181f017e235424100184157b) (uint32\_t u32NVMTargetAddress, uint8\_t bIsEEPROM) |
|  | erase flash/EEPROM block. |
|  | |
| uint16\_t | [FLASH\_EraseVerifyBlock](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga7b8a2aa2bdd088ce5d1946cfee41b380) (uint32\_t u32NVMTargetAddress, uint8\_t bIsEEPROM) |
|  | erase verify flash/EEPROM block. |
|  | |
| uint16\_t | [FLASH\_EraseVerifySection](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gacf3c1ec4e07091ba40d6bc7991b169e0) (uint32\_t u32NVMTargetAddress, uint16\_t u16LongWordCount) |
|  | erase verify flash section. |
|  | |
| uint16\_t | [FLASH\_EraseVerifyAll](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga4645c09e2de6f4aaa3e6ef8ccce3d2d8) (void) |
|  | erase verify all flash and EEPROM blocks. |
|  | |
| uint16\_t | [FLASH\_EraseAll](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga93cdd13a2c76089024f5de633f7e9d8c) (void) |
|  | erase all flash and EEPROM blocks. |
|  | |
| uint16\_t | [FLASH\_ProgramOnce](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga045683d1cd648e8aca7119a1d868cf7a) (uint8\_t u8PhraseIndex, uint8\_t \*pData8Bytes) |
|  | program once command, write data buff to IFR. |
|  | |
| uint16\_t | [FLASH\_ReadOnce](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga8d78bc1effa1f434a13ba8b3a87c7b3b) (uint8\_t u8PhraseIndex, uint8\_t \*pData8Bytes) |
|  | read data from IFR address. |
|  | |
| uint16\_t | [FLASH\_EraseBlock\_NoWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gacf71bbd36dd425ffcace44a373e4db5f) (uint32\_t u32NVMTargetAddress, uint8\_t bIsEEPROM) |
|  | erase flash/Flash block without wait. |
|  | |
| uint16\_t | [FLASH\_VerifyBackdoorKey](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gaa47891b7a310538730ced347e5fc6fa1) (uint8\_t \*pKey) |
|  | verify backdoor key. NOTE: this routine must be running from RAM or EEPROM. |
|  | |
| uint16\_t | [FLASH\_SetUserMarginLevel](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga535b899b91683ee9a7c3b22a6a2584c3) (uint32\_t u32NVMTargetAddress, uint16\_t u16MarginLevel, uint8\_t bIsEEPROM) |
|  | set user margin level. |
|  | |
| uint16\_t | [FLASH\_CheckErrStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga47403f93f6879f555225748c6c8568bd) (void) |
|  | check flash error status. |
|  | |
| void | [FLASH\_LaunchCMD](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#ga2f81c69fbc7b183432b9fd5b38be81de) (uint8\_t bWaitComplete) |
|  | launch NVM command. if bWaitComplete is true, then wait till command is completed. It should be placed in RAM for some commands like verify backdoor key if enabling flash controller bit is cleared. Otherwise, it can be executed in flash. |
|  | |
| \_\_STATIC\_INLINE void | [FLASH\_IntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gae053361f2edba284ebb320bda6b0f6b8) (void) |
|  | Command Complete Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [FLASH\_IntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__nvm__api__list.html#gae1fa0bbb363972cf1b77efe1361f5a84) (void) |
|  | Command Complete Interrupt Disable. |

### I2C API Specification

|  |  |
| --- | --- |
| void | [I2C\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gab7bf14ec0e5177ba55bf9b616a02486e) (I2C\_Type \*pI2Cx, [I2C\_ConfigPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__config__type.html#struct_i2_c___config_type) pI2CConfig) |
|  | Initialize I2C module. |
|  | |
| uint8\_t | [I2C\_Start](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga0e724b1af66b4379ac71a359c3e18239) (I2C\_Type \*pI2Cx) |
|  | send out start signals. |
|  | |
| uint8\_t | [I2C\_Stop](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gadd641c2c3667fd2ae66e87c86927dba4) (I2C\_Type \*pI2Cx) |
|  | send out stop signals. |
|  | |
| uint8\_t | [I2C\_RepeatStart](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gaba64d54c3b740559906af4d921868651) (I2C\_Type \*pI2Cx) |
|  | send out repeat start signals. |
|  | |
| void | [I2C\_SetSlaveAddress](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga7ca7d19d055fc1ae102838ee647aa3b3) (I2C\_Type \*pI2Cx, uint16\_t u16SlaveAddress) |
|  | set slave address. |
|  | |
| void | [I2C\_IntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gaea8eb06eee5b8458ffef249d6d0c9a09) (I2C\_Type \*pI2Cx) |
|  | disable IICIF interrupt. |
|  | |
| void | [I2C\_IntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga70b6db691cf28f65369a2ce5858be345) (I2C\_Type \*pI2Cx) |
|  | enable IICIF interrupt. |
|  | |
| void | [I2C\_SetSCLLowTimeout](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga678311c76e9ff8c0e3e1dca210a2e217) (I2C\_Type \*pI2Cx, uint16\_t u16Timeout) |
|  | SCL low timeout value that determines the timeout period of SCL low. |
|  | |
| void | [I2C\_Deinit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga8a49b736aa84617e9ab3d66f374fd405) (I2C\_Type \*pI2Cx) |
|  | deinit I2C module. |
|  | |
| uint8\_t | [I2C\_WriteOneByte](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gaec4d72bbea9d3a23f079c0f794665885) (I2C\_Type \*pI2Cx, uint8\_t u8WrBuff) |
|  | write a byte to I2C module. |
|  | |
| uint8\_t | [I2C\_ReadOneByte](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga7c968470c638f044d34d8c615cee1419) (I2C\_Type \*pI2Cx, uint8\_t \*pRdBuff, uint8\_t u8Ack) |
|  | read a byte from slave I2C. |
|  | |
| uint8\_t | [I2C\_MasterSendWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gac817c3a1a89e73dd0545fabe04e5701a) (I2C\_Type \*pI2Cx, uint16\_t u16SlaveAddress, uint8\_t \*pWrBuff, uint32\_t u32Length) |
|  | send data to I2C, and wait to complete transfering. |
|  | |
| uint8\_t | [I2C\_MasterReadWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga55e639eb604b070a4abb0dcab83e3bff) (I2C\_Type \*pI2Cx, uint16\_t u16SlaveAddress, uint8\_t \*pRdBuff, uint32\_t u32Length) |
|  | read data from I2C,and wait to complete transferring. |
|  | |
| void | [I2C1\_SetCallBack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga88f0eaccfa5e760fbb5a62cd8be7ab9c) ([I2C\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__callback.html#ga1c74e06c7d833dba25a2dd1fdf6219fe) pCallBack) |
|  | set call back function for I2C1 module. |
|  | |
| void | [I2C0\_SetCallBack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga2cec969582ad739bd968f25464c3cba9) ([I2C\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__callback.html#ga1c74e06c7d833dba25a2dd1fdf6219fe) pCallBack) |
|  | set call back function for I2C0 module. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_TxEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gac622a653b38d9a2247c7a3c255d7190c) (I2C\_Type \*pI2Cx) |
|  | enable I2C to transmit data. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_RxEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga3beaf3464ee811af6538e7aaabec37d4) (I2C\_Type \*pI2Cx) |
|  | enable I2C to receive data. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SetBaudRate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga99fec427c96c020e8f1e1ee67638656c) (I2C\_Type \*pI2Cx, uint32\_t u32Bps) |
|  | set IIC band rate. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_GeneralCallEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gae42750d07385ccba21ee08f69f4a21cc) (I2C\_Type \*pI2Cx) |
|  | enable general call. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SMBusAlertEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gafcfd6a01c0a8a14c32c47addf775bcfb) (I2C\_Type \*pI2Cx) |
|  | SMBus Alert Response Address Enable. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_RangeAddressEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga31f8049ee24118040140f0a42a21b761) (I2C\_Type \*pI2Cx) |
|  | Range Address Matching Enable. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SHTF2IntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gaa558bd0f00a194063a53a984b4c3c104) (I2C\_Type \*pI2Cx) |
|  | SHTF2 Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_TimeoutCounterClockSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gacc7e99b5221d867213fc0e902dde2dfc) (I2C\_Type \*pI2Cx, uint8\_t u8Clock) |
|  | Timeout Counter Clock Select. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_GetStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gacda9405d30b4c0e3490ae87009759abe) (I2C\_Type \*pI2Cx) |
|  | get I2C status. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_ClearStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga96372a9fce4d5ab52f295bc3419aba30) (I2C\_Type \*pI2Cx, uint8\_t u8ClearFlag) |
|  | clear specified status. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_WriteDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga08f5550932c9cb2ff48bf1bffbf5ab21) (I2C\_Type \*pI2Cx, uint8\_t u8DataBuff) |
|  | write data to data register. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_ReadDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga93d39e33b4994bb2223a8b437ba5452e) (I2C\_Type \*pI2Cx) |
|  | read data from data register. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsTxMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga5c8c3da4f808194012699aee7aa2b7e6) (I2C\_Type \*pI2Cx) |
|  | check if is Tx mode. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsBusy](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga80891bf40f774d61bacafae3821416e8) (I2C\_Type \*pI2Cx) |
|  | check I2C if busy. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsReceivedAck](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gad47512bf30ef05bab1a6eaf1dc8773e9) (I2C\_Type \*pI2Cx) |
|  | check I2C receive ack or nack. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsMasterMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gabac74cf7dcca73ca1b0882696717200b) (I2C\_Type \*pI2Cx) |
|  | check I2C if is master mode. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsSMB\_SLTF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga98992168d083dbc013a2ebb91e5a54e1) (I2C\_Type \*pI2Cx) |
|  | check SCL Low Timeout Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [I2C\_IsSMB\_SHTF2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gaa714505521034757c4f2c4d2ff524031) (I2C\_Type \*pI2Cx) |
|  | check SCL High Timeout Flag is set or clear. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_ClearSLTF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga7bca069c9fea75b16f952d3a5c5a3997) (I2C\_Type \*pI2Cx) |
|  | clear SLTF flag. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_ClearSHTF2](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga8ee42c33c02cc13814f14d2e35ba4c25) (I2C\_Type \*pI2Cx) |
|  | clear SHTF2 flag. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SendAck](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga3cdfb22156cccf5083f64c179f636edb) (I2C\_Type \*pI2Cx) |
|  | send out ACK. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SendNack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#ga216cd0fe2db56c9c3bd72a6f49fd1f01) (I2C\_Type \*pI2Cx) |
|  | send out NACK. |
|  | |
| \_\_STATIC\_INLINE void | [I2C\_SecondAddressEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__i2c__api__list.html#gab3fee989171954b145b5295fa58bbce5) (I2C\_Type \*pI2Cx) |
|  | Second I2C Address Enable. |

### SPI API Specification

|  |  |
| --- | --- |
| void | [SPI\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga6ff85bea31ca5a3f131404b3fcc751fa) (SPI\_Type \*pSPI, [SPI\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__config__type.html#struct_s_p_i___config_type) \*pConfig) |
|  | initialize SPI as per params. |
|  | |
| void | [SPI\_SetBaudRate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga49c3ba68702af48c04858a72e133646b) (SPI\_Type \*pSPI, uint32\_t u32BusClock, uint32\_t u32Bps) |
|  | SPI set band rate. |
|  | |
| ResultType | [SPI\_TransferWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga34940049b1132a600ef39f62fb2085f1) (SPI\_Type \*pSPI, SPI\_WidthType \*pRdBuff, SPI\_WidthType \*pWrBuff, uint32 uiLength) |
|  | implement write data to SPI. |
|  | |
| void | [SPI\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga0096c8da4f239ce000ded1b37afedf33) (SPI\_Type \*pSPI) |
|  | Deinitialize SPI to the default state (reset value). |
|  | |
| void | [SPI\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gabcc3529f5dd1dc4d5080d4f52cd02015) (SPI\_Type \*pSPI, [SPI\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__callback.html#ga995f243c4f8e138fa8447f9e35e76a72) pfnCallback) |
|  | set up SPI callback routines to be called by interrupt service routine. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetLSBFirst](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gaa0874b8a4da816f053cfffd70508ffd2) (SPI\_Type \*pSPI) |
|  | LSB first (shifter direction). |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetMSBFirst](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga5233c8bfdee16e4b365246976e640843) (SPI\_Type \*pSPI) |
|  | MSB first (shifter direction). |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetClockPol](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga8b1c9653968918c36de35fe82df3a1f9) (SPI\_Type \*pSPI, uint8\_t u8PolLow) |
|  | set SPI clock polarity. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetClockPhase](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga0cceba1b3a4771f00d29ddd4682237dc) (SPI\_Type \*pSPI, uint8\_t u8Phase) |
|  | set SPI clock phase. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga72e38c365a2ed10b5cd58bf504592a64) (SPI\_Type \*pSPI) |
|  | enable SPI module. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga98e89c87c091b273342c13864a86a1d2) (SPI\_Type \*pSPI) |
|  | disable SPI module. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_IntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gaf16c913a45445d3c2e64af5990dbdbc9) (SPI\_Type \*pSPI) |
|  | enable SPI interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_IntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga8ccad39e261036501301166734a677fd) (SPI\_Type \*pSPI) |
|  | disable SPI interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetMasterMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga60b07785ae6d08950ff3e26c6c8b9509) (SPI\_Type \*pSPI) |
|  | set SPI to master mode. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SetSlaveMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gace28a02154045b59c3517678537bff51) (SPI\_Type \*pSPI) |
|  | set SPI to slave mode. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_TxIntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga5b0cd11076263352da06b0905855a8b5) (SPI\_Type \*pSPI) |
|  | SPI transmit interrupt enable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_TxIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gac2da5a3d9d51af77e34d605883a8cebe) (SPI\_Type \*pSPI) |
|  | SPI transmit interrupt disable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SSOutputEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga1416c483869a48cc6ad7bf766b145703) (SPI\_Type \*pSPI) |
|  | Slave select output enable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_SSOutputDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gaba878b314cd41b5165b6a7b83e1bcfac) (SPI\_Type \*pSPI) |
|  | Slave select output disable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_MatchIntEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gac46c320642a7c2e9eb81f86ef974856c) (SPI\_Type \*pSPI) |
|  | SPI match interrupt enable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_MatchIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gab2e8350d24f17b0e95650298ce312c19) (SPI\_Type \*pSPI) |
|  | SPI match interrupt disable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_ModfDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gade626e6106105fc0bd6e090a5cf24d48) (SPI\_Type \*pSPI) |
|  | Master mode-fault function disable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_ModfEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga8db995841ad1ddb91b793b05296a7eac) (SPI\_Type \*pSPI) |
|  | Master mode-fault function enable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_BidirOutEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga3ee90a5fd1a2e10b88ddb1e857ee4038) (SPI\_Type \*pSPI) |
|  | Bidirectional mode output enable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_BidirOutDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga174e3183d17ecc32e4f51b1a38c4654a) (SPI\_Type \*pSPI) |
|  | Bidirectional mode output disable. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_ClockStopDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga71cb089c0602ae197686afcb65d85845) (SPI\_Type \*pSPI) |
|  | SPI stop in wait mode. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_ClockStopEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga90b7ffc8a266b21817090021a8b5490a) (SPI\_Type \*pSPI) |
|  | SPI stop in wait mode. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_BidirPinEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga8432c0df13b96e9266a8fc81cca095e7) (SPI\_Type \*pSPI) |
|  | enables bidirectional pin configurations. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_BidirPinDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga61c76892d839db8062a76d2ab5eddd5b) (SPI\_Type \*pSPI) |
|  | enables bidirectional pin configurations. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [SPI\_IsSPRF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga1fe1004c7e9558fa59ab801d4cd2aa9a) (SPI\_Type \*pSPI) |
|  | check SPI read buffer full flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [SPI\_IsSPMF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga46fcac0920f3eba5debe1590634fae04) (SPI\_Type \*pSPI) |
|  | check SPI match flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [SPI\_IsSPTEF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga54fcf5bd72a87cdedf1c869e16cab166) (SPI\_Type \*pSPI) |
|  | check SPI transmit buffer empty flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [SPI\_IsMODF](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gafa3dc95ff316510eaec77508d80c282c) (SPI\_Type \*pSPI) |
|  | check master mode fault flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [SPI\_ReadDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#ga0570f247f76570ba927d27b1e4c9f018) (SPI\_Type \*pSPI) |
|  | read SPI data register. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_WriteDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gabe0d266d9520a7ca849ddc3e1d1cf4de) (SPI\_Type \*pSPI, uint8\_t u8WrBuff) |
|  | write SPI data register. |
|  | |
| \_\_STATIC\_INLINE void | [SPI\_WriteMatchValue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__spi__api__list.html#gac982d830b5b9ab42090fa185a23b6b93) (SPI\_Type \*pSPI, uint8\_t u8WrBuff) |
|  | write SPI match register. |

### PIT API Specification

|  |  |
| --- | --- |
| void | [PIT\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gafdd140a281a1c8545a177b173dd52c62) (uint8\_t u8Channel\_No, [PIT\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\struct_p_i_t___config_type.html) \*pConfig) |
|  | initialize pit module. |
|  | |
| void | [PIT\_SetLoadVal](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga338a0d45fd203f5bd2398cb60ef0ebfd) (uint8\_t u8Channel, uint32\_t u32loadvalue) |
|  | initialize pit module. |
|  | |
| void | [PIT\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gab06d66c616a76895be1f599b33844ae7) (uint8\_t u8Channel\_No, [PIT\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__callback.html#ga0142f548857e94955fb00c96bbcdd01f) pfnCallback) |
|  | pit module set call back. |
|  | |
| void | [PIT\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gae9b3a2ac77509bf2396339c313efc742) (void) |
|  | pit module de-initialize, reset pit register |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga2acf5257c1b2ca5705fdb52511fe1b36) (void) |
|  | enable pit module. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga6a7753b5b326d8deea3ae360fbcf5fdd) (void) |
|  | disable pit module. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_SetDebugFreeze](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gae83bd03475604aa3f17b87853a3ea1ee) (void) |
|  | pit timers are stopped in debug mode. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_SetDebugOn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga3f0dfd21fc4aeae89aacfcb884b4e45a) (void) |
|  | pit timers are running in debug mode. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelEnable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gac398d9ca6385221e8c3844a50300f1db) (uint8\_t u8Channel) |
|  | enable pit channel timer. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga20f9dc79103f2acb12fa257cecb7e8e9) (uint8\_t u8Channel) |
|  | disable pit channel timer. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelEnableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gae6817fefbd51573851394911350fe2aa) (uint8\_t u8Channel) |
|  | enable pit channel timer interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelDisableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga9b804c8d336acb6722a40b44954d301b) (uint8\_t u8Channel) |
|  | disable pit channel timer interrupt . |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelEnableChain](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gacdb484aee37a69d80224bf9107a78ea9) (uint8\_t u8Channel) |
|  | enable pit channel timer chain mode. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelDisableChain](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#gab061b4873ef82ba91d11650c4ec22a1a) (uint8\_t u8Channel) |
|  | disable pit channel timer chain mode. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [PIT\_ChannelGetFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga9d51f886db81cf28da4a5aa89ff3e9d2) (uint8\_t u8Channel) |
|  | get pit channel timer interrrupt flag. |
|  | |
| \_\_STATIC\_INLINE void | [PIT\_ChannelClrFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pit__api__list.html#ga78ccb6afb578ac72b5445eed8a0a5291) (uint8\_t u8Channel) |
|  | clear pit channel timer interrrupt flag. |

### RTC API Specification

|  |  |
| --- | --- |
| void | [RTC\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#ga1fc859c538eb17e48c1f8bd3d0c98d40) ([RTC\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\struct_r_t_c___config_type.html) \*pConfig) |
|  | inital RTC module |
|  | |
| void | [RTC\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#ga315321925a02e6441c9867e536b54d88) ([RTC\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__callback.html#ga6cab305e0984669f04982c4558e29fff) pfnCallback) |
|  | set call back function for rtc module |
|  | |
| void | [RTC\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#gaff0c9fef0c90de5f25dc02b7ec887d63) (void) |
|  | de-initialize rtc module , reset rtc register |
|  | |
| \_\_STATIC\_INLINE void | [RTC\_EnableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#gadeab7be961d678f83b3413a1de60a38c) (void) |
|  | enable rtc interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [RTC\_DisableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#ga911284d3a75c1e9e02c50b8a49610f1b) (void) |
|  | disable rtc interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [RTC\_SetModulo](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#gaa759dd1305782d27ccfee8b9ddc8e17a) (uint16\_t u16Mod\_Value) |
|  | set rtc modulo value. |
|  | |
| \_\_STATIC\_INLINE void | [RTC\_SetClock](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#ga4e06676f1ba3487e3b89b24aeb4ee731) (uint16\_t u16Clock\_Number, uint16\_t u16Presalcer) |
|  | set rtc clock source and presalcer. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [RTC\_GetFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#ga2d906948e0fd000db75395c71ef497b3) (void) |
|  | get rtc flag bit. |
|  | |
| \_\_STATIC\_INLINE void | [RTC\_ClrFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__rtc__api__list.html#gad6b5d91cb6c03dcc8e260c3fa0182563) (void) |
|  | clear rtc flag bit. |

### Cyclic Redundancy Check API Specification

|  |  |
| --- | --- |
| void | [CRC\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__api__list.html#ga775f83655a061c26f26f173f66d6e694) ([CRC\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__config__type.html#struct_c_r_c___config_type) \*pConfig) |
|  | initialize CRC with poly per control parameters |
|  | |
| uint32\_t | [CRC\_Cal16](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__api__list.html#ga6097d930e96444a6d3fe3923500e89dc) (uint32\_t seed, uint8\_t \*msg, uint32\_t sizeBytes) |
|  | crc module 16-bit mode calculation. |
|  | |
| uint32\_t | [CRC\_Cal32](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__api__list.html#gaba1ad6766386e3216c01716584e14c3f) (uint32\_t seed, uint8\_t \*msg, uint32\_t sizeBytes) |
|  | crc module 32-bit mode calculation. |
|  | |
| void | [CRC\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__crc__api__list.html#ga11f30b83f52bd9eafec8c174244ae07e) (void) |
|  | de-initialize crc module, reset crc register. |

### UART API Specification

|  |  |
| --- | --- |
| void | [UART\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga3b866303e68e454693946326b44b1680) (UART\_Type \*pUART, [UART\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__config__type.html#struct_u_a_r_t___config_type) \*pConfig) |
|  | initialize the UART, interrupts disabled, and no hardware flow-control. |
|  | |
| uint8\_t | [UART\_GetChar](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga85322b57e1677f1a2d936474a5d2d708) (UART\_Type \*pUART) |
|  | receive a character. |
|  | |
| void | [UART\_PutChar](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga9c896c2fe41317b252ca930e35096c2b) (UART\_Type \*pUART, uint8\_t u8Char) |
|  | send a character. |
|  | |
| void | [UART\_SetBaudrate](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga61e1532d0ad6cb2b576131d6ce3cae2f) (UART\_Type \*pUART, [UART\_ConfigBaudrateType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__config__baudrate__type.html#struct_u_a_r_t___config_baudrate_type) \*pConfig) |
|  | set baudrate. |
|  | |
| void | [UART\_EnableInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga109abebe8a702bb6a71724ac5a508337) (UART\_Type \*pUART, [UART\_InterruptType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__interrupt__type__list.html#ga211d043ce35fa7dcd1cffd7f205ccb3c) InterruptType) |
|  | enable interrupt. |
|  | |
| void | [UART\_DisableInterrupt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga2283fe93ce92499d505751a09ca798e5) (UART\_Type \*pUART, [UART\_InterruptType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__interrupt__type__list.html#ga211d043ce35fa7dcd1cffd7f205ccb3c) InterruptType) |
|  | disable interrupt. |
|  | |
| uint16\_t | [UART\_GetFlags](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga19ab62b2122c5ec227cb82de4934bdee) (UART\_Type \*pUART) |
|  | get flags from 2 UART status registers. |
|  | |
| uint8\_t | [UART\_CheckFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gacb9bb2934353ff3b716decccc530d5e1) (UART\_Type \*pUART, [UART\_FlagType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__flag__type__list.html#ga5607766e712786809dd6af012103edd0) FlagType) |
|  | check whether the specified flag is set. |
|  | |
| void | [UART\_SendWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga858e629e33240428998b435c6def7105) (UART\_Type \*pUART, uint8\_t \*pSendBuff, uint32\_t u32Length) |
|  | send a series of charecters using polling mode. |
|  | |
| void | [UART\_ReceiveWait](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga9d8d1f859915b7056e3b4cddfa687345) (UART\_Type \*pUART, uint8\_t \*pReceiveBuff, uint32\_t u32Length) |
|  | receive a series of charecters using polling mode. |
|  | |
| void | [UART\_WaitTxComplete](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gaba71c7772bb3a939e6e997f30753de33) (UART\_Type \*pUART) |
|  | wait tx complete. |
|  | |
| void | [UART\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga1ebca97629515a3d82f068d8d25c946a) (UART\_CallbackType pfnCallback) |
|  | set up UART callback routines to be called by interrupt service routine. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [UART\_ReadDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga18f3c6dfe548bc917d7c130efb5cf42e) (UART\_Type \*pUART) |
|  | read receive buffer |
|  | |
| \_\_STATIC\_INLINE void | [UART\_WriteDataReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga2036af80fda46dac03a6276b3b8cc93f) (UART\_Type \*pUART, uint8\_t u8Char) |
|  | write transmit buffer |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [UART\_CharPresent](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga661f23c83a1cafa069eeefd07bda0599) (UART\_Type \*pUART) |
|  | check if a character has been received |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableTx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga1442b8c25eaaf4c0af150181702d3e08) (UART\_Type \*pUART) |
|  | enable transmit |
|  | |
| \_\_STATIC\_INLINE void | [UART\_DisableTx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gab11ec606e7c61e9c647b741d7c290776) (UART\_Type \*pUART) |
|  | disable transmit |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableRx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gaa0abcfa9df86ad67b2b44b430fd19be6) (UART\_Type \*pUART) |
|  | enable receive |
|  | |
| \_\_STATIC\_INLINE void | [UART\_DisableRx](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gac65267b3434af880135518fae43a213e) (UART\_Type \*pUART) |
|  | disable receive |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableLoopback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga4b8d0fc0d899e6075d8ba903b7a8e22d) (UART\_Type \*pUART) |
|  | Enable loopback mode. |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableSingleWire](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga2341dc0cf05d4763a91f422329f50daa) (UART\_Type \*pUART) |
|  | enable single wire mode |
|  | |
| \_\_STATIC\_INLINE void | [UART\_Set8BitMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga50a4a3a353272e9ddc612cfc7746ef15) (UART\_Type \*pUART) |
|  | set 8-bit mode |
|  | |
| \_\_STATIC\_INLINE void | [UART\_Set9BitMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga71f075bb6a0f0bdb9763c120a70d8ae6) (UART\_Type \*pUART) |
|  | set 9-bit mode |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableTxBuffEmptyInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gab6381f5bb40a895bf9b6aaf0ec642930) (UART\_Type \*pUART) |
|  | enable transmit buffer empty interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableTxCompleteInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga6712f2f05f277da3deb85ba32b198719) (UART\_Type \*pUART) |
|  | enable transmit complete interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_EnableRxBuffFullInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga614a2b758d6b4ab8a481d1c8c4fc2a84) (UART\_Type \*pUART) |
|  | enable receive buffer full interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_DisableTxBuffEmptyInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga509f4bd25a1e4809af3ed2358be449bd) (UART\_Type \*pUART) |
|  | disable transmit buffer empty interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_DisableTxCompleteInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga449a8727aea8f68e4c1b1f1c65b7fa7d) (UART\_Type \*pUART) |
|  | disable transmit complete interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_DisableRxBuffFullInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga996d58ec8ad21972ed2253089428b9dd) (UART\_Type \*pUART) |
|  | disable receive buffer full interrupt |
|  | |
| \_\_STATIC\_INLINE void | [UART\_PutBreak](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga1fa728be09e622fb2f51e3baa880efd4) (UART\_Type \*pUART) |
|  | print out break character |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [UART\_IsTxComplete](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#gaf43b220b3a09bc19512d6edadef31943) (UART\_Type \*pUART) |
|  | check whether tx is complete,i.e. data has been sent out. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [UART\_IsTxBuffEmpty](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga2b0a03cd5e29545aa8b156350bdbcf60) (UART\_Type \*pUART) |
|  | check whether Tx buffer is empty |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [UART\_IsRxBuffFull](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__uart__api__list.html#ga042a861e2435cc3234e414970d13875d) (UART\_Type \*pUART) |
|  | check whether Rx buffer is full, i.e. receive a character |

### GPIO API Specification

|  |  |
| --- | --- |
| void | [GPIO\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga35bac6c452a6b0fa277003aa7578ed2f) (GPIO\_Type \*pGPIO) |
|  | Initialize the GPIO registers to the default reset values. |
|  | |
| void | [GPIO\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#gaef89050db3f627617a24b8c77ba03fe9) (GPIO\_Type \*pGPIO, uint32\_t u32PinMask, [GPIO\_PinConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__config__type__list.html#gac96f36bf22865743c87799ffb1054a15) sGpioType) |
|  | Initialize GPIO pins which are specified by u32PinMask. |
|  | |
| void | [GPIO\_Toggle](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga58432a2de3941bfe79dc79b2af2e5312) (GPIO\_Type \*pGPIO, uint32\_t u32PinMask) |
|  | Toggle the pins which are specified by u32PinMask. |
|  | |
| uint32\_t | [GPIO\_Read](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga180a671d41bdae6f9012d3c284a42d96) (GPIO\_Type \*pGPIO) |
|  | Read input data from GPIO which is specified by pGPIO. |
|  | |
| void | [GPIO\_Write](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga28b6197c414d7b23598e70d8705de903) (GPIO\_Type \*pGPIO, uint32\_t u32Value) |
|  | Write output data to GPIO which is specified by pGPIO. |
|  | |
| void | [GPIO\_PinInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga0765ca80746724544a0963c09353e8f3) ([GPIO\_PinType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__name__list.html#ga6282ee51186766675c00c446e48f6450) GPIO\_Pin, [GPIO\_PinConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__config__type__list.html#gac96f36bf22865743c87799ffb1054a15) GPIO\_PinConfig) |
|  | Initialize GPIO single pin which is specified by GPIO\_Pin. |
|  | |
| void | [GPIO\_PinToggle](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga6028cda1fa89f1f018edaf87f3b3bf61) ([GPIO\_PinType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__name__list.html#ga6282ee51186766675c00c446e48f6450) GPIO\_Pin) |
|  | Toggle GPIO single pin which is specified by GPIO\_Pin. |
|  | |
| void | [GPIO\_PinSet](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga5f6015a2fa4d4c7651943810719bef67) ([GPIO\_PinType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__name__list.html#ga6282ee51186766675c00c446e48f6450) GPIO\_Pin) |
|  | Set GPIO single pin which is specified by GPIO\_Pin. |
|  | |
| void | [GPIO\_PinClear](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga9b16ea2095852a6f769395dc4fc2e1d7) ([GPIO\_PinType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__pin__name__list.html#ga6282ee51186766675c00c446e48f6450) GPIO\_Pin) |
|  | Clear GPIO single pin which is specified by GPIO\_Pin. |
|  | |
| \_\_STATIC\_INLINE void | [FGPIO\_Toggle](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga3994e3a1fdeb15f339f447b4a8348f75) (FGPIO\_Type \*pFGPIO, uint32\_t u32PinMask) |
|  | Toggle the pins which are specified by u32PinMask in single cycle. |
|  | |
| \_\_STATIC\_INLINE uint32\_t | [FGPIO\_Read](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#ga4c8b4f0a97f0f3ad78683a0a21687670) (FGPIO\_Type \*pFGPIO) |
|  | Read input data from GPIO which is specified by pGPIO in single cycle. |
|  | |
| \_\_STATIC\_INLINE void | [FGPIO\_Write](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__gpio__api__list.html#gacf28fd9ddf878f89bc3ac0eeddb1cdb3) (FGPIO\_Type \*pFGPIO, uint32\_t u32Value) |
|  | Write output data to GPIO which is specified by pGPIO in single cycle. |

### PWT API Specification

|  |  |
| --- | --- |
| void | [PWT\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga41d95517cd8c823edd2a825769f482ea) ([PWT\_ConfigType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__config__type.html#struct_p_w_t___config_type) \*pConfig) |
|  | initialize pwt module. |
|  | |
| void | [PWT\_SetCallback](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga0a08ba4464600362f9b9d8adad82c2ff) ([PWT\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__callback.html#ga0c4eb080a548e5eaff5b8427c65c58cd) pfnCallback) |
|  | pwt module set call back. |
|  | |
| void | [PWT\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga9c5a6ca40e0d4985814b0e46cb522d16) (void) |
|  | pwt module de-initialize, reset pwt register |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga6d13cb7bddd9e2ba58fce7abfc906985) (void) |
|  | enable pwt module. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga7637f27ef7aeebed6dc1d12d742e17de) (void) |
|  | disable pwt module. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_EnableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga7f315140a9aab4ec51a0a6e22338be4c) (void) |
|  | enable pwt module interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_DisableInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga30a4661d70ef2881451ebda45c556882) (void) |
|  | disable pwt module interrupt . |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_EnableOverFlowInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga95bb74fa6b9119a0811606f655173a84) (void) |
|  | enable pwt module timer over flow interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_DisableOverFlowInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga4e25038bf0791bfe43fc9302f25ddfef) (void) |
|  | disable pwt module timer over flow interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_EnableReadyInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#gadca3ff9e7ab6510a8b7d0f218b32248e) (void) |
|  | enable pwt module data ready interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_DisableReadyInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga3782674574baa20ee686e030c28e182b) (void) |
|  | disable pwt module data ready interrupt. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_SetSoftReset](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga20c3efdb99ad9e9fb2e9f3e641f332a2) (void) |
|  | set pwt module soft reset. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_SetPinSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga5d06fedc32faab53d91b7c4856c4464f) (uint8\_t u8PinSelect) |
|  | set pwt module input pin selct. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_SetEdgeSensitivity](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga1b8d79590c0e90ae882559291041061e) (uint8\_t u8EdgeSensitivity) |
|  | set pwt module input edge sensitivity. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_SetClockPresacler](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga4a9f9e3af415f8d3644a0903e2ef06bc) (uint8\_t u8ClockPresacler) |
|  | set pwt module input clock presacler. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [PWT\_GetOverflowFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga39fd00ed68ed22254d9b96049a62d492) (void) |
|  | get pwt overflow flag. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_ClrOverflowFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga85377371e5791163e207ad69db31f54b) (void) |
|  | clear pwt overflow flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [PWT\_GetReadyFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga154c354c99888102eb096db565da9703) (void) |
|  | get pwt ready flag. |
|  | |
| \_\_STATIC\_INLINE void | [PWT\_ClrReadyFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__pwt__api__list.html#ga7c03e806fed4cc65c904e714cb516c7e) (void) |
|  | clear pwt ready flag. |
|  | |

### BME API Specification

|  |  |
| --- | --- |
| #define | [BME\_AND](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1aa902312bef07dd995b8f2de5a94461)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_OR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gae647428045f10813c9adf5b4705385f3)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_XOR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga5363f0afb2bbc2673a069509e1bac1e6)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BIT\_CLEAR](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gac6cd56c73903d423100506ecaa66cbd3)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1748e7c9c7fa55745a87292931eef9fe)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BITFIELD\_INSERT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga83b580edd411d979714b7b289f54c060)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga137adf383dbe3b4b851c0e769b089016)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address). |
|  | |
| #define | [BME\_AND\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gaa9ba136dd0b51048e2bc2e93d4929589)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_OR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga5db86a62bc2d981fcd541f692ea9d74e)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_XOR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gae85e1942e2b95f7a1a4cc6d083f5ee52)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BIT\_CLEAR\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga4a144977bf06634c568655f9a7a0ff05)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga84100d47a0f13ab2e527c0db19ce76b9)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BITFIELD\_INSERT\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gab4b15fff26eda50d0b93a834c5367634)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT\_8b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gac27561cbff63967157b8d56e02f343ad)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address) for 8-bit data. |
|  | |
| #define | [BME\_AND\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga0feed195dc2eb8d813a49236d8d5e74e)(ADDR) |
|  | generates BME AND operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_OR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gada49ceacf4be9a3f3760803cbb9ce920)(ADDR) |
|  | generates BME OR operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_XOR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1017565a07d0554b314bf073e67d1d18)(ADDR) |
|  | generates BME XOR operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BIT\_CLEAR\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#gaa3f3269ccb4258e434ab4bf0bf6753b0)(ADDR, bit) |
|  | This is fastest way for BME without sanity check. |
|  | |
| #define | [BME\_BIT\_SET\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga20a106dda348cf1541830a0f78880703)(ADDR, bit) |
|  | generates BME bit set operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BITFIELD\_INSERT\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga4254ebadcaba31e730669ae8fd7dcd35)(ADDR, bit, width) |
|  | generates BME bitfield insert operation addresss (hardcoded 32-bit address) for 16-bit data. |
|  | |
| #define | [BME\_BITFIELD\_EXTRACT\_16b](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_m_e___utilities.html#ga1fb14e4c123fe034f259371cf4abbb6f)(ADDR, bit, width) |
|  | generates BME bitfield extract operation addresss (hardcoded 32-bit address) for 16-bit data. |

### BitBand API Specification

|  |  |
| --- | --- |
| \_\_STATIC\_INLINE void | [BIT\_BandVariableInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__bit__band__api__list.html#gad385691ad94995fb1aa6c703dee47387) (uint32\_t \*pVariableAddress, [BIT\_BandPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group___b_i_t___band_type.html#struct_b_i_t___band_type) \*pBitbandPtr) |
|  | bit-band initialize pointer, so that invoke the pointer to access alisaed bitband. |

### CAN APIs Specification

|  |  |
| --- | --- |
| void | [CAN\_Init](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga25fbb5ee11c03a20edb3e45d373c04c7) (MSCAN\_Type \*pCANx, [MSCAN\_ConfigPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__config__type.html#struct_m_s_c_a_n___config_type) pCANConfig) |
|  | Initialize MSCAN module. |
|  | |
| uint8\_t | [CAN\_DeInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga4e8e8ff5970559eca4136b41a8a58bad) (MSCAN\_Type \*pCANx) |
|  | Denitialize MSCAN module. |
|  | |
| uint8\_t | [CAN\_EnterSleep](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaa47f3648da6073fc4090dbec1d3f53a0) (MSCAN\_Type \*pCANx) |
|  | MSCAN enter into sleep mode. |
|  | |
| uint8\_t | [CAN\_WakeUpFromSleep](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf679b94fafdc402e1d59e2ba2eb8bc6e) (MSCAN\_Type \*pCANx) |
|  | MSCAN wakeup from sleep mode. |
|  | |
| uint8\_t | [CAN\_LaunchInitialRequest](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3baa087ca083ecfce6eb56cc212606ac) (MSCAN\_Type \*pCANx) |
|  | launch the CAN initialization mode request |
|  | |
| uint8\_t | [CAN\_LoadOneFrameToBuff](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga0c2a2e0fb8b72f5231b517903b0b16ee) (MSCAN\_Type \*pCANx, [MSCAN\_RegisterFramePtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__register__frame__struct__type.html#struct_m_s_c_a_n___register_frame_type) pFrameBuff) |
|  | Write a frame data to transmitter buff. |
|  | |
| uint8\_t | [CAN\_ReadOneFrameFromBuff](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga01dfab88d93c3544d8e5355f3c15fb6e) (MSCAN\_Type \*pCANx, [MSCAN\_FramePtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__frame__type.html#struct_m_s_c_a_n___frame_type) pRxFrame) |
|  | read a frame data from receiver buffer |
|  | |
| void | [MSCAN\_SetRxCallBack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad398f8a0c3924e24455c587fec374f5a) ([MSCAN\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__callback.html#ga42a99caeb0e89b2283aeb9015591f1f8) pCallBack) |
|  | set receive call back function for MSCAN module. |
|  | |
| void | [MSCAN\_SetTxCallBack](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga60ae9148de941af1fee76244a13b42b3) ([MSCAN\_CallbackType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__callback.html#ga42a99caeb0e89b2283aeb9015591f1f8) pCallBack) |
|  | set transmitting call back function for MSCAN module. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsRxFrameFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3227c7978ba9aeaa462be5a711ed2381) (MSCAN\_Type \*pCANx) |
|  | Check Received Frame Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsReceiving](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gac354d8895abb3fa2e4a28066d61301f2) (MSCAN\_Type \*pCANx) |
|  | Check Receiver Active Status. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsSynToBus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf1db1df04a099a1c0c23be7bc642d439) (MSCAN\_Type \*pCANx) |
|  | Check CAN Synchronized Status. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_SleepModeReq](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gab2ac4651636e56be9dafda1ff7adf07a) (MSCAN\_Type \*pCANx) |
|  | Sleep Mode Request. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_InitialModeReq](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gadf6da889c0e9aaba56e62a31c1802cfd) (MSCAN\_Type \*pCANx) |
|  | Initialization Mode Request. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_NormalModeReq](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gab3f7288fba38eb216a19286d96375e8c) (MSCAN\_Type \*pCANx) |
|  | Normal Mode Request. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_LoopModeEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga50dc462c503983f276d0dd1bd9e33f39) (MSCAN\_Type \*pCANx) |
|  | Loopback Self Test Mode Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_LoopModeDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga9c38b41c11d9046a2cadc576cbb07859) (MSCAN\_Type \*pCANx) |
|  | Loopback Self Test Mode Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ListenModeEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaeb6e41f6014819634f4eb9074c28e04a) (MSCAN\_Type \*pCANx) |
|  | Listen Only Mode Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ListenModeDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad00a43adc5dad3f12348f055d59b5799) (MSCAN\_Type \*pCANx) |
|  | Listen Only Mode Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_BusOffUserRecovery](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf20c489b797843f4c9c59048f07e4c40) (MSCAN\_Type \*pCANx) |
|  | Bus-Off Recovery Mode - user request. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_BusOffAutoRecovery](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaadc743ba9887855ad0e31cad0ba1528d) (MSCAN\_Type \*pCANx) |
|  | Bus-Off Recovery Mode - Auto. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsSleepMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga35071771c30defbe477637c39f2e6664) (MSCAN\_Type \*pCANx) |
|  | Check Sleep Mode Acknowledge. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsInitialMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gac68ec445ac5bc5aaf1f36934592521e2) (MSCAN\_Type \*pCANx) |
|  | Check Initialization Mode Acknowledge. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsWakeUpIntFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3a993d783abf67c9befa652cda1a773c) (MSCAN\_Type \*pCANx) |
|  | Check Wake-Up Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsStatusChangeFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gafb37d70b17dc8d89fccbd243fd260b62) (MSCAN\_Type \*pCANx) |
|  | Check CAN Status Change Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsOverRunFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga5335a8c3d22ed36c54390bf56bf10dc6) (MSCAN\_Type \*pCANx) |
|  | Check Overrun Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsRxBuffFull](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga4ee62465391d7c81221efcd9f8abffb6) (MSCAN\_Type \*pCANx) |
|  | Check Receive Buffer Full Flag. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetReceiverStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga06278670eed6449a2f0685e5ac6891f0) (MSCAN\_Type \*pCANx) |
|  | Get Receiver Status. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetTransmitterStatus](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga2f34a8abf34869087da4e5f0ef469543) (MSCAN\_Type \*pCANx) |
|  | Get Transmitter Status. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetTransmitterBufferEmptyFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaa1f034d237bd6dc676f45f59c89ec33c) (MSCAN\_Type \*pCANx) |
|  | Get Transmitter Buffer Empty Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TransmitterEmptyIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaad9b64b822cad3a277bf9a8a55574d5e) (MSCAN\_Type \*pCANx) |
|  | Transmitter Empty Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TransmitterEmptyIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaea21b5aa4d33fee51f5a2d8cd34f3b93) (MSCAN\_Type \*pCANx) |
|  | Transmitter Empty Interrupt Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteAbortRequestReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gae86bbda8963e4d7432a6ccfbf990e701) (MSCAN\_Type \*pCANx, uint8\_t u8AbortRequest) |
|  | Write Abort Request Register. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetAbortFlag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad58142bd0c359f75c52fd836c4e99482) (MSCAN\_Type \*pCANx) |
|  | Get Abort Acknowledge. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TxBuffSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaeb681fcee2129c8def1400dc8b2962f3) (MSCAN\_Type \*pCANx, uint8\_t u8TxBuff) |
|  | Transmit Buffer Select - The lowest numbered bit places the respective transmit buffer in the CANTXFG register space. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetTxBuffSelect](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga824449cc88fdf6bb6177175a8d4f3737) (MSCAN\_Type \*pCANx) |
|  | Get the selected transmitter buffer. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_IDAcceptMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga41daede43ccdc36f49ee8a518111ab32) (MSCAN\_Type \*pCANx, uint8\_t u8IDAcceptMode) |
|  | Set Identifier Acceptance Mode. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IDAcceptHit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga0c2e27b04bb83596e44e7f9b1e5e3746) (MSCAN\_Type \*pCANx) |
|  | Set Identifier Acceptance Hit Indicator. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_IsBusOff](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga8fbbc36a6e77b1c2833c5fea2aa3aa76) (MSCAN\_Type \*pCANx) |
|  | Check if is Bus-Off Status. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_GetReceiveErrorCount](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gac413dae835a63a7cce77d2202c32bb1e) (MSCAN\_Type \*pCANx) |
|  | Get Receive Error Counter. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_Enable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaba9d8b473dc62b6dc84a5b133e06cd32) (MSCAN\_Type \*pCANx) |
|  | CAN enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_Disable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga05a5fcb22a633c4a6f826527a510ae16) (MSCAN\_Type \*pCANx) |
|  | CAN disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaa9cef2dab34db85f7f4b088dc877f49a) (MSCAN\_Type \*pCANx) |
|  | WakeUp Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga7d54f992b8aaa1d04cf2887a88e6bc2a) (MSCAN\_Type \*pCANx) |
|  | WakeUp Interrupt Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_StatusChangeIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga6d8cda4bb001055bc89228479ebe48b0) (MSCAN\_Type \*pCANx) |
|  | CAN Status Change Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_StatusChangeIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gae714ab2305e3f04bbf9e46704f5a08f8) (MSCAN\_Type \*pCANx) |
|  | CAN Status Change Interrupt Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ReceiverStatusChangeIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga975095fd17afdb54aef8f4436df66172) (MSCAN\_Type \*pCANx) |
|  | Receiver Status Change Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ReceiverStatusChangeIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga05d6c65024451aa62f20a4ed6e67061b) (MSCAN\_Type \*pCANx) |
|  | CAN Receiver Status Change Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TransmitterStatusChangeIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga48150d58f35f4a2f60fca4fafc5bd34c) (MSCAN\_Type \*pCANx) |
|  | Transmitter Status Change Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TransmitterStatusChangeIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga4b48d892bb78aff79db701141b99ac4a) (MSCAN\_Type \*pCANx) |
|  | Transmitter Status Change Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_OverrunIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga08e8b43c15d3ec2e8b69f821a2355538) (MSCAN\_Type \*pCANx) |
|  | Overrun Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_OverrunIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga7055cbf309b7eb91bb44f23dcad61678) (MSCAN\_Type \*pCANx) |
|  | Overrun Interrupt Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ReceiverFullIntEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga6ffa0ceab0bc3f11928936314d5a6138) (MSCAN\_Type \*pCANx) |
|  | Receiver Full Interrupt Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ReceiverFullIntDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga25f26df8130c489622d33c460c182ca9) (MSCAN\_Type \*pCANx) |
|  | Receiver Full Interrupt Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TimerEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga2f66442d093b97d7b622dfaa7555d869) (MSCAN\_Type \*pCANx) |
|  | Timer Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_TimerDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga142e1a22e8514bc4e34927a94f180a28) (MSCAN\_Type \*pCANx) |
|  | Timer Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_StopInWaitModeEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3b80c42894707bc9dea31eee84124f91) (MSCAN\_Type \*pCANx) |
|  | CAN Stops in Wait Mode Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_StopInWaitModeDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga05b49d4dbab0d666841f626180dc0cd4) (MSCAN\_Type \*pCANx) |
|  | CAN Stops in Wait Mode Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpEn](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga6578f83a453164202810f920a17b9182) (MSCAN\_Type \*pCANx) |
|  | WakeUp Enable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpDisable](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaabfc9979720925fb3367851bb097ff09) (MSCAN\_Type \*pCANx) |
|  | WakeUp Disable. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpModeFilter](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga596d2482f117ed2cc13a50bcac97d91d) (MSCAN\_Type \*pCANx) |
|  | MSCAN wakes up only in case of a dominant pulse on the CAN bus that has a length of Twup. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WakeUpModeNoFilter](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga81ee5d4d7b07d9e31864685b4b4ba5b0) (MSCAN\_Type \*pCANx) |
|  | MSCAN wakes up on any dominant level on the CAN bus. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteIDAR0Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3525e3e9bfa56d60eb505eb20548d110) (MSCAN\_Type \*pCANx, uint8\_t \*pID) |
|  | write MSCAN Identifier Acceptance Register n of First Bank |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteIDAR1Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga739df6ca01d8125e8dbf4b080e4c7838) (MSCAN\_Type \*pCANx, uint8\_t \*pID) |
|  | write MSCAN Identifier Acceptance Register n of Second Bank |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteIDAM0Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga60543af55b3090c2fce71bb3ad0ef537) (MSCAN\_Type \*pCANx, uint8\_t \*pID) |
|  | write MSCAN Identifier Mask Register n of First Bank |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_SetBaudRateReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gab30b2c80bdcdf459a182f7e9025ca5f5) (MSCAN\_Type \*pCANx, [MSCAN\_BaudRateSettingType](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__baudrate__setting__type.html#struct_m_s_c_a_n___baud_rate_setting_type) sBspSetting) |
|  | write MSCAN Bus Timming Register |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteIDAM1Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga805b25ccf721110136b1d4c0c654251d) (MSCAN\_Type \*pCANx, uint8\_t \*pID) |
|  | write MSCAN Identifier Mask Register n of Second Bank |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_LaunchTransmitBuffer](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga843782569e794d4701e77a0a30a0b2db) (MSCAN\_Type \*pCANx, uint8\_t TxBuffer) |
|  | Clear Transmitter Buffer Empty Flag to schedule for transmission. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteTEIDR0Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf4e7515af5c47d4a4f43b114be803120) (MSCAN\_Type \*pCANx, uint8\_t ucBuffer) |
|  | Write Transmit Extended and Standard Identifier Register 0. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteTEIDR1Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga0df8ee1848f64275ec6257c6cbaef6c6) (MSCAN\_Type \*pCANx, uint8\_t ucBuffer) |
|  | Write Transmit Extended Identifier Register 1. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteTEIDR2Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga09f73f94228f2ace73487c370b2dfe55) (MSCAN\_Type \*pCANx, uint8\_t ucBuffer) |
|  | Write Transmit Extended Identifier Register 2. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteTEIDR3Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad3418d5c5c143147667f77c892f140e9) (MSCAN\_Type \*pCANx, uint8\_t ucBuffer) |
|  | Write Transmit Extended Identifier Register 3. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_WriteTSIDR1Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga3587a680d1019b6fd0e18ef39dcfeba4) (MSCAN\_Type \*pCANx, uint8\_t ucBuffer) |
|  | Write Transmit Standard Identifier Register 1. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadIDR1Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga92139dd02db742c78aeeb6ade4babc45) (MSCAN\_Type \*pCANx) |
|  | Read receiver extend and standard identifier register 1. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadIDR0Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gadcacd114b2d29438eb97a8623111c3af) (MSCAN\_Type \*pCANx) |
|  | Read receiver extend and standard identifier register 0. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadIDR2Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf9ffdeb76d0d3013954f5feec2cd3f4c) (MSCAN\_Type \*pCANx) |
|  | Read receiver extend identifier register 2. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadIDR3Reg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga4aa8b6944a19ae35cdaf1a679f8a6074) (MSCAN\_Type \*pCANx) |
|  | Read receiver extend identifier register 3. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadRTSRHReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga2cda9d4b415faa0fd3457bebe7159b5f) (MSCAN\_Type \*pCANx) |
|  | Read receiver stamps registers high bytes. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadRTSRLReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad0dc54496f10c27224c1b2270988110c) (MSCAN\_Type \*pCANx) |
|  | Read receiver stamps registers low bytes. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadTTSRHReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga14182a69fd3f1ce85e17bb9d5ce8a1fa) (MSCAN\_Type \*pCANx) |
|  | Read transmitter stamps registers high byte. |
|  | |
| \_\_STATIC\_INLINE uint8\_t | [CAN\_ReadTTSRLReg](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga44201b92ab63398120060e3416138409) (MSCAN\_Type \*pCANx) |
|  | Read transmitter stamps registers low byte. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearWUPIF\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga9ed77a1c72ea799e7f61b3cffd7bdca4) (MSCAN\_Type \*pCANx) |
|  | Clear Wake-Up Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearCSCIF\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf91944bb9c03c26b9b47b305b022076c) (MSCAN\_Type \*pCANx) |
|  | Clear CAN Status Change Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearOVRIF\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga9ffed3a7f03c556260a88ff25a11f31e) (MSCAN\_Type \*pCANx) |
|  | Clear Overrun Interrupt Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearRXF\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gad4e31c22ad68dc0c3f9cb79da5116d3d) (MSCAN\_Type \*pCANx) |
|  | Clear Receive Buffer Full Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearRXFRM\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga9a7743c1b3e2c8bfa8be2d8eded2a8ea) (MSCAN\_Type \*pCANx) |
|  | Clear Received Frame Flag. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_ClearSLPRQ\_Flag](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga022ac57c76b2d7512e3915b67915ad7f) (MSCAN\_Type \*pCANx) |
|  | Clear Sleep Mode Request. |
|  | |
| \_\_STATIC\_INLINE void | [CAN\_SetIDAMode](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga5d0df50dd49baa12da03a7652cc1cebd) (MSCAN\_Type \*pCANx, uint8\_t IDAMode) |
|  | Identifier Acceptance Mode. |
|  | |
| void | [MSCAN\_RxIsr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaf091c7ee22f1cab13a574055242f8dda) (void) |
|  | MSCAN Receive interrupt service routine. |
|  | |
| void | [MSCAN\_TxIsr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gabe17631c7ccbef1304673ba9f5c49155) (void) |
|  | MSCAN transmitting interrupt service routine. |
|  | |
| uint8\_t | [CAN\_SendItemToBuffer](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga79bc7f00d94663e3a62bcab41e74da17) (MSCAN\_Type \*pCANx, [ItemInfoPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__item__info___list.html#struct_item_info_type) pTxItemInfo, [FrameBufferInfoPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__frame__buffer__info___list.html#struct_frame_buffer_info_type) pTxBuffInfo) |
|  | Write a frame data to buffers. |
|  | |
| uint8\_t | [CAN\_TransmitItemByInt](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gabadfc733565d527d2e4db6fb3155780f) (MSCAN\_Type \*pCANx, [ItemInfoPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__item__info___list.html#struct_item_info_type) pTxItemInfo, [FrameBufferInfoPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__frame__buffer__info___list.html#struct_frame_buffer_info_type) pTxBuffInfo) |
|  | Write a frame data to buffers. |
|  | |
| uint8\_t | [CAN\_CheckSendBufferFrame](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga4c190346960497e814796280760c3d27) (MSCAN\_Type \*pCANx, [FrameBufferInfoPtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__frame__buffer__info___list.html#struct_frame_buffer_info_type) pTxBuffInfo) |
|  | Check buffer status and send data frame to transmitting buffer of MSCAN. |
|  | |
| uint8\_t | [CAN\_ReadOneFramefromBufferQueue](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaa9f04f0fea039bd4ae1f312897e6102a) ([MSCAN\_FramePtr](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__frame__type.html#struct_m_s_c_a_n___frame_type) pRxFrameInfo) |
|  | Check buffer status and receive data frame from receiver buffer of MSCAN. |
|  | |
| void | [MSCAN\_GlobeVaribleInit](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga8faf77ea6e88bc52c46ddb11c74ff891) (MSCAN\_Type \*pCANx) |
|  | Initialize the globe variable for CAN buffer and buffer Queue. |
|  | |
| void | [MSCAN\_TxProcessing](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#ga16a5a7a7011ae0a20ab1ad370b1fd491) (void) |
|  | MSCAN Transmitting Callback function. |
|  | |
| void | [MSCAN\_RxProcessing](file:///C:\Users\B45107\Desktop\kexx_drv_lib\refman\ke06\group__mscan__api__list.html#gaca45cd19c6ae80306f39be67f71e8bba) (void) |
|  | MSCAN receiving Callback function. |