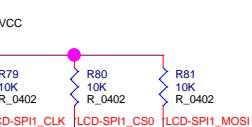
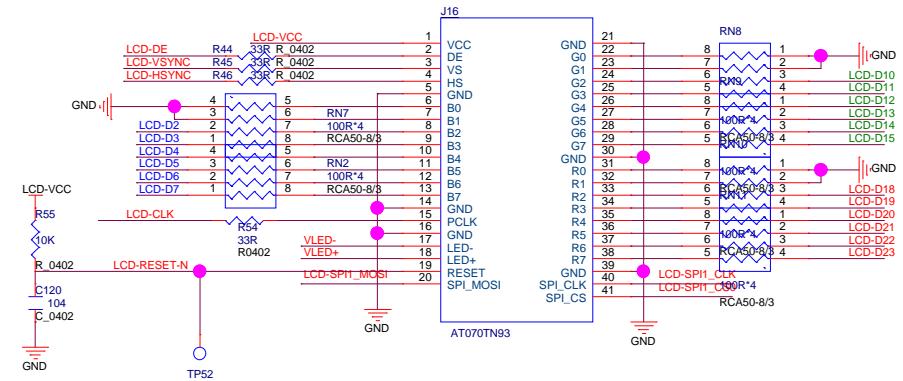
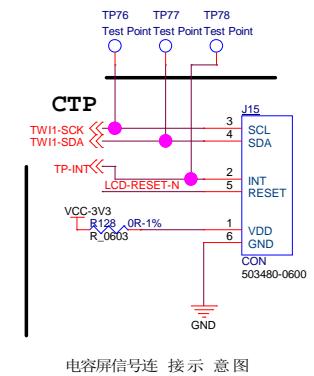
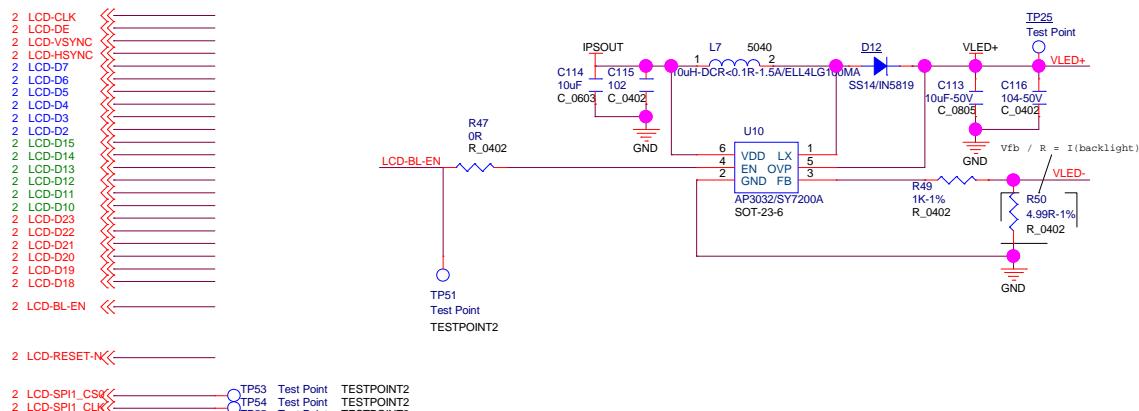
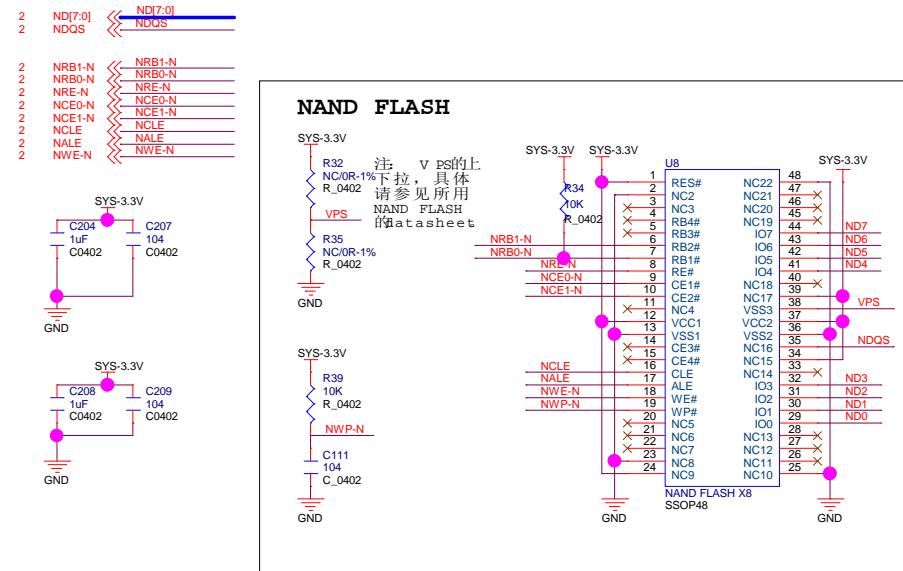


DISPLAY

LCD

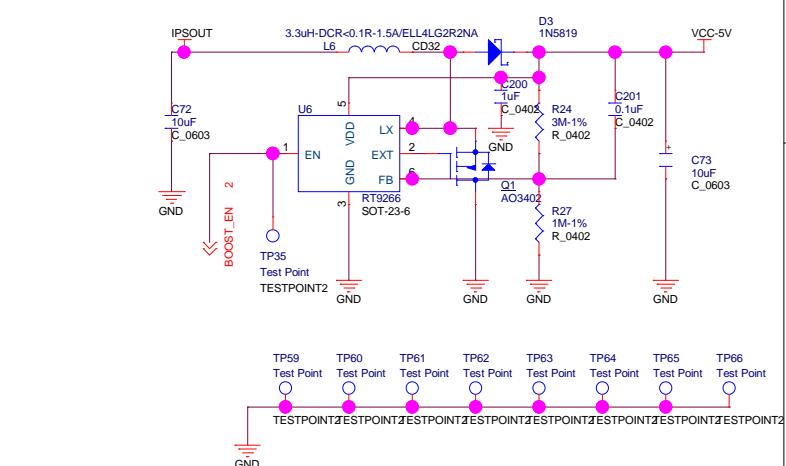
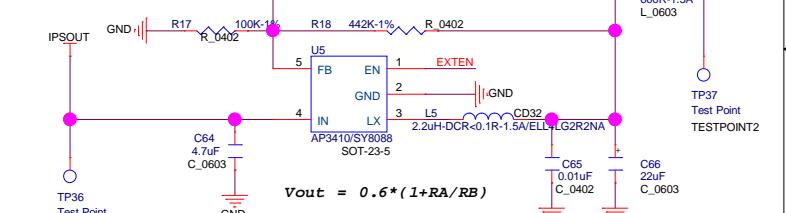
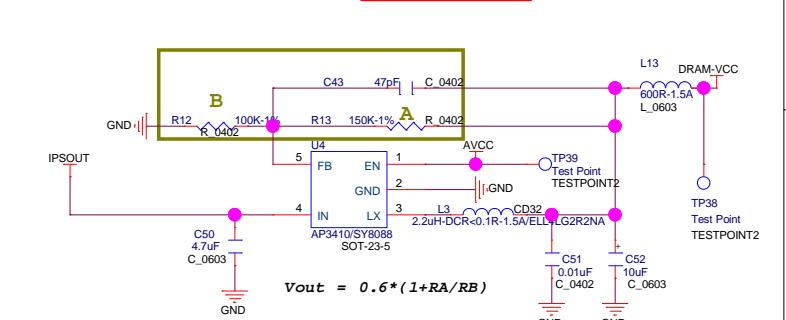
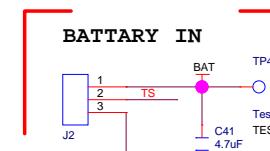
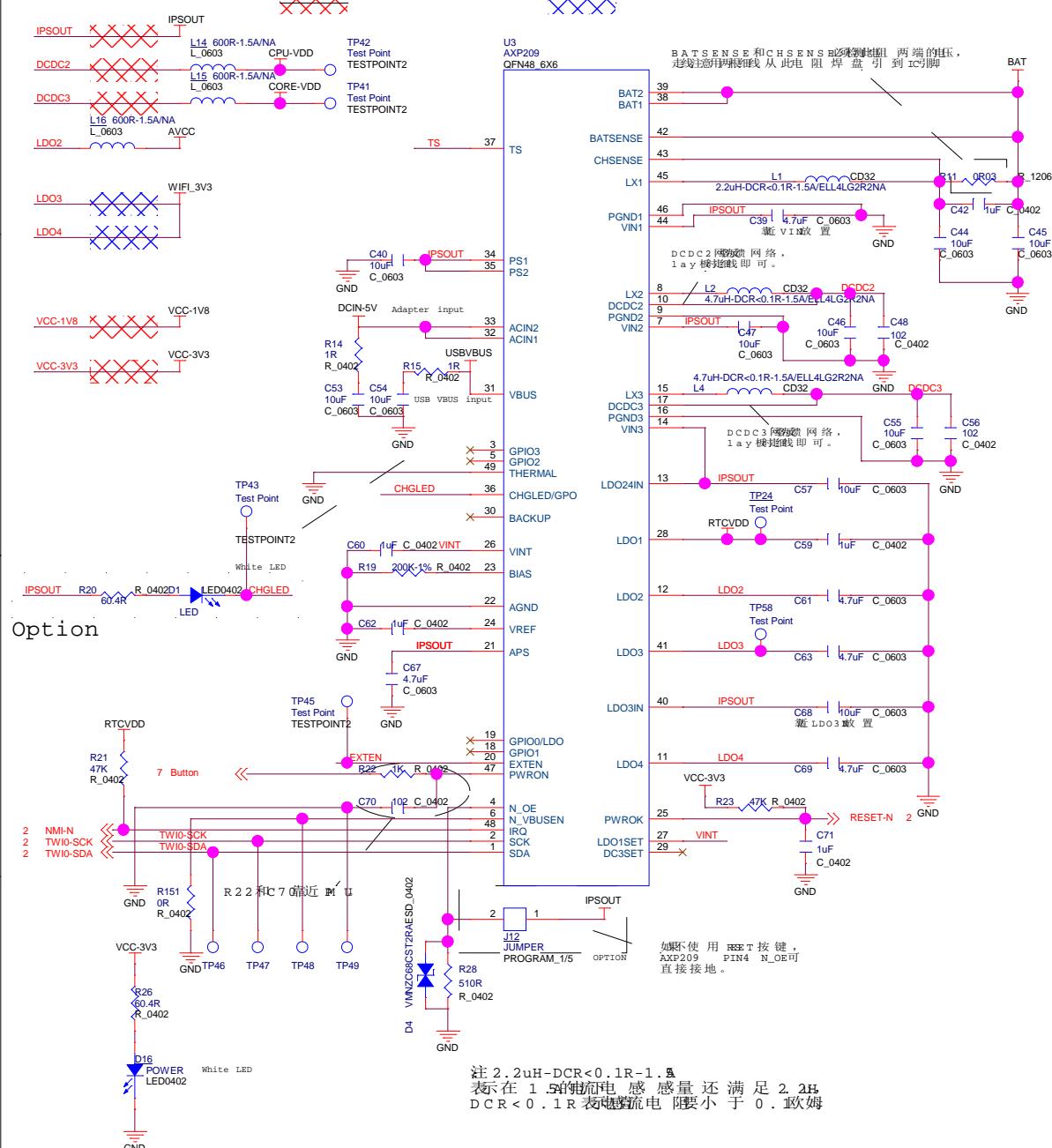


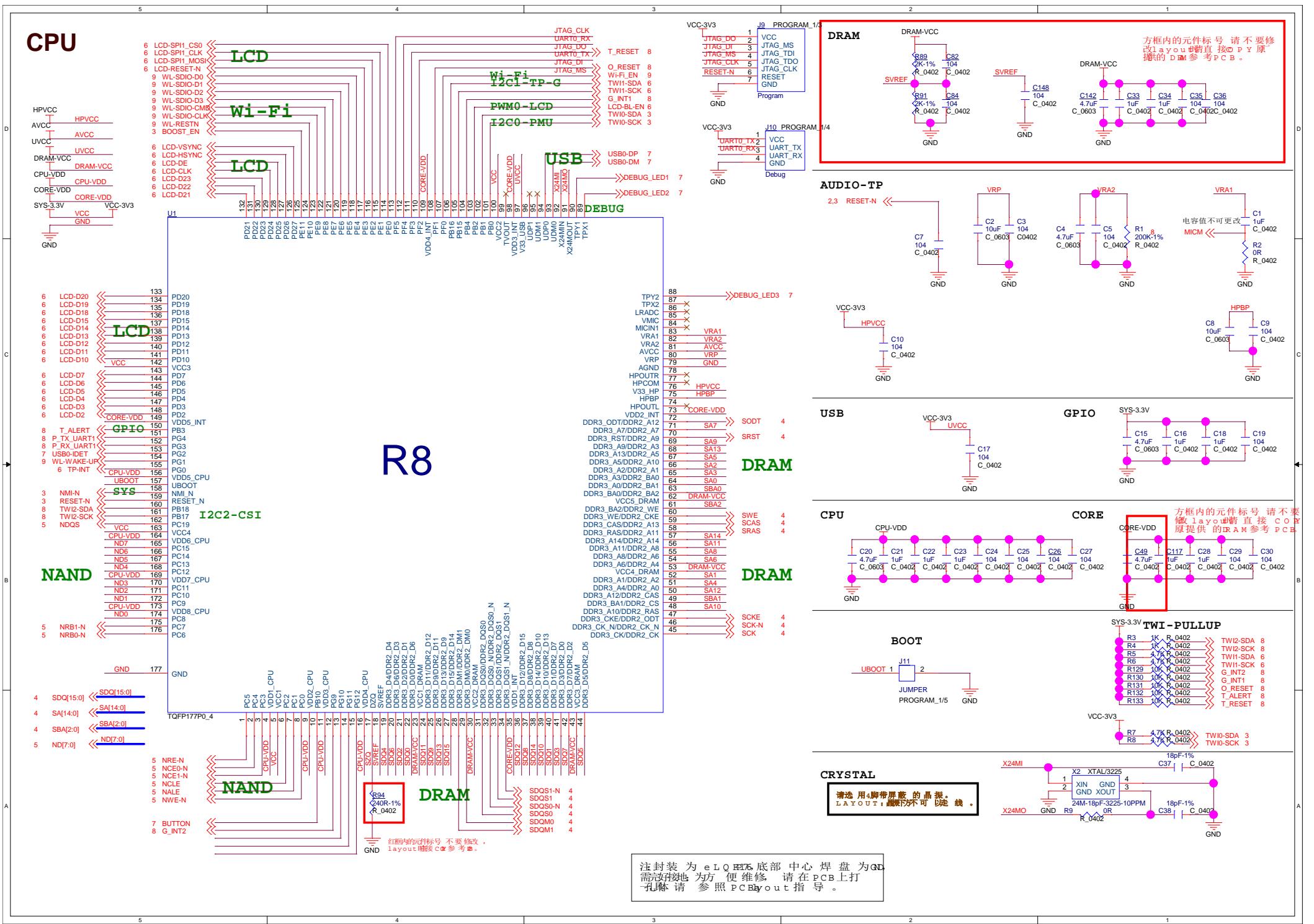
FLASH/CARD



POWER

POWER LINE:Width>=80mil POWER LINE:Width>=50mil





COVER

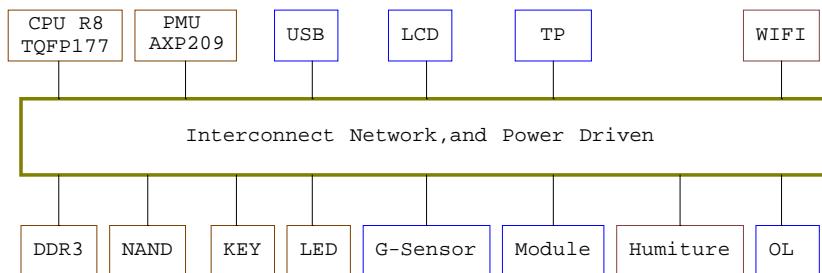
Schematics Index

Bran-Design

- 01 COVER
- 02 CPU
- 03 POWER
- 04 DDR3 16x1
- 05 FLASH
- 06 DISPLAY
- 07 PERIPHERS
- 08 SENSOR
- 09 WI-FI
- Option:
- 10 BLE

特别提醒：
 1: PG0 / PG1 这两个IN脚
 具有 INPIW 功能。
 2: PMU的GPIO0/1/2这三个IN脚
 做GPIO-OUTP功能。
 3: PG10 / PG11 / PG12 这三个IN脚
 的输出可改变。
 4: CSI-PCLK / CSI-MCL这两个IN脚
 具有 INPIW 功能。
 5: CSI-HSYNC具有 INPIW 功能，
 不作他用途。

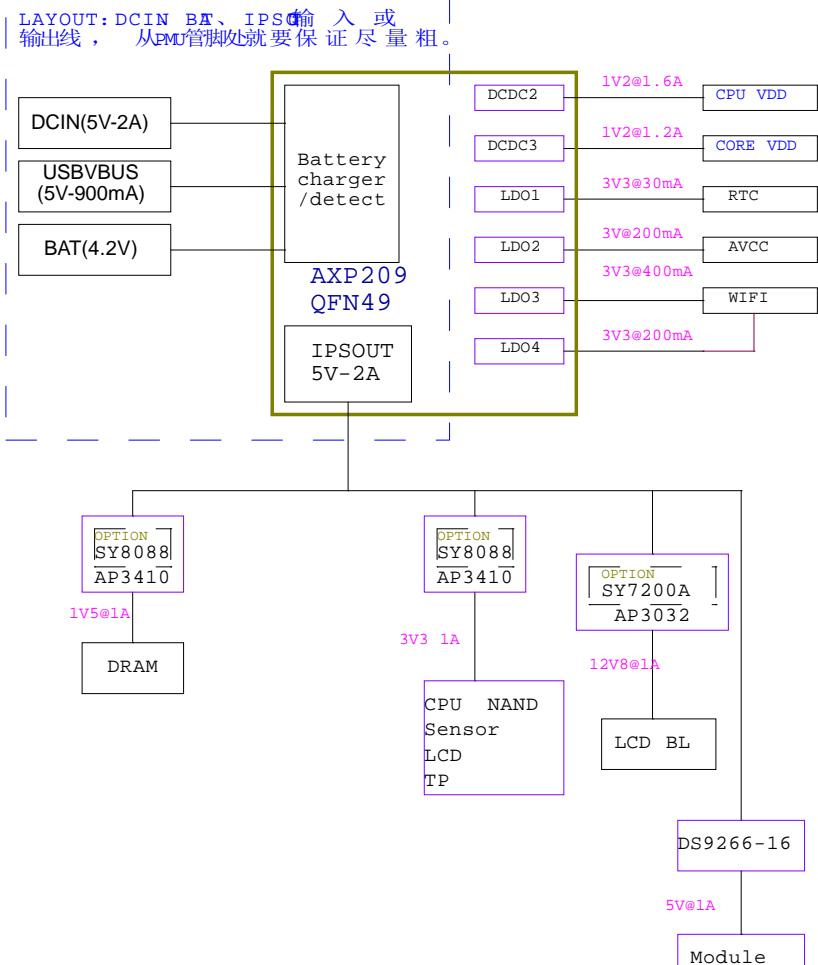
BLOCK



REVISION HISTORY

| Revision | Description | Date | Drawn | Checked | Approved |
|----------|--------------------|-----------|---------------|---------------|----------|
| Ver 1.0 | Bran Main PCB ASSY | 2016/6/19 | Violet/Jemmey | Jemmey | |
| Ver 1.5 | Bran Main PCB ASSY | 2016/7/31 | Jemmey | Jemmey/Violet | |
| | | | | | |
| | | | | | |
| | | | | | |

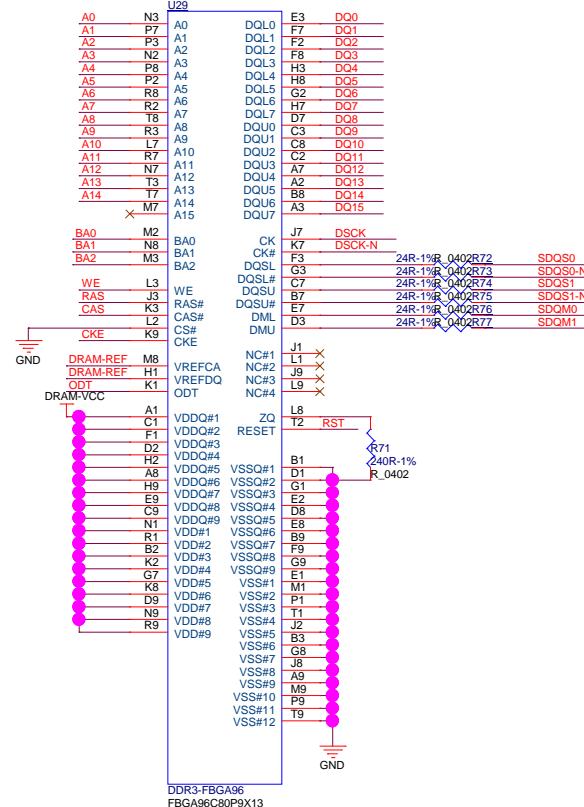
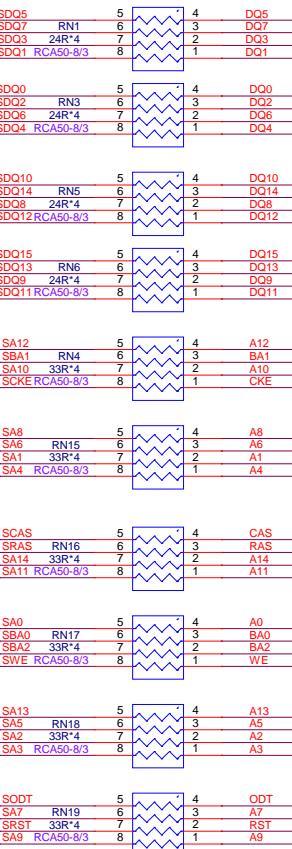
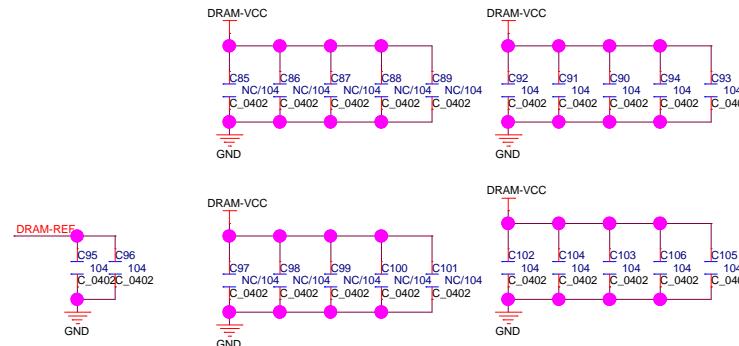
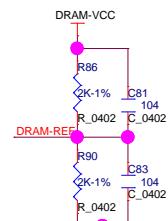
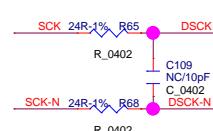
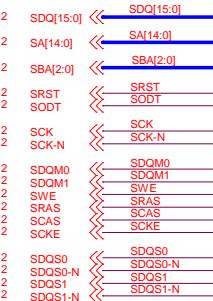
POWER TREE

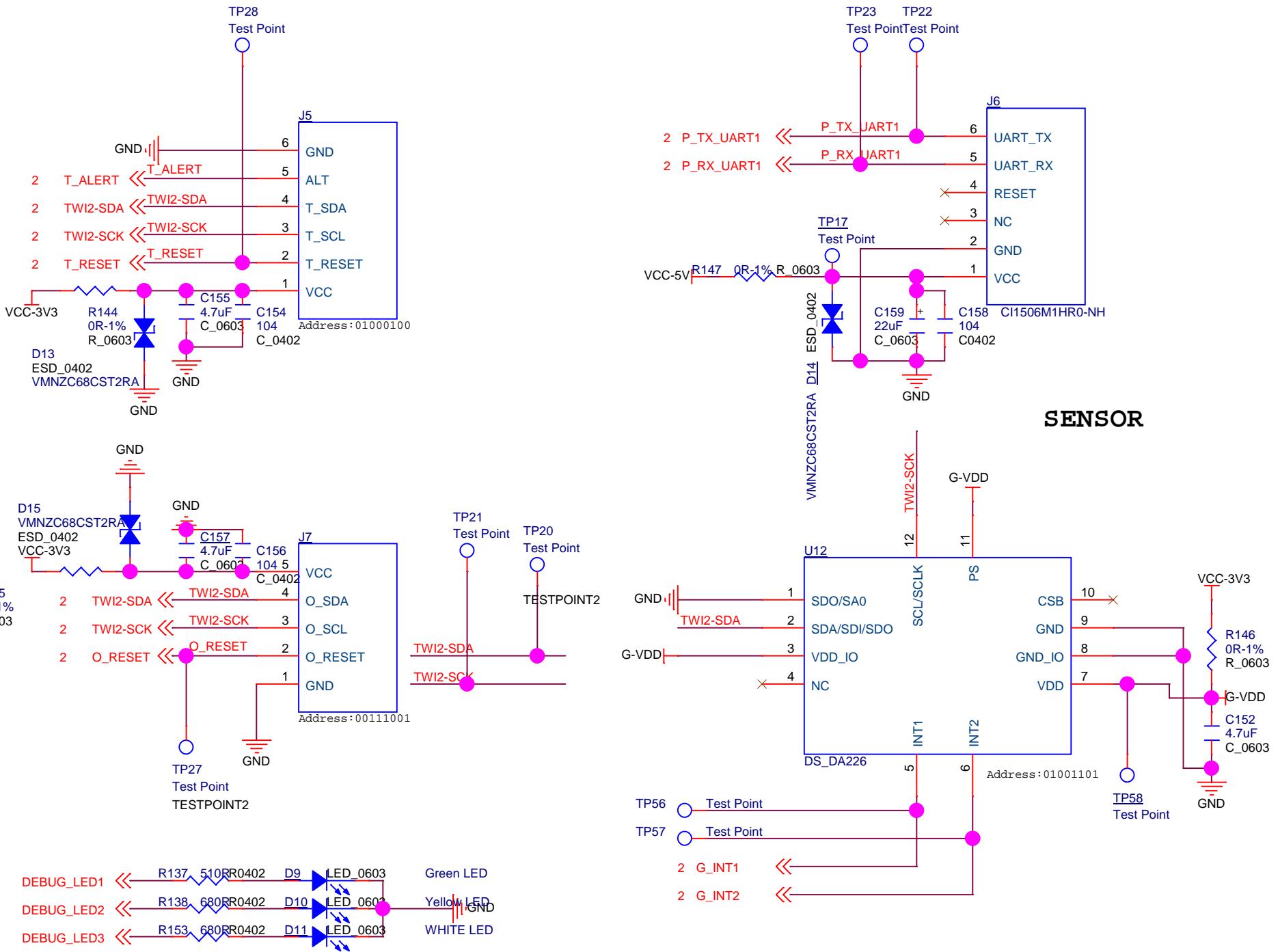


DDR3 16x1

DDR3

本页的元件标号请不要修改
layout时直接用原厂提供的DRAM参考PCB

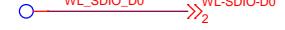
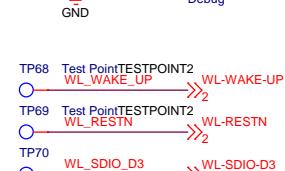
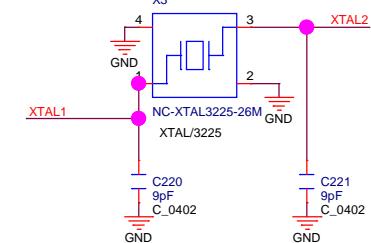
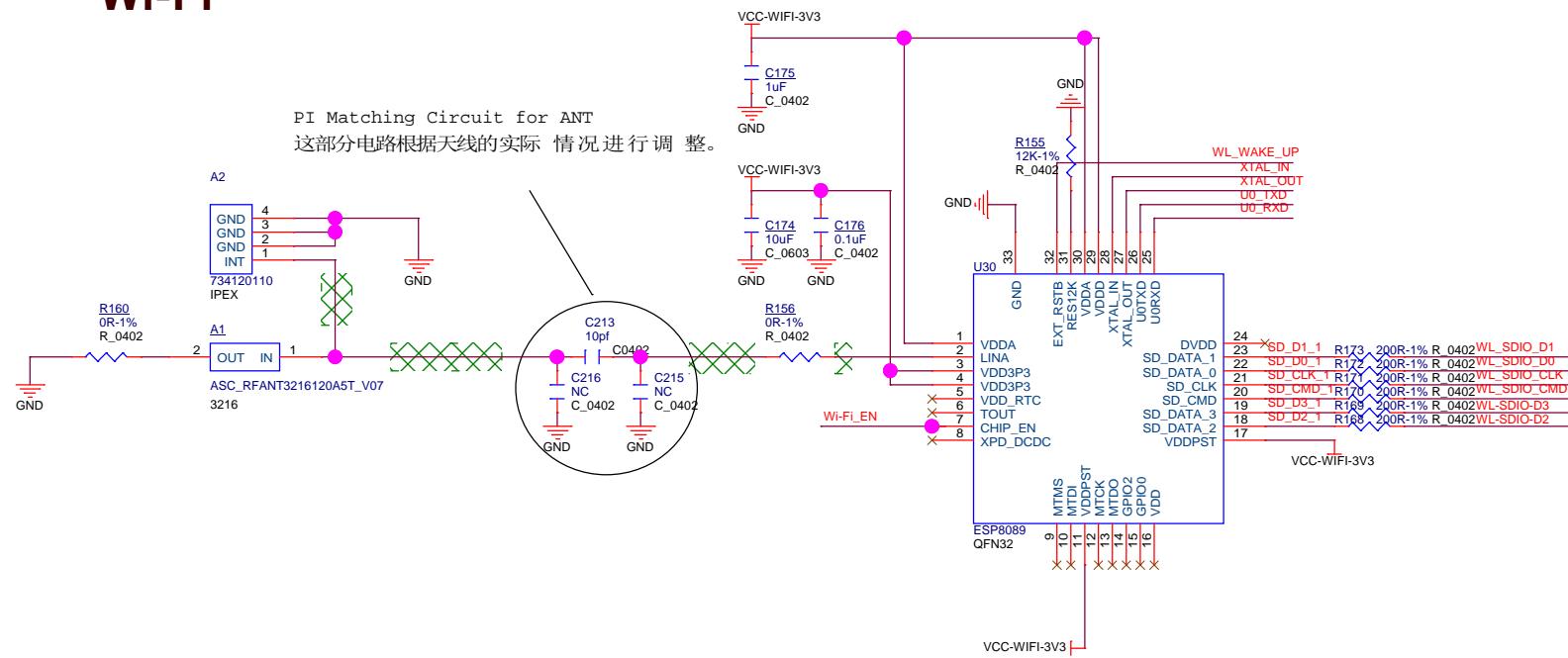




Wi-Fi

PI Matching Circuit for ANT

这部分电路根据天线的实际 情况进行调整



| | | | |
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| Title <Title> | | | |
| Size B | Document Number <Doc> | Rev <Rev> | Code: |
| Date: Sunday, July 31, 2016 | | Sheet | 1 of 1 |