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1
 2
         -- Company: GRUPO 9 - ED I
 3
         -- Engineers: Collante Gerardo; Ferraris Domingo
4
5
         -- Create Date: 19:23:48 05/20/2017
 6
         -- Module Name: ALUv2 - Behavioral
7
         -- Target Devices: Spartan 3E
8
         -- Description:
9
         ______
10
11
         library IEEE;
12
         use IEEE.STD LOGIC 1164.ALL;
         use IEEE.STD LOGIC ARITH.ALL;
13
14
         use IEEE.STD LOGIC UNSIGNED.ALL;
15
16
         entity ALU4Bit is
17
             Port ( A : in STD LOGIC VECTOR (3 DOWNTO 0);
18
                    B : in STD LOGIC VECTOR (3 DOWNTO 0);
19
                    OP: in STD LOGIC VECTOR (3 DOWNTO 0); --SumRestNandXor (SRNX)
                    RTA: out STD LOGIC VECTOR (6 DOWNTO 0);
20
                    CN : out STD LOGIC VECTOR (1 DOWNTO 0);
21
22
                    ZERO : out STD LOGIC
23
24
         end ALU4Bit;
25
         architecture Behavioral of ALU4Bit is
26
27
            signal aux res : STD LOGIC VECTOR (4 DOWNTO 0) := "00000";
            signal aux sum : STD LOGIC VECTOR (4 DOWNTO 0) := "00000";
28
29
            signal aux rta : STD LOGIC VECTOR (3 DOWNTO 0) := "0000";
30
               signal aux display : STD LOGIC VECTOR (6 DOWNTO 0) := "0000000";
31
         begin
32
33
         --SUMA
34
            aux sum <= ('0'&A) + ('0'&B);
35
36
         --RESTA
37
            aux res \leq '0'&(A-B) when A > B else
38
                       '1'&(B-A) when A < B else
                       "00000";
39
40
         --XOR
41
         --NAND
42
43
         with OP select
44
            aux rta <= aux sum(3) & aux sum(2) & aux sum(1) & aux sum(0) when "1000",
45
                       aux res(3) & aux res(2) & aux res(1) & aux res(0) when "0100",
46
                       A xor B when "0010",
47
                       A nand B when "0001",
48
                       "0000" when others;
49
         --INDICADORES CNZ
50
         with OP select
51
            CN \le aux sum(4) & '0' when "1000",
52
                   '0' & aux res(4) when "0100",
                   "00" when others;
53
54
         with aux rta select
55
            ZERO <= '1' when "0000",</pre>
56
                    '0' when others;
57
         --DECODER
58
59
         -- segment encoinputg
60
         -- 0
61
                ___
```

```
-- 5 | 1
62
63
         -- <- 6
64
         -- 4 | 2
65
         --
66
               3
         --
67
68
        with aux rta select
69
            aux display <= "1111001" when "0001", --1</pre>
70
                          "0100100" when "0010",
                          "0110000" when "0011", --3
71
72
                          "0011001" when "0100",
                                                --4
                          "0010010" when "0101",
73
                                                 --5
74
                         "0000010" when "0110", --6
                         "1111000" when "0111", --7
75
76
                         "0000000" when "1000", --8
                         "0010000" when "1001",
77
                                                 --9
78
                         "0001000" when "1010",
                                                 --A
                         "0000011" when "1011",
79
                                                 --b
80
                         "1000110" when "1100",
                                                --C
                          "0100001" when "1101",
81
                                                 --d
82
                          "0000110" when "1110", --E
83
                          "0001110" when "1111", --F
                          "1000000" when others; --0
84
85
         RTA <= not aux_display;</pre>
86
        end Behavioral;
87
88
```