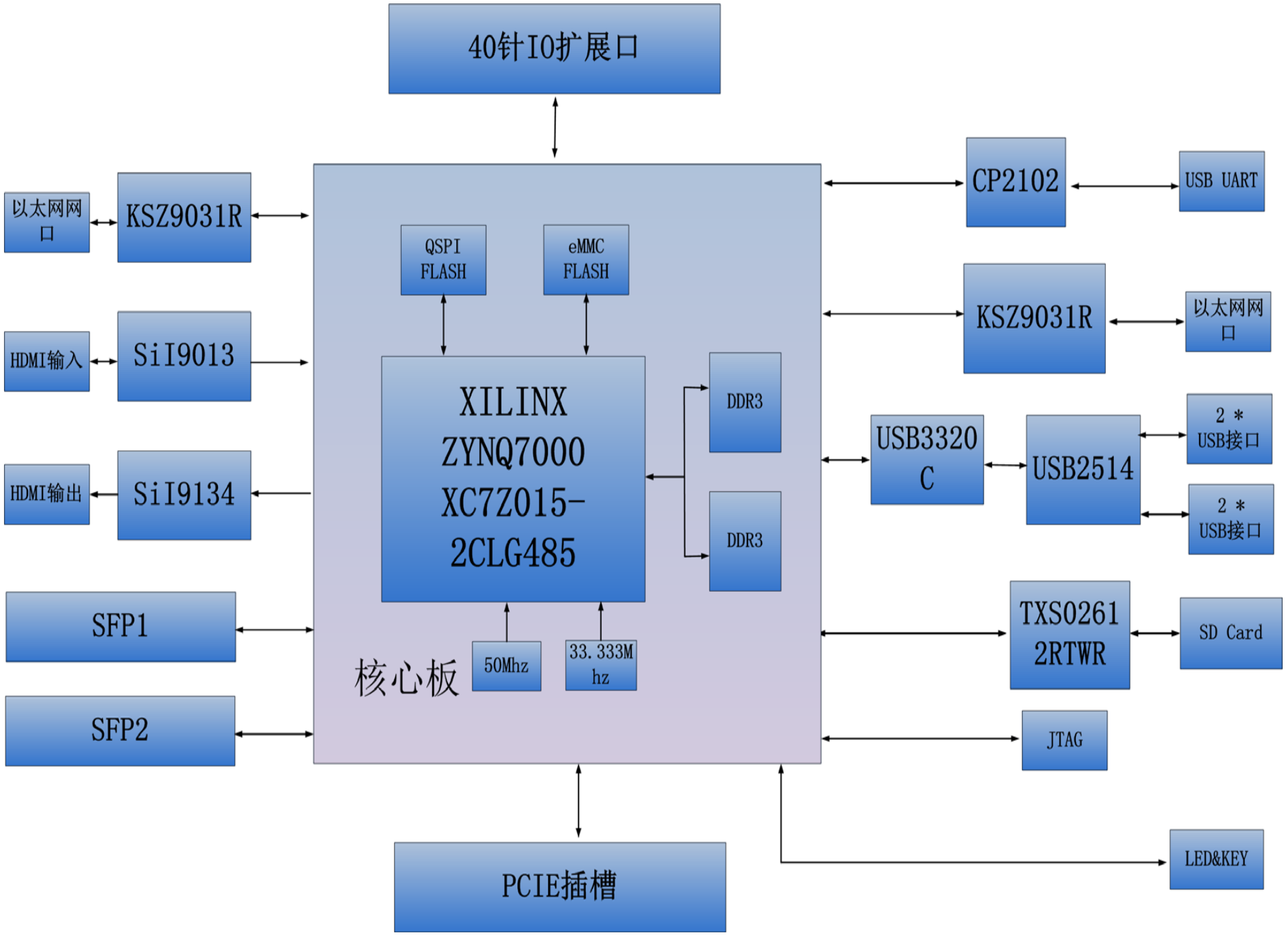

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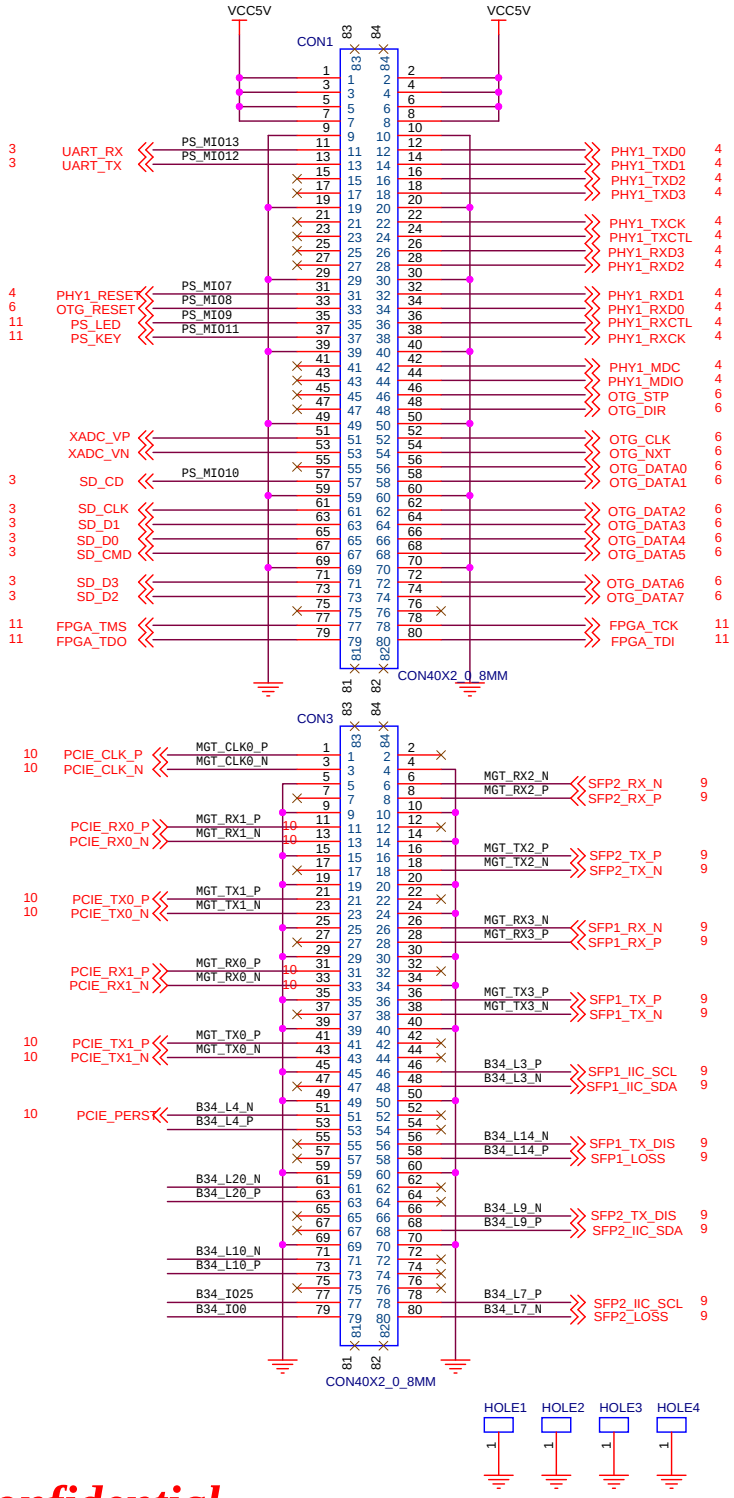
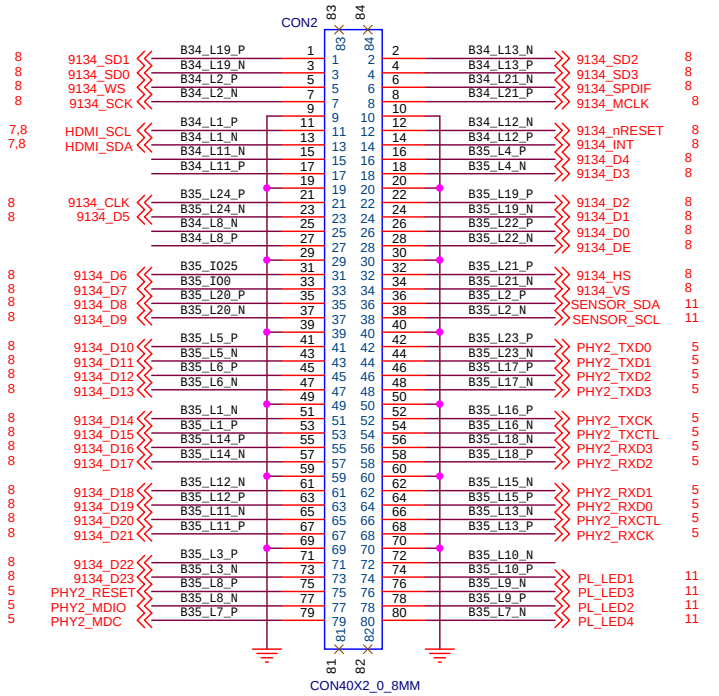
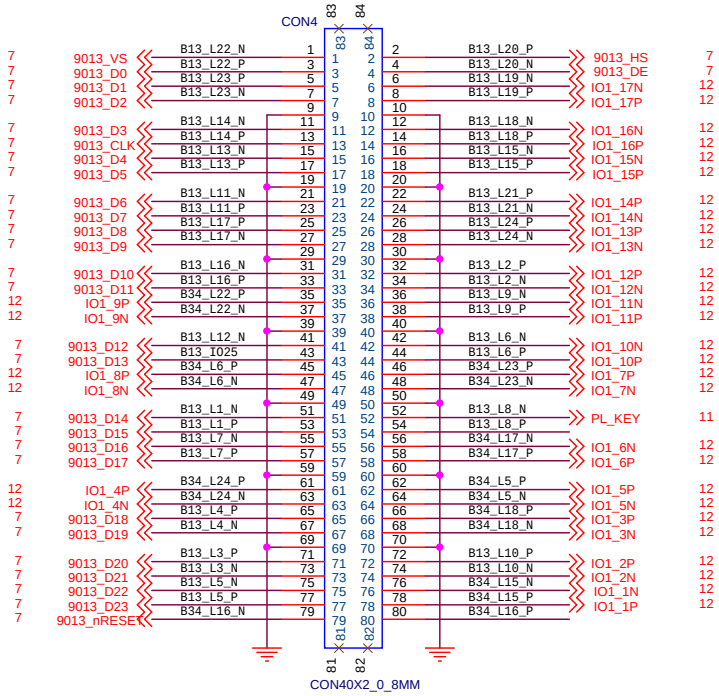
Title	PAGE01 Block Diagram	
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Wednesday, January 10, 2018	Sheet 1 of 13	







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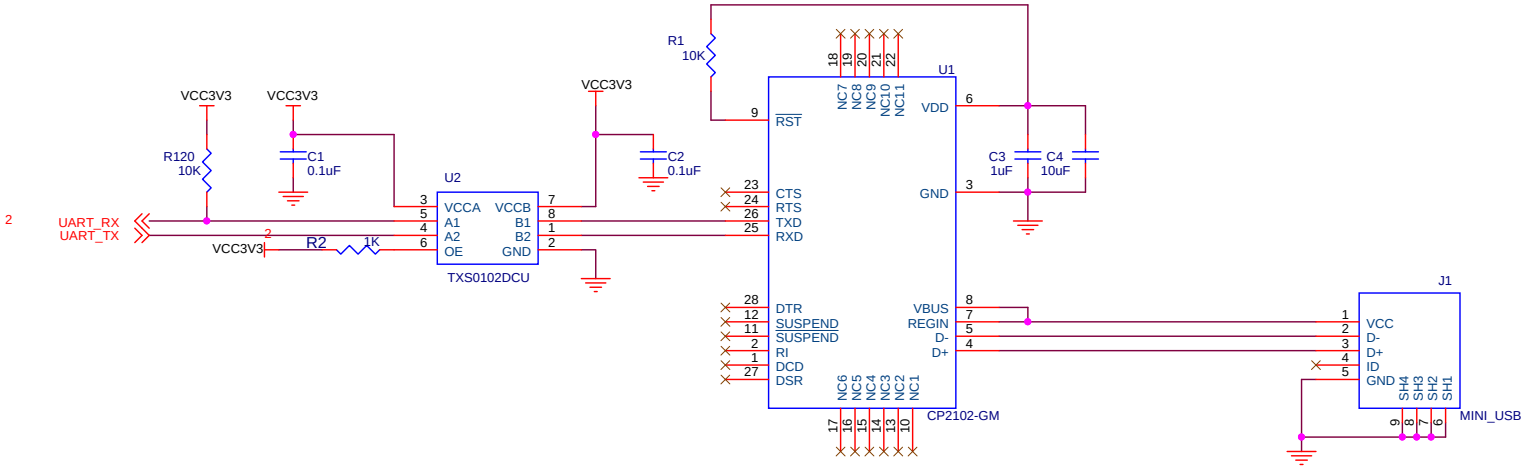
Title: PAGE02 Board Connector

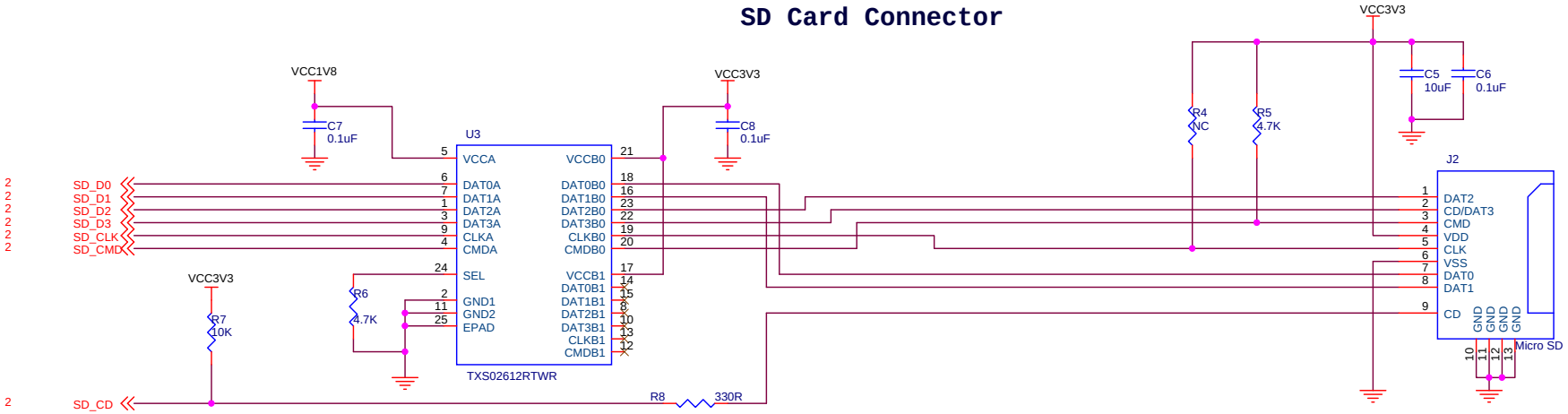
Size | Document Number: AX7015 开发板原理图 | Rev: 1.0

Date: Wednesday, January 10, 2018 | Sheet 2 of 13

USB Uart



SD Card Connector



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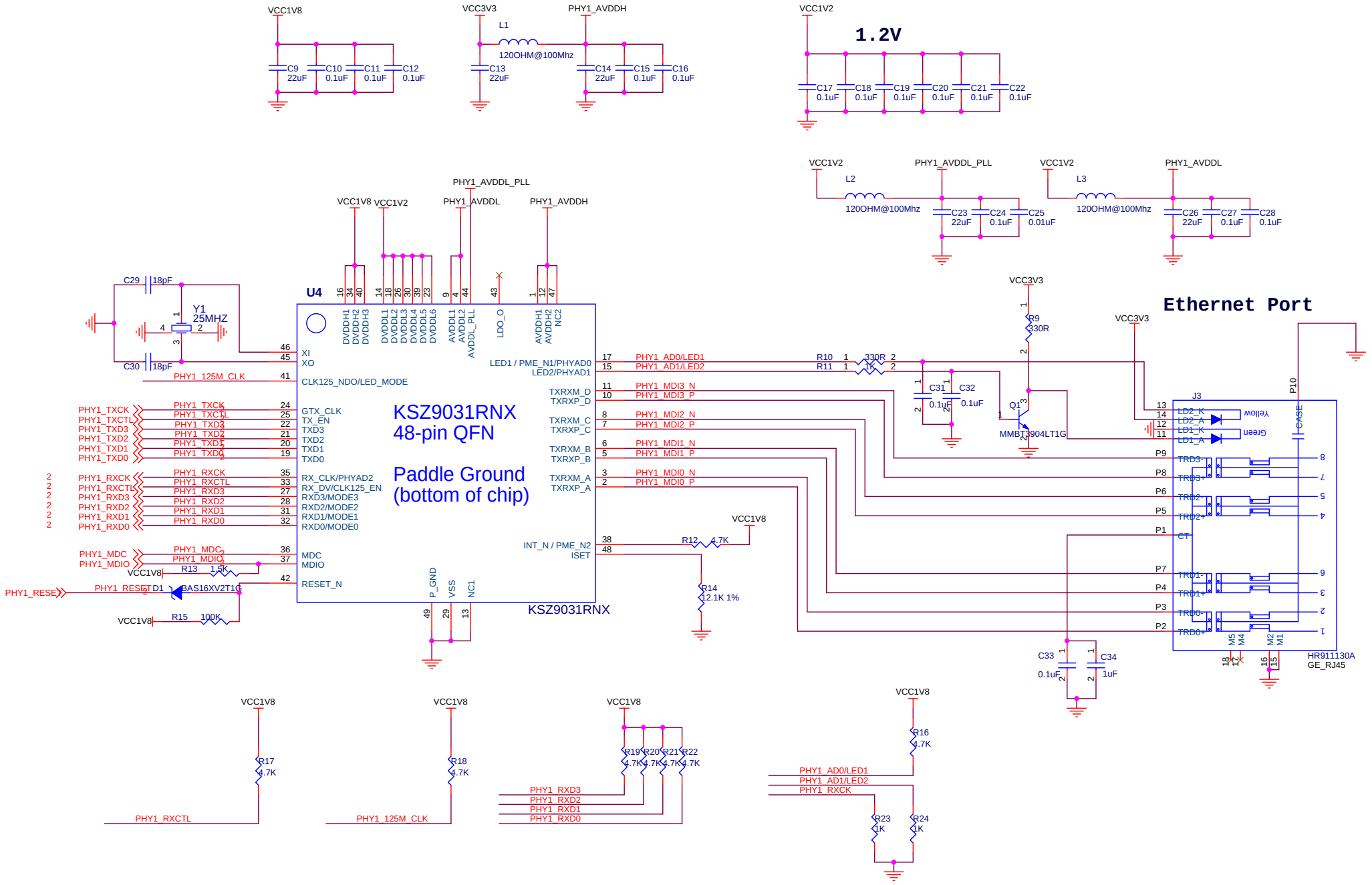
ALINX	Http://www.heijin.org
Title	PAGE03 SD Card and UART
Size	Document Number: AX7015 开发板原理图
Rev	1.0
Date:	Wednesday, January 10, 2018
Sheet	3 of 13



Enable 125Mhz Clock out Single-LED mode RGMII mode - Advertise all capabilities PHY Address is 001

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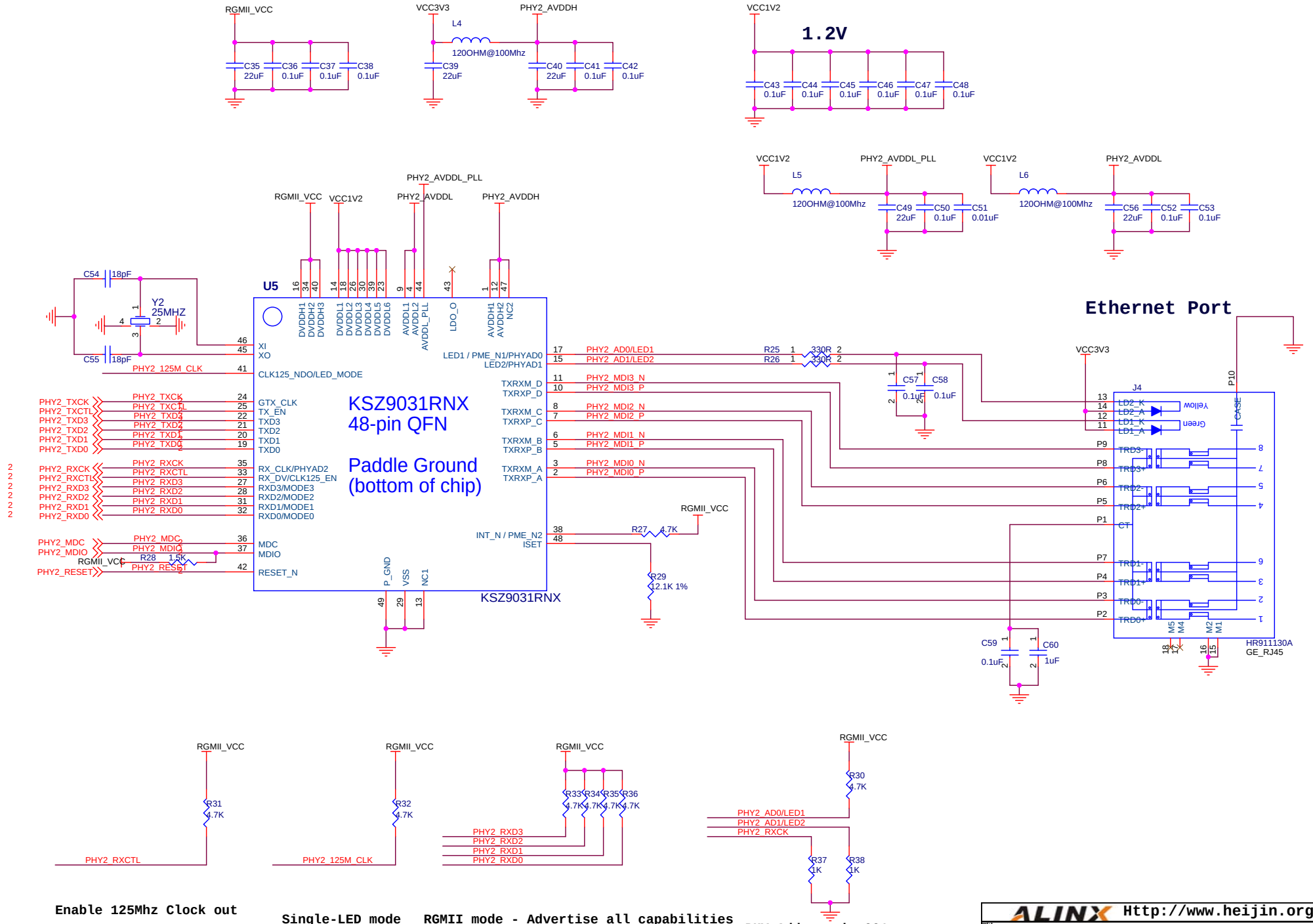
ALINX	Http://www.heijin.org
Title	PAGE04 PS Ethernet
Size	Document Number: AX7015 开发板原理图 — Rev: 1.0
Date:	Wednesday, January 10, 2018 — Sheet 4 of 13



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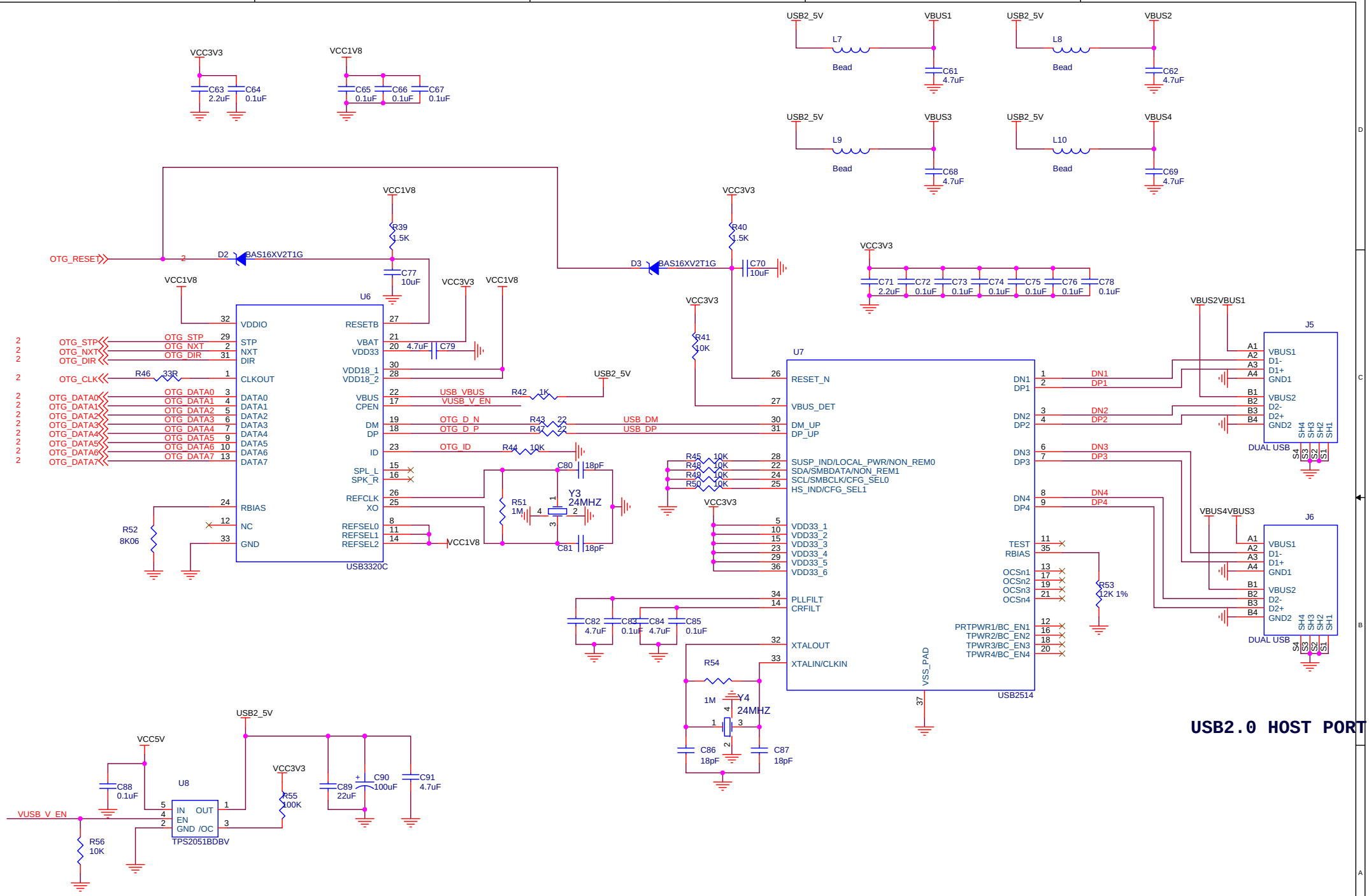
Title	PAGE05 PL Ethernet	
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Saturday, February 24, 2018	Sheet 5 of 13	



USB2.0 HOST PORT

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ALINX	Http://www.heijin.org	
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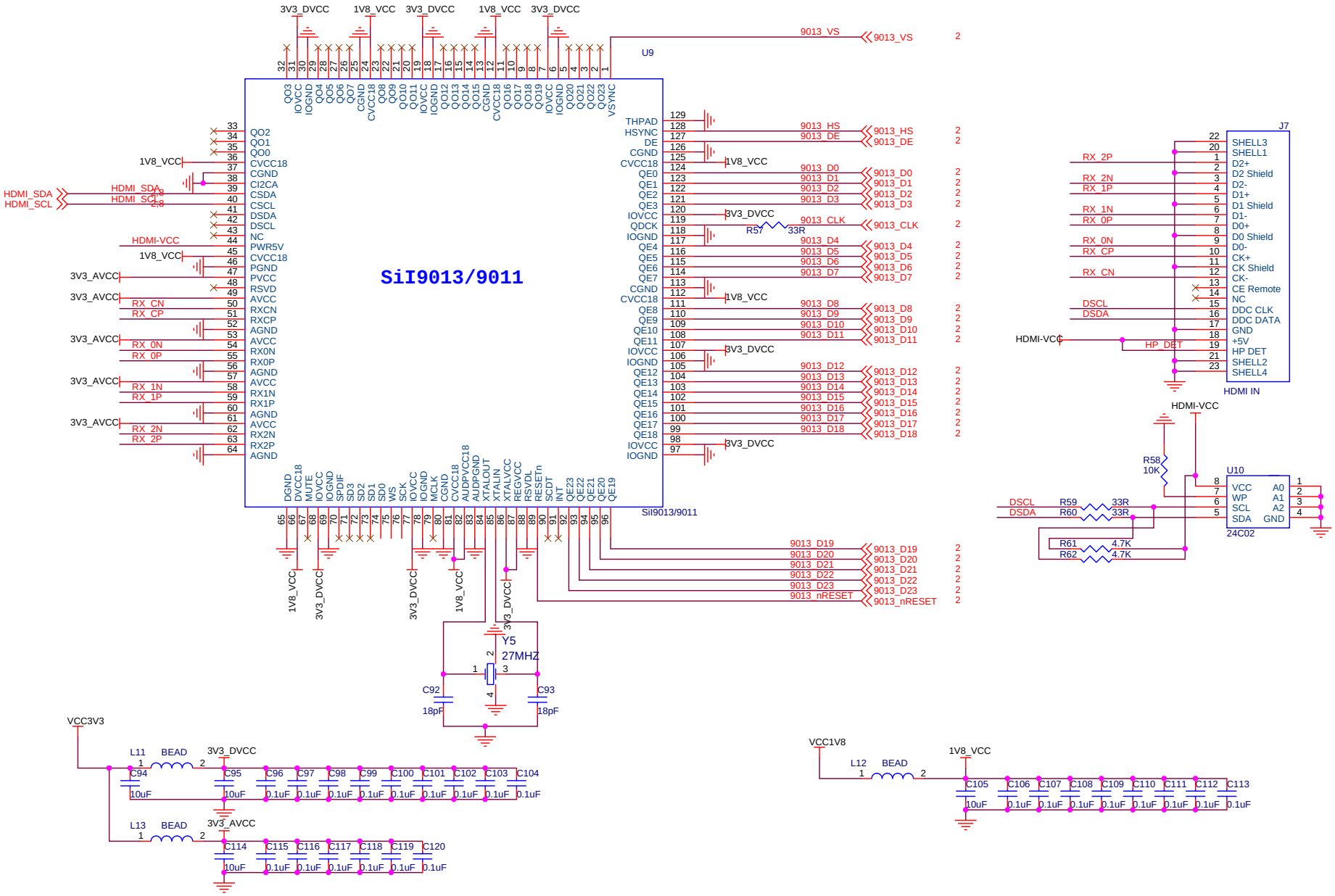
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Wednesday, January 10, 2018	Sheet 6 of 13	



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ALINX	Http://www.heijin.org	
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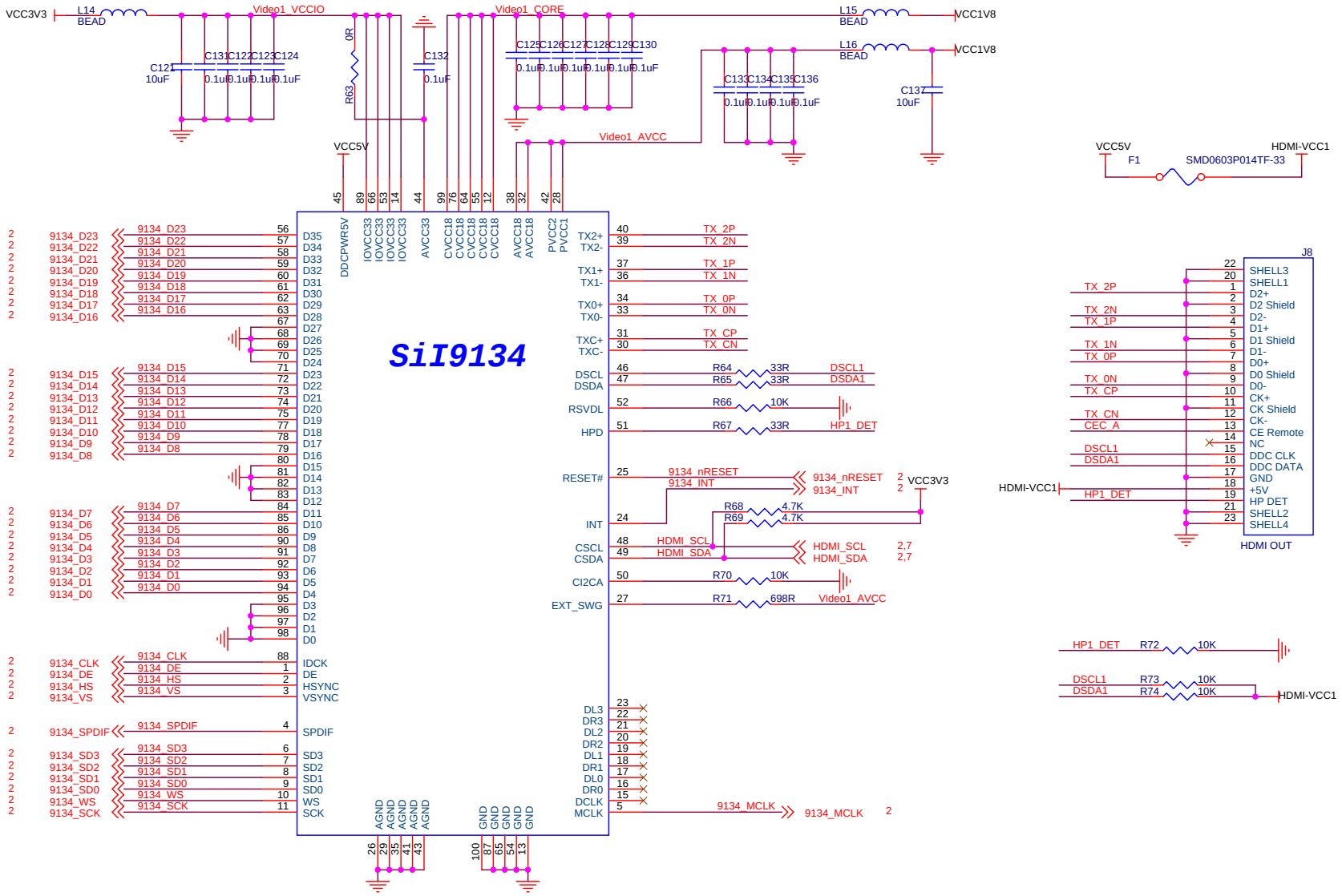
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Saturday, February 24, 2018	Sheet 7 of 13	



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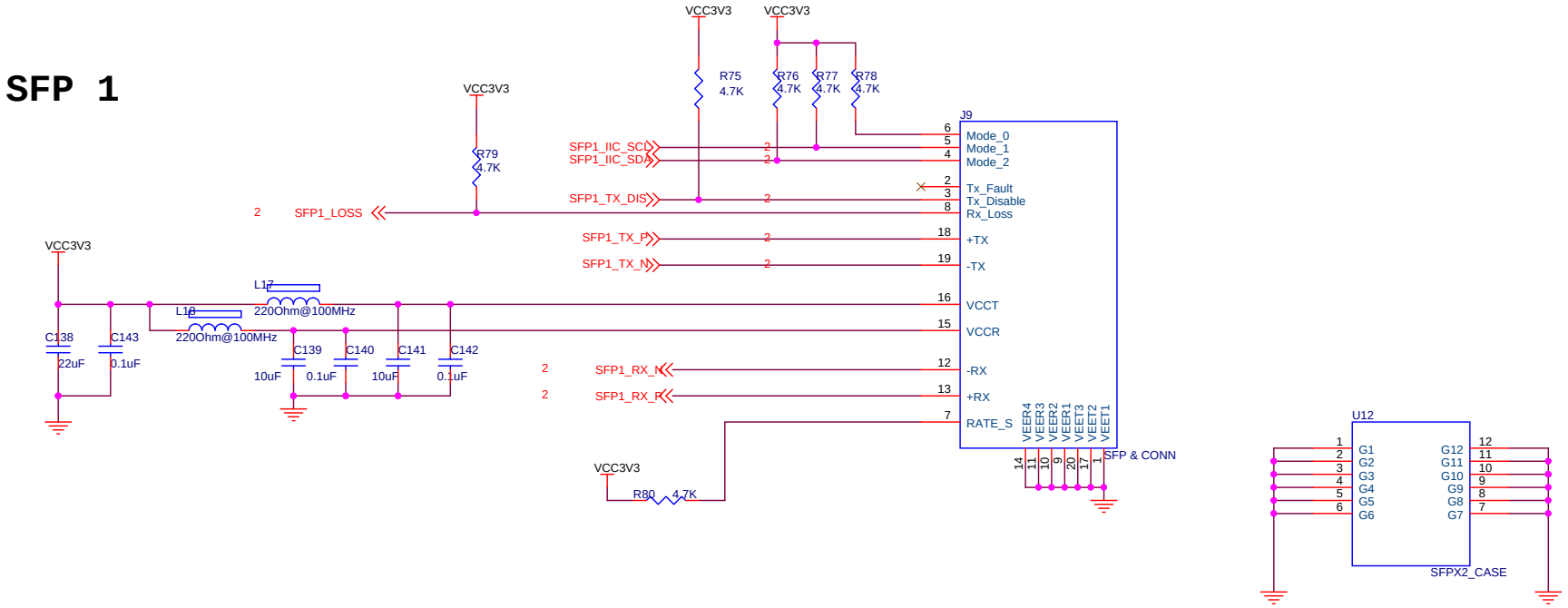
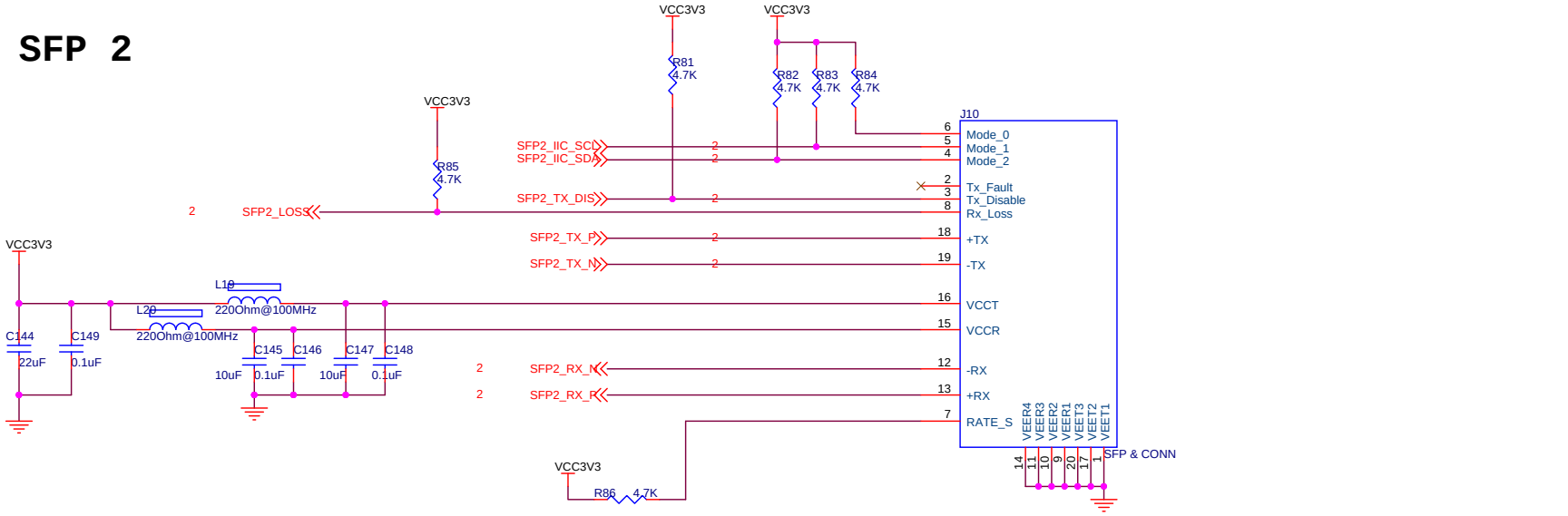
Title: PAGE08 HDMI OUT

Size	Document Number	Rev
	AX7015 开发板原理图	1.0

Date: Wednesday, January 10, 2018 Sheet 8 of 13





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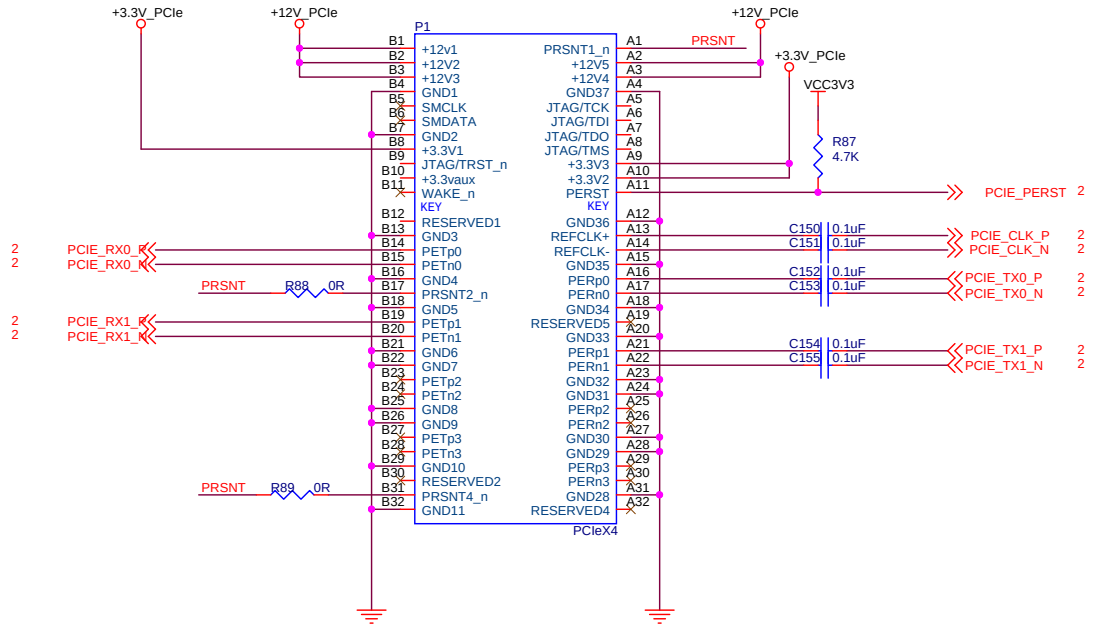
Title: PAGE09 SFP1 & SPF2

Size | Document Number: AX7015 开发板原理图 | Rev: 1.0

Date: Saturday, February 24, 2018 | Sheet 9 of 13

PCIE X2 SLOT



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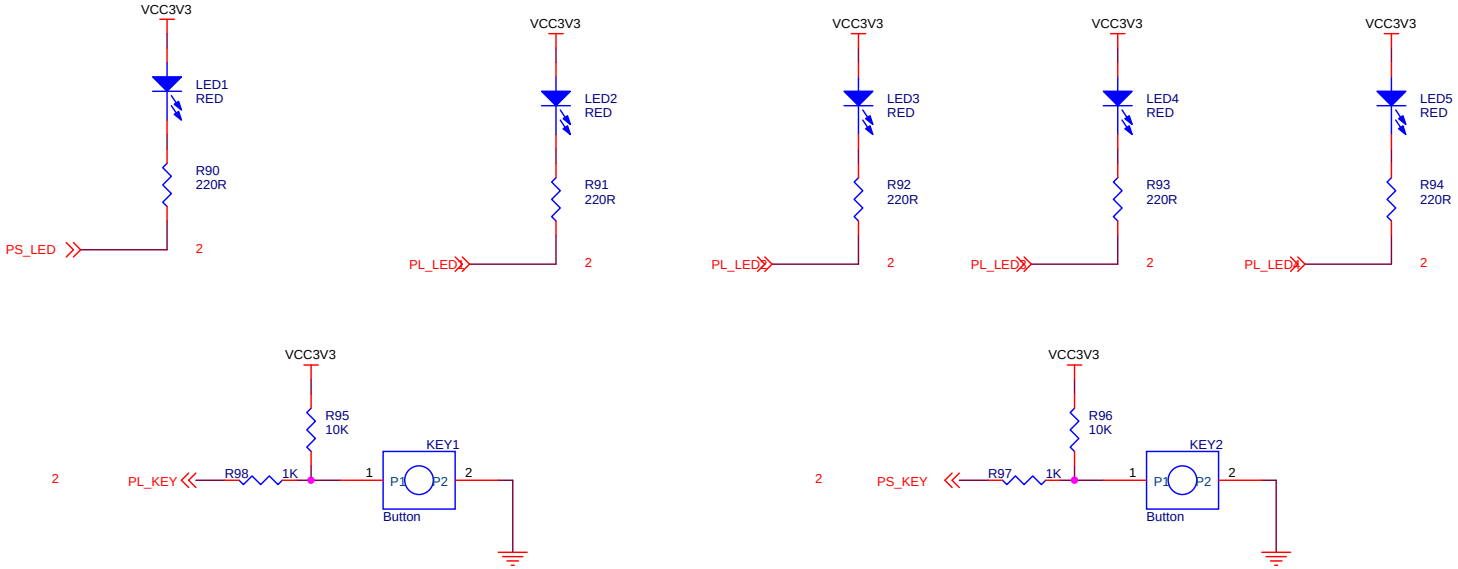
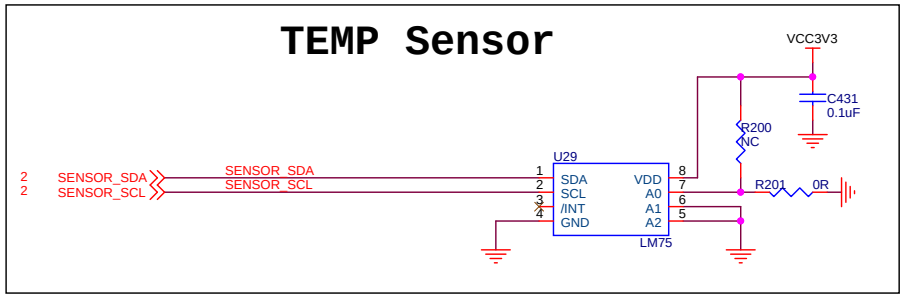
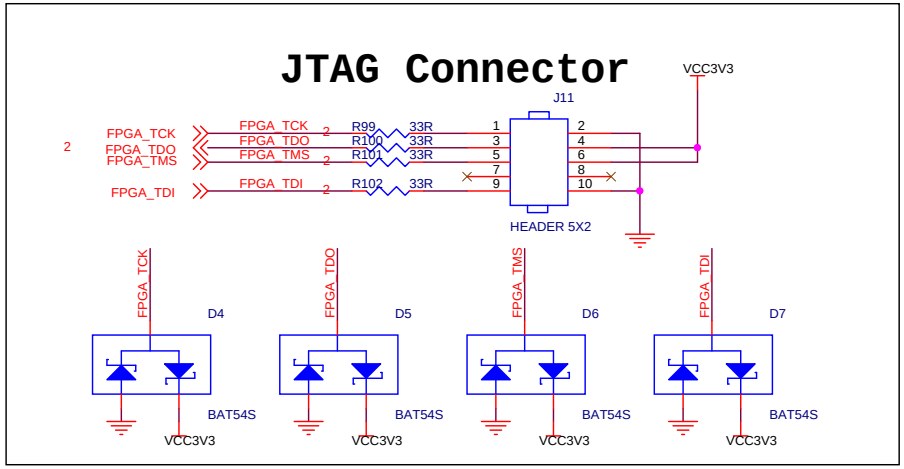
Title	PAGE10 PCIE	
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Saturday, February 24, 2018	Sheet 10 of 13	







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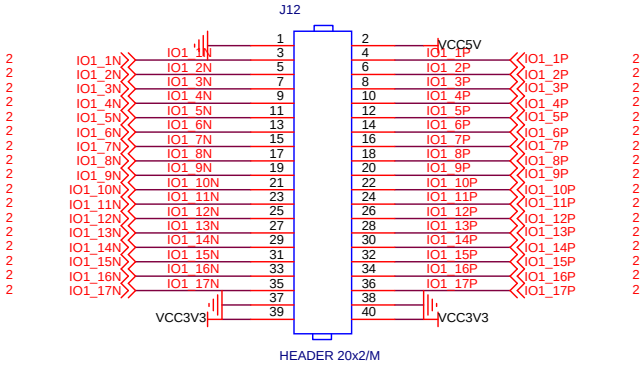
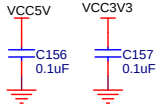
Title: PAGE11 KEY and LED

Size | Document Number: AX7015 开发板原理图 | Rev: 1.0

Date: Saturday, February 24, 2018 | Sheet 11 of 13

FPGA 40 PIN External IO





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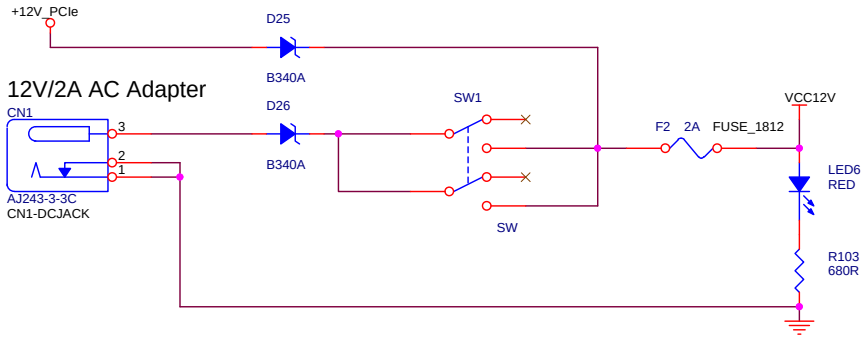
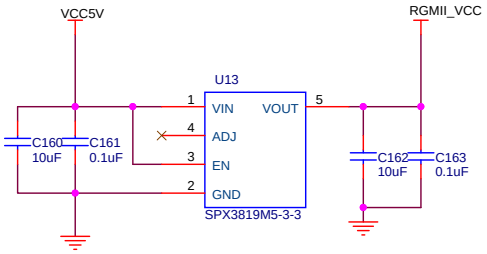
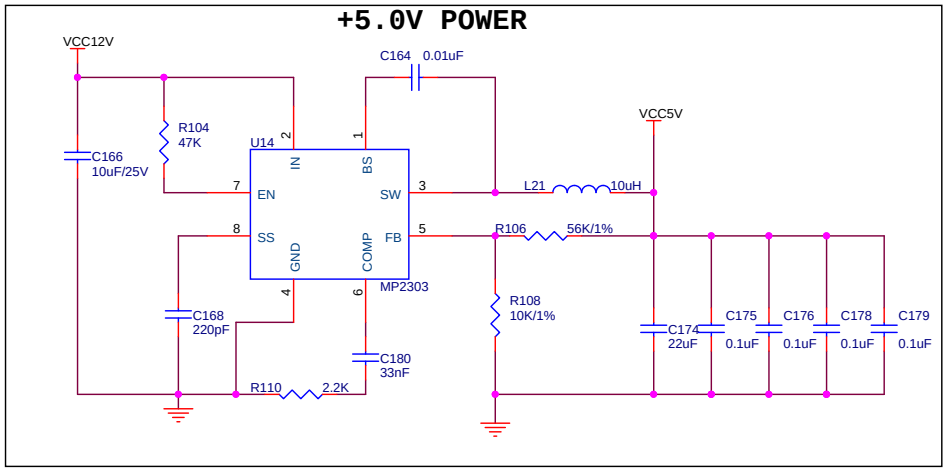
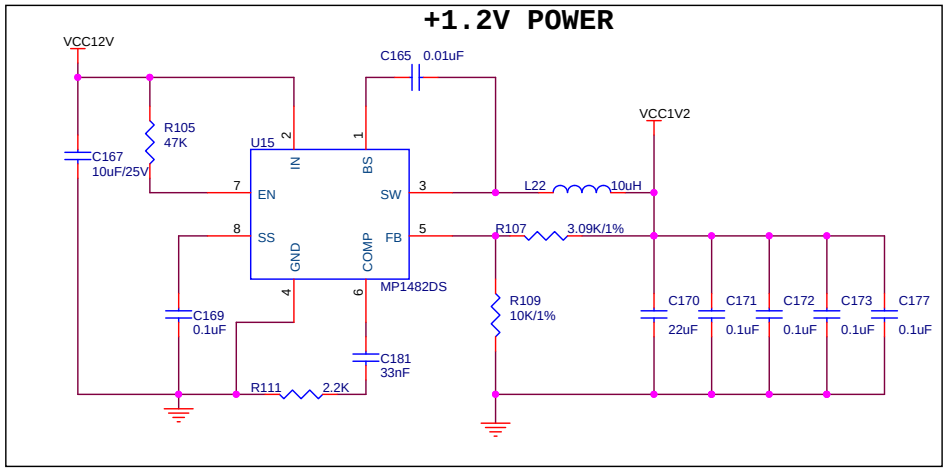
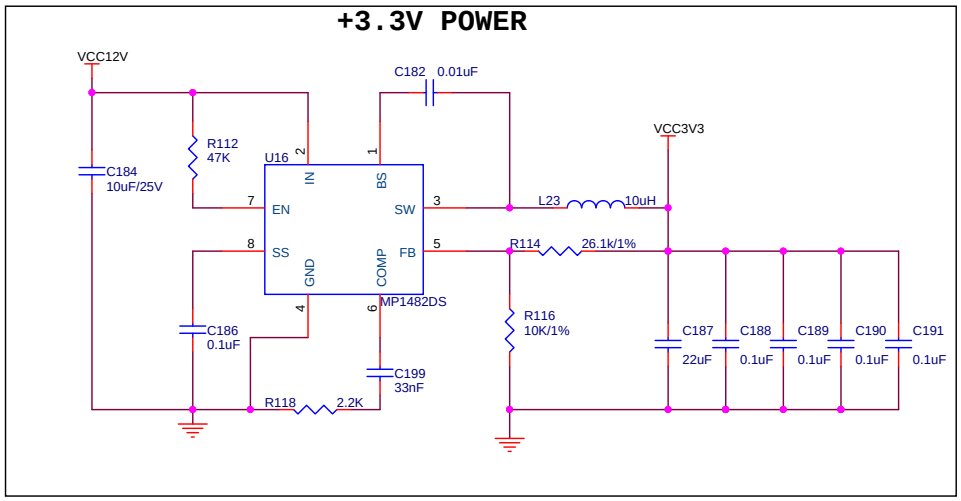
ALINX	Http://www.heijin.org	
Title: PAGE12 EX Connector		
Size	Document Number: AX7015 开发板原理图	Rev: 1.0
Date: Wednesday, January 10, 2018	Sheet 12 of 13	

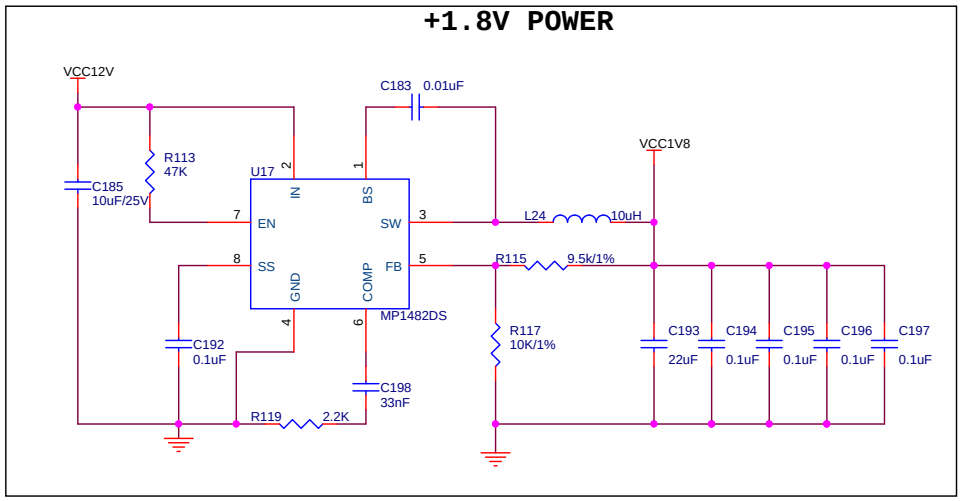












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Title: PAGE13 Power

Size | Document Number: AX7015 开发板原理图 | Rev: 1.0

Date: Saturday, February 24, 2018 | Sheet 13 of 13