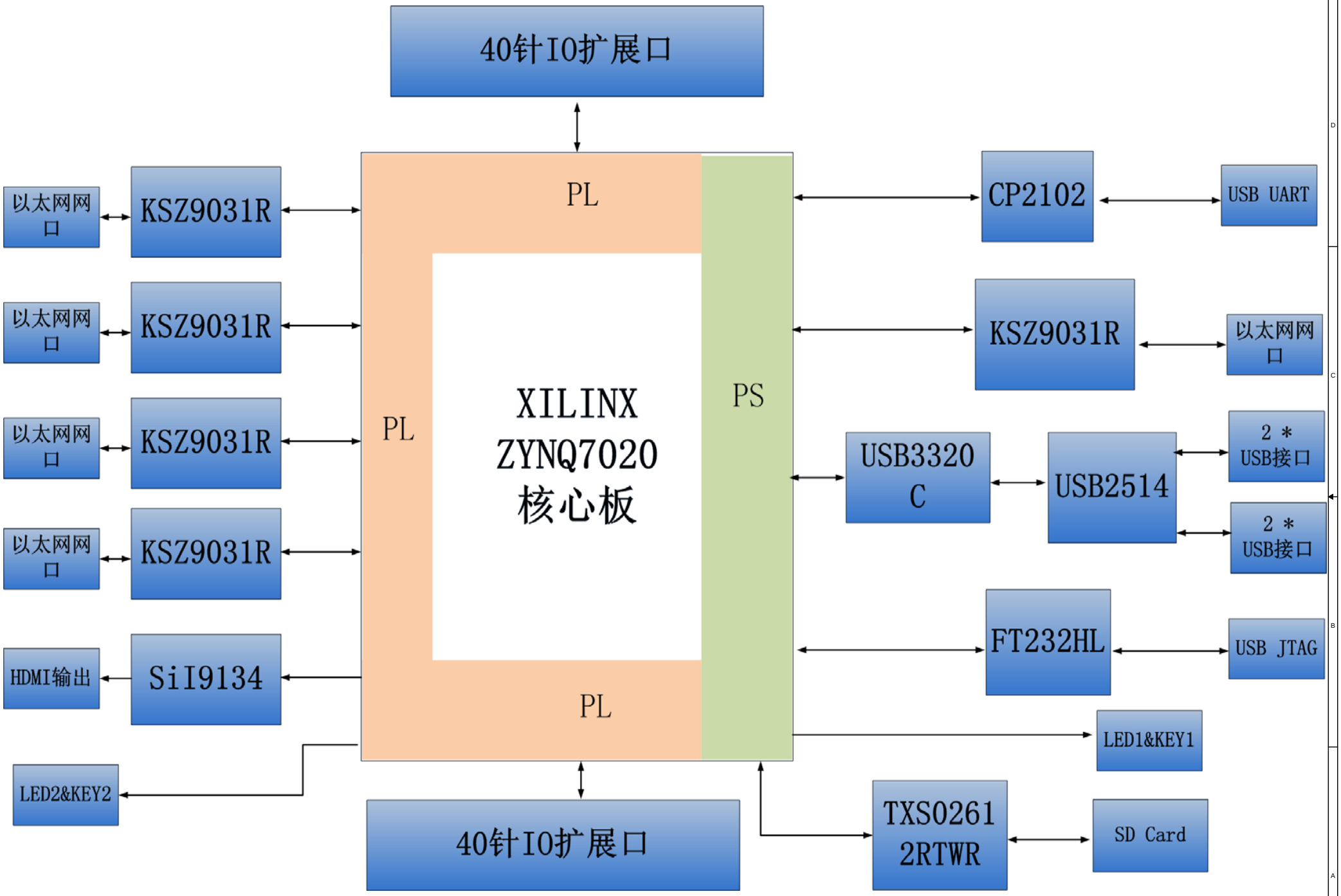

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Title	PAGE01 Block Diagram	
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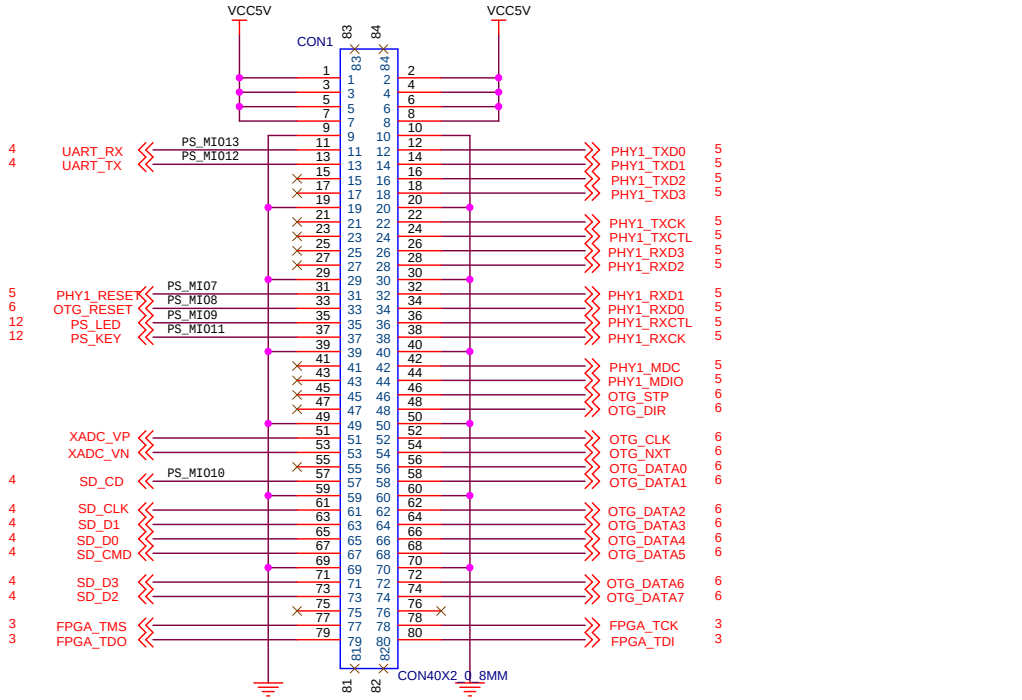

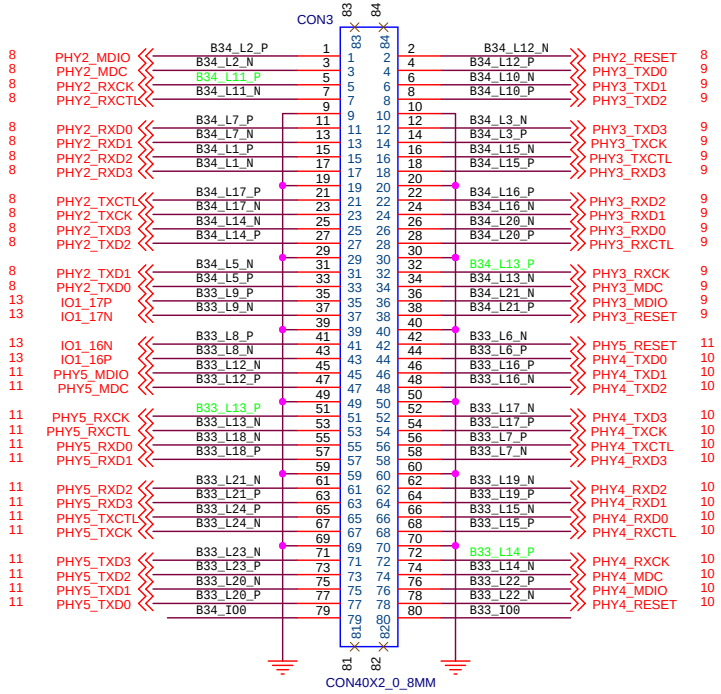

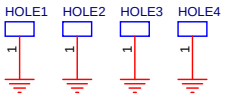

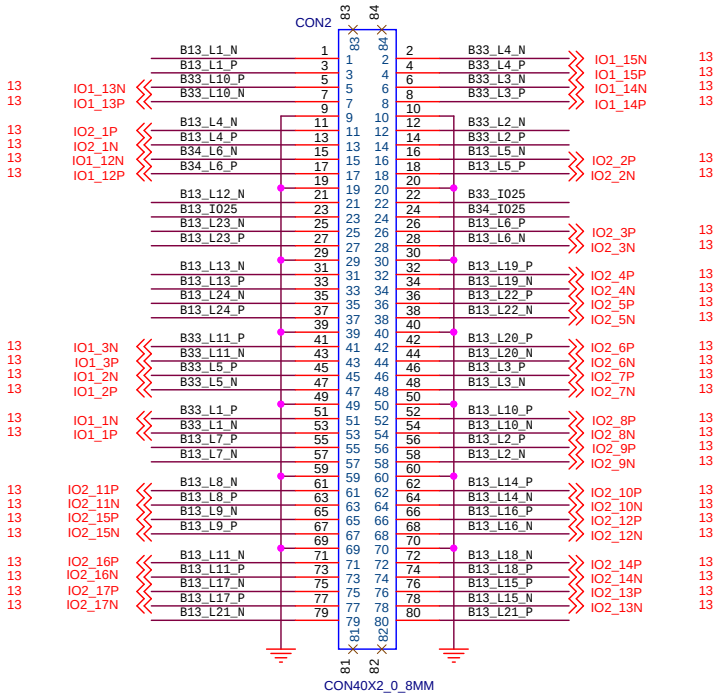

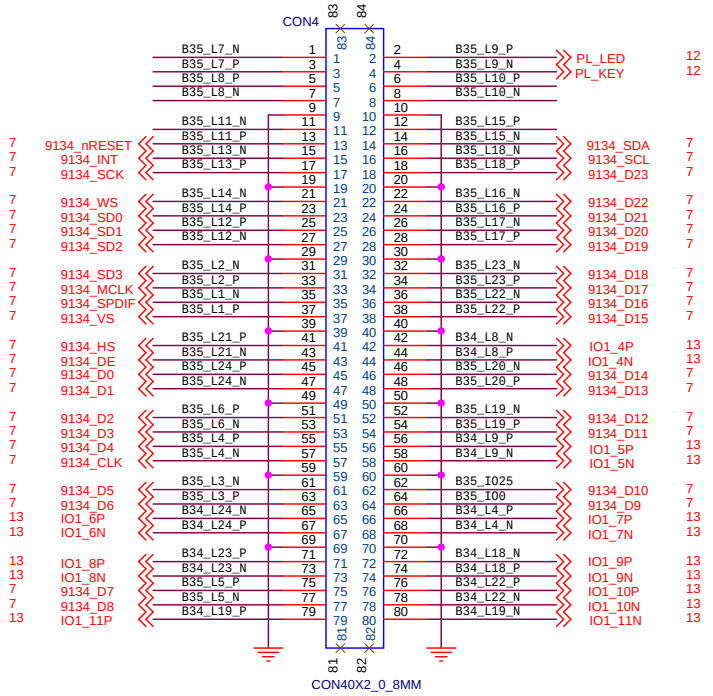

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Title	PAGE02 Board Connector
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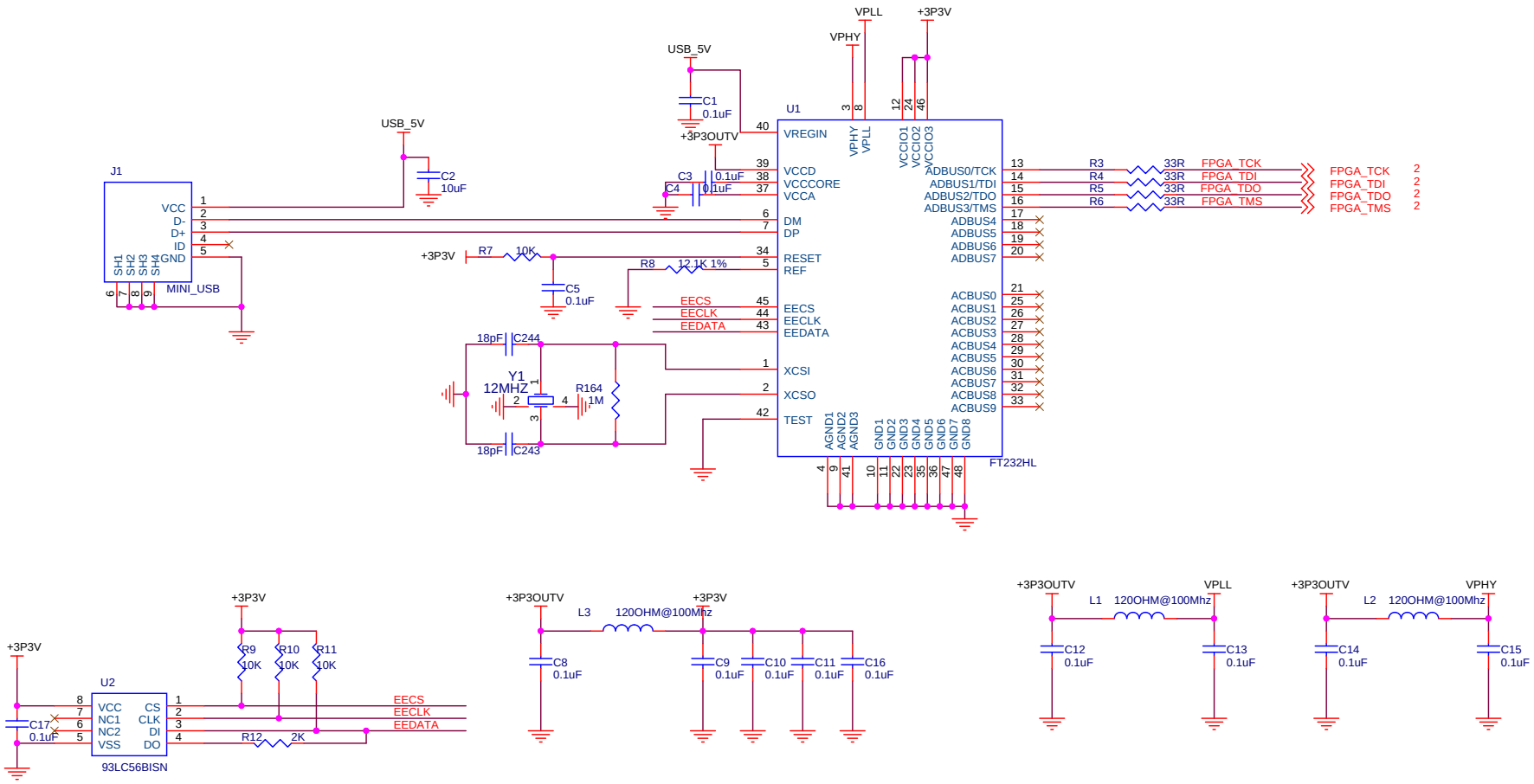

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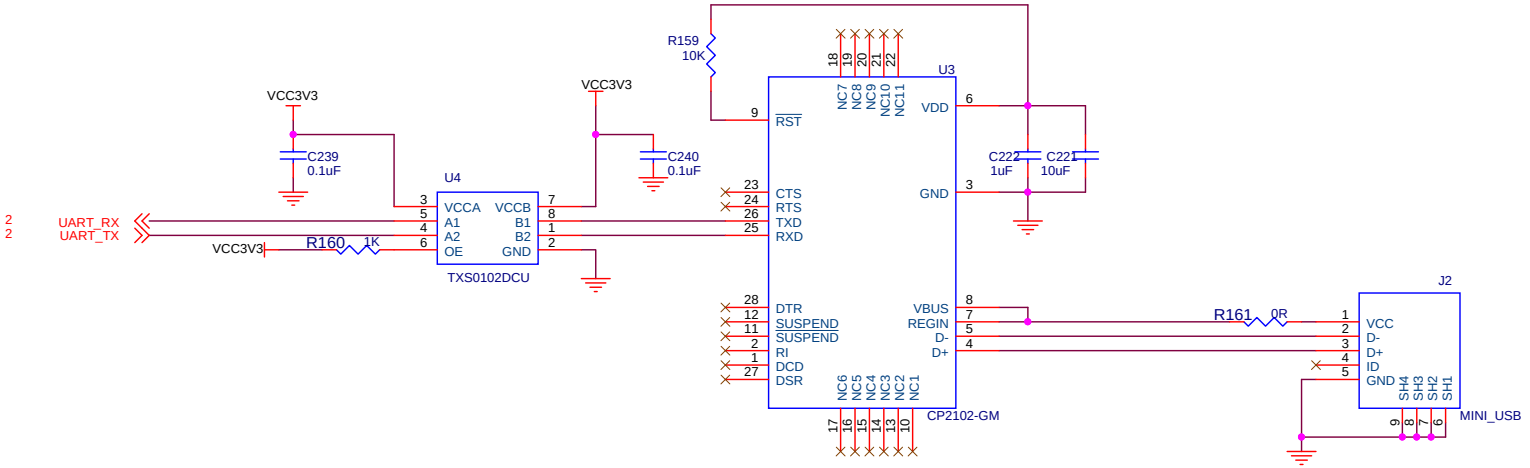
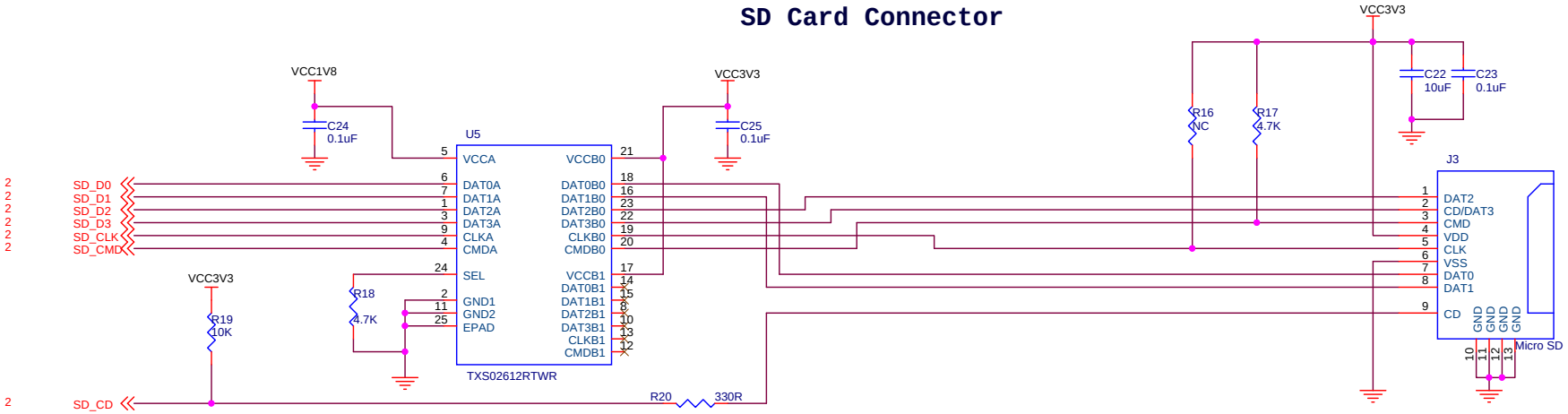
USB Uart



SD Card Connector



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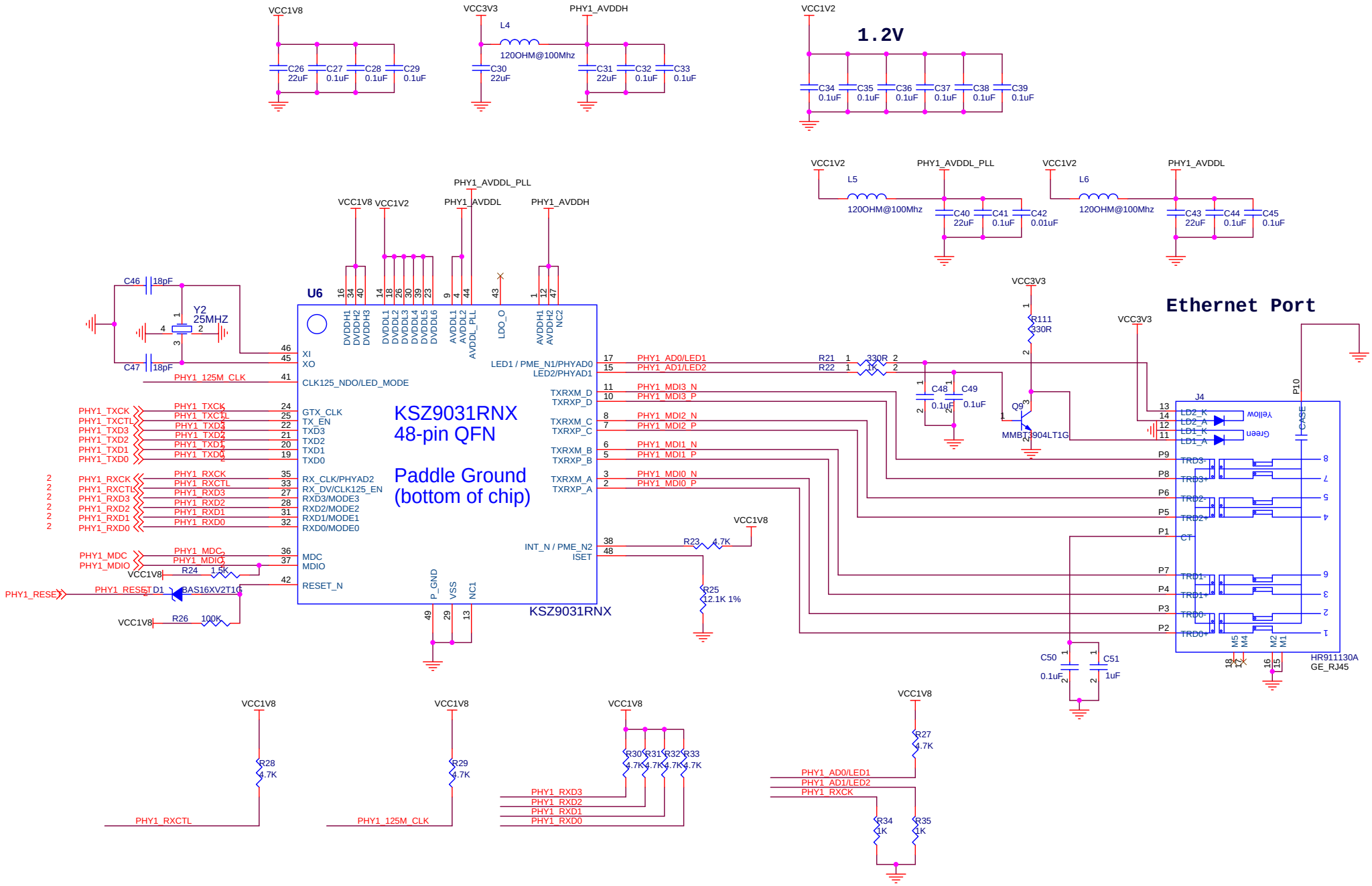

Enable 125Mhz Clock out Single-LED mode RGMII mode - Advertise all capabilities PHY Address is 001

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Title	PAGE05 PS Ethernet1
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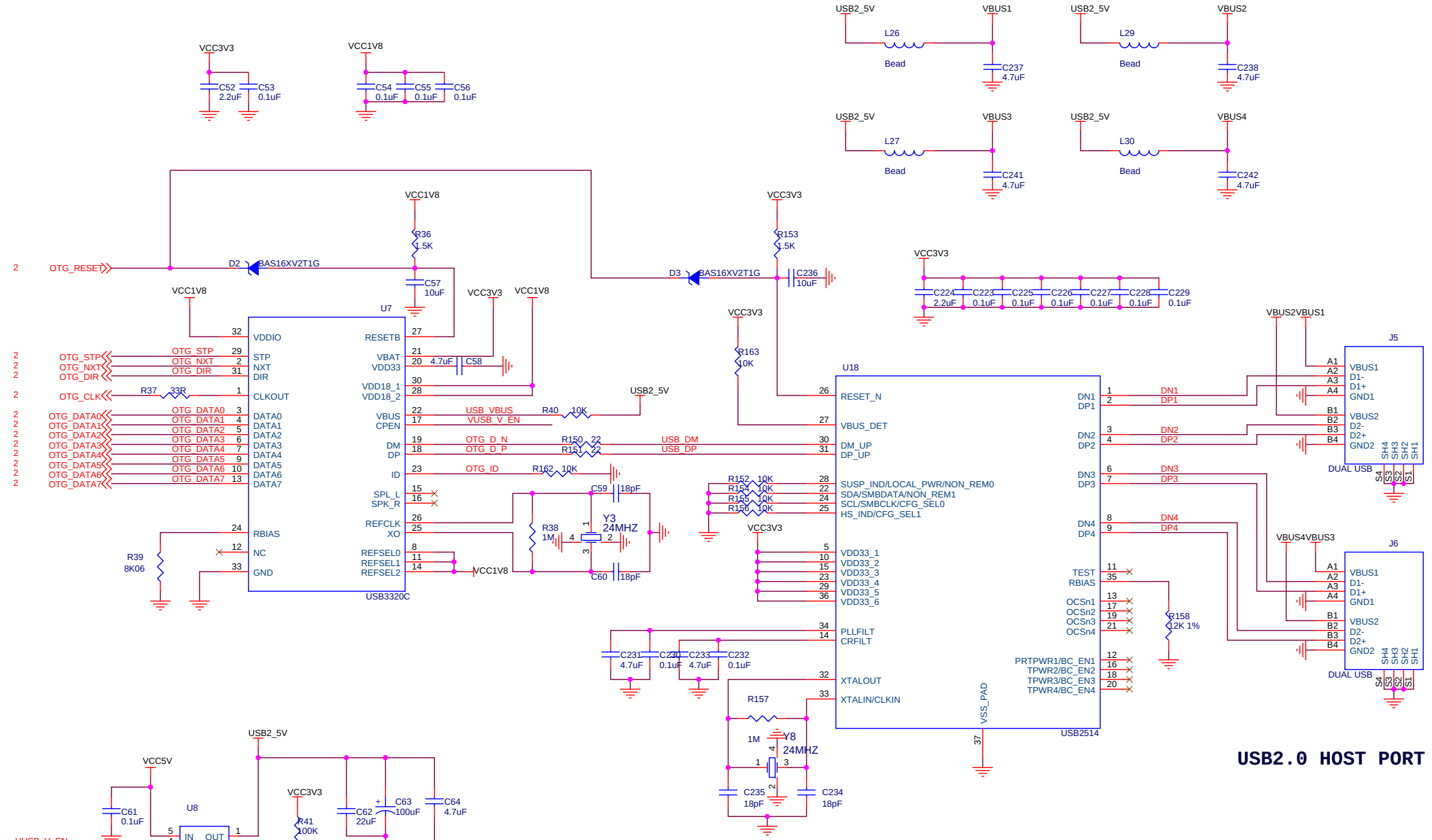

USB2.0 HOST PORT

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Title	PAGE06 HOST USBX4
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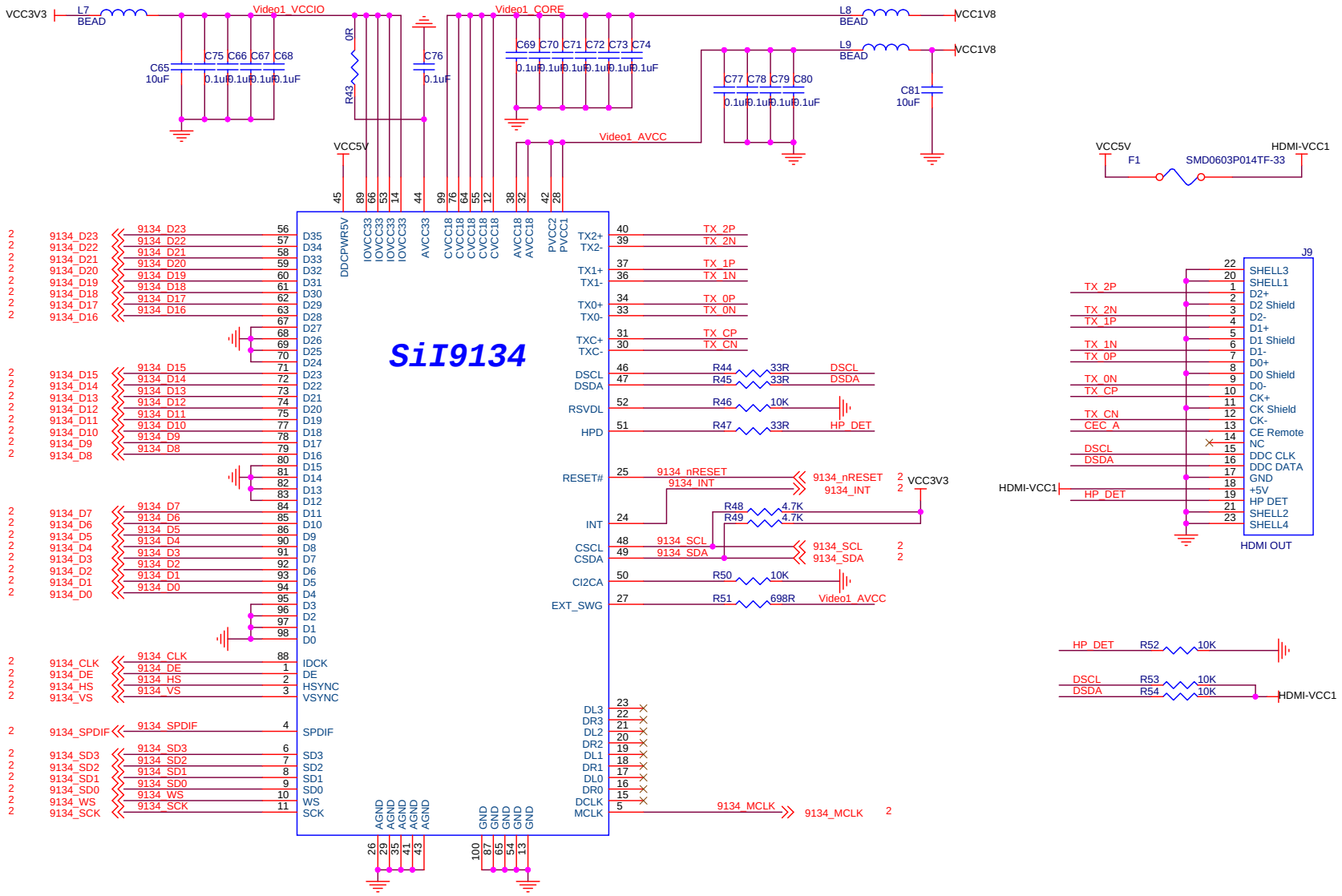

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Title: PAGE07 HDMI OUT

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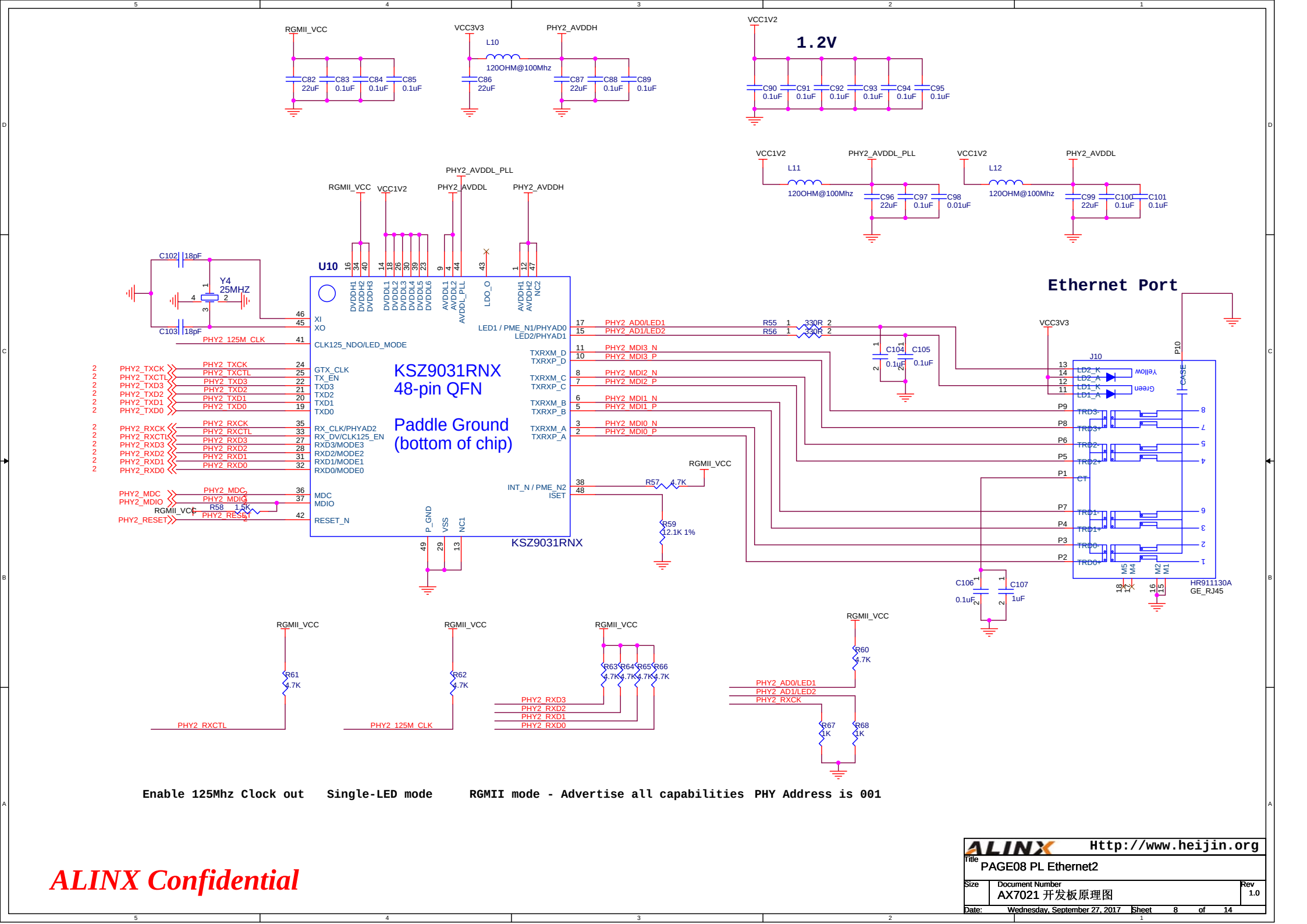

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Title	PAGE08 PL Ethernet2	
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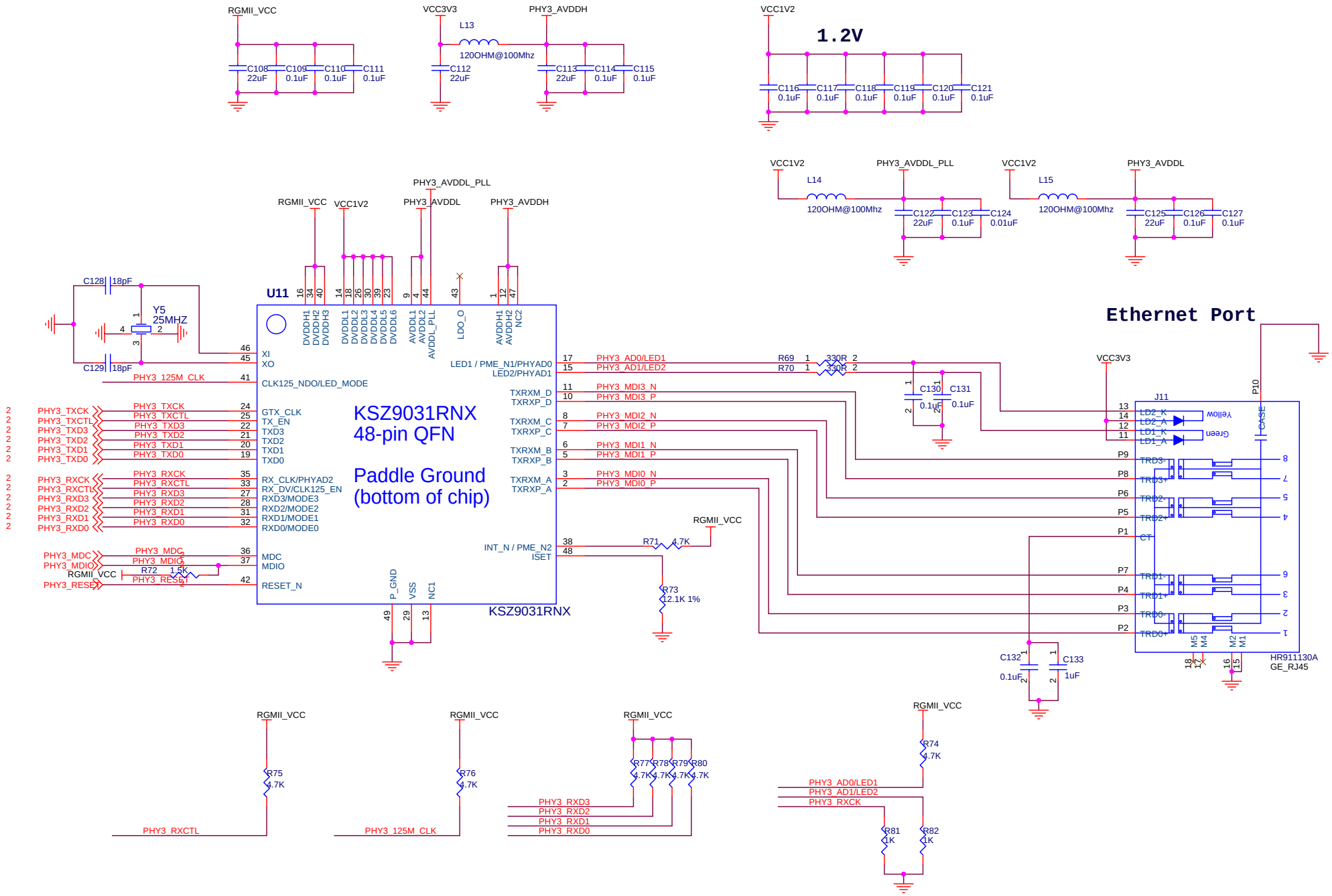

Enable 125Mhz Clock out Single-LED mode RGMII mode - Advertise all capabilities PHY Address is 001

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Title	PAGE09 PL Ethernet3
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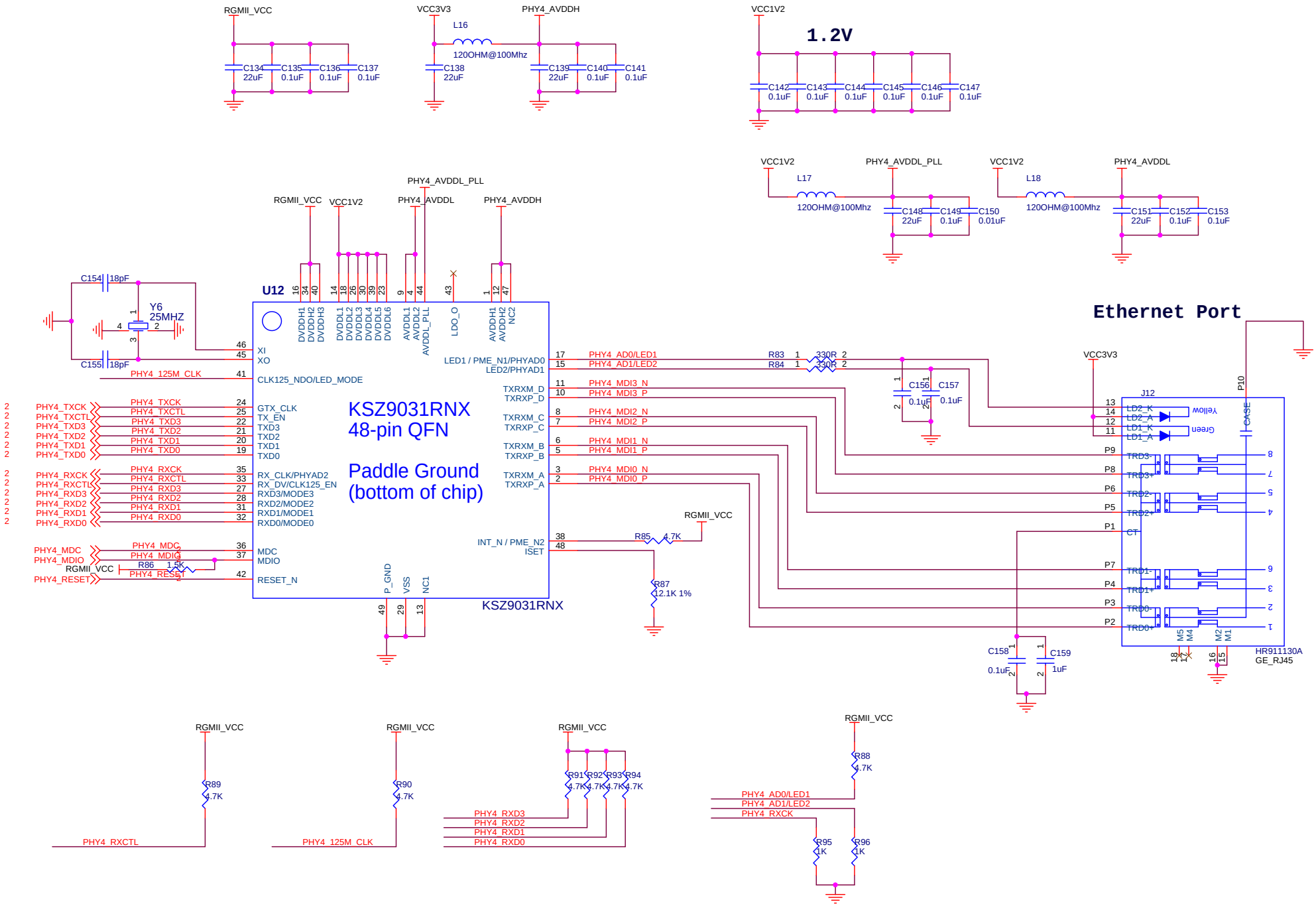

Enable 125Mhz Clock out Single-LED mode RGMII mode - Advertise all capabilities PHY Address is 001

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Title	PAGE10 PL Ethernet4
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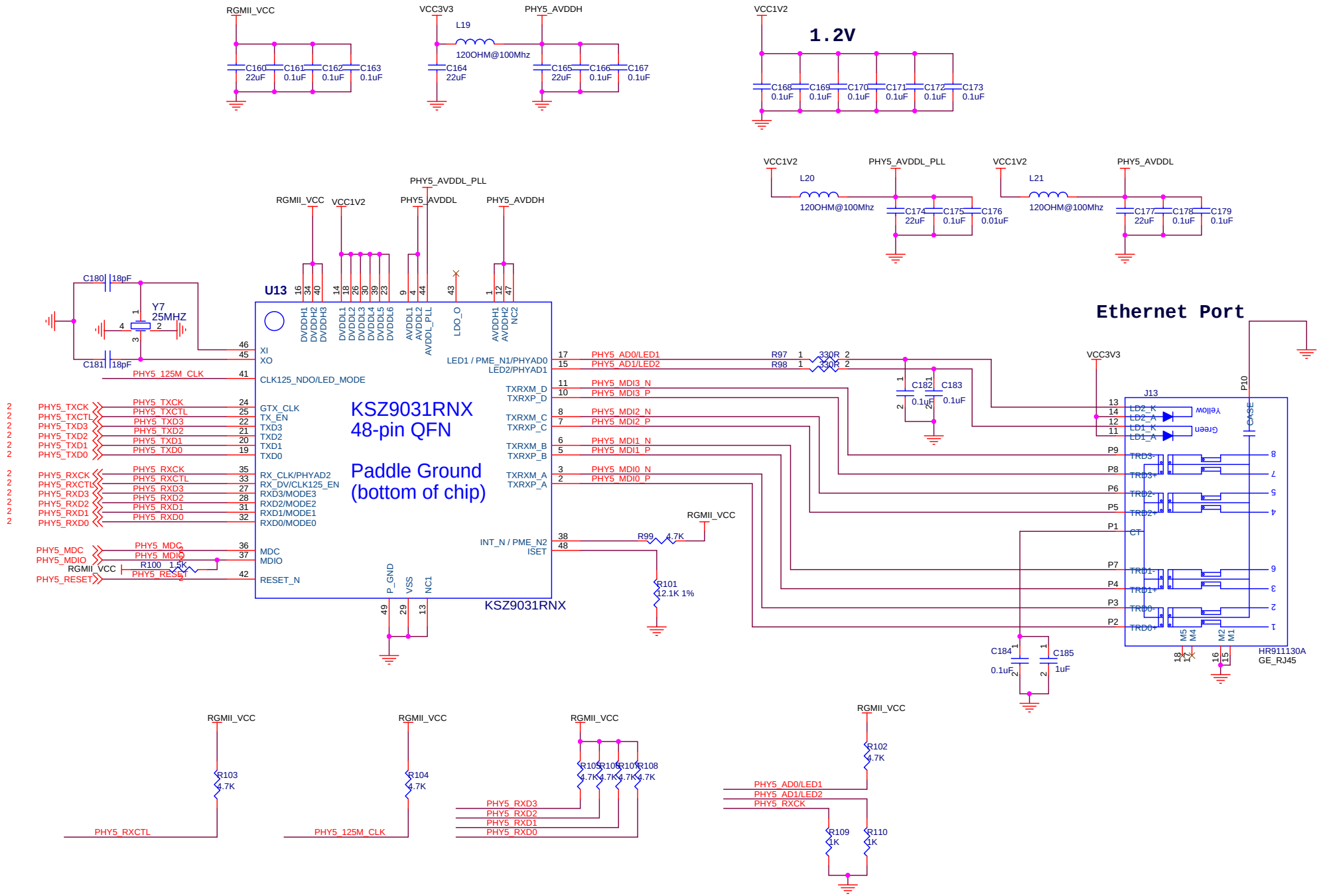

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Title	PAGE11 PL Ethernet5
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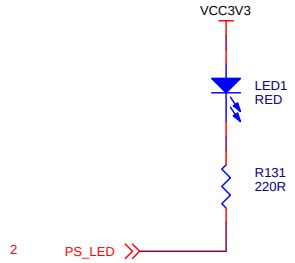

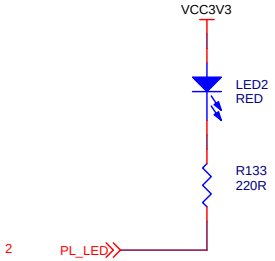

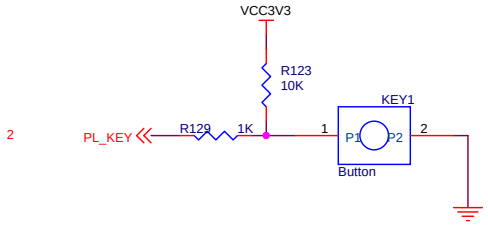

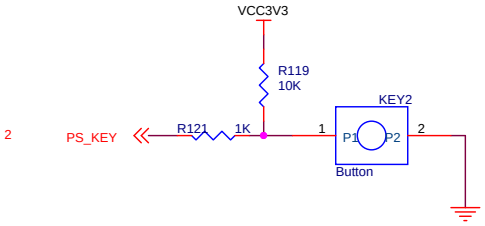

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Title: PAGE12 KEY and LED		
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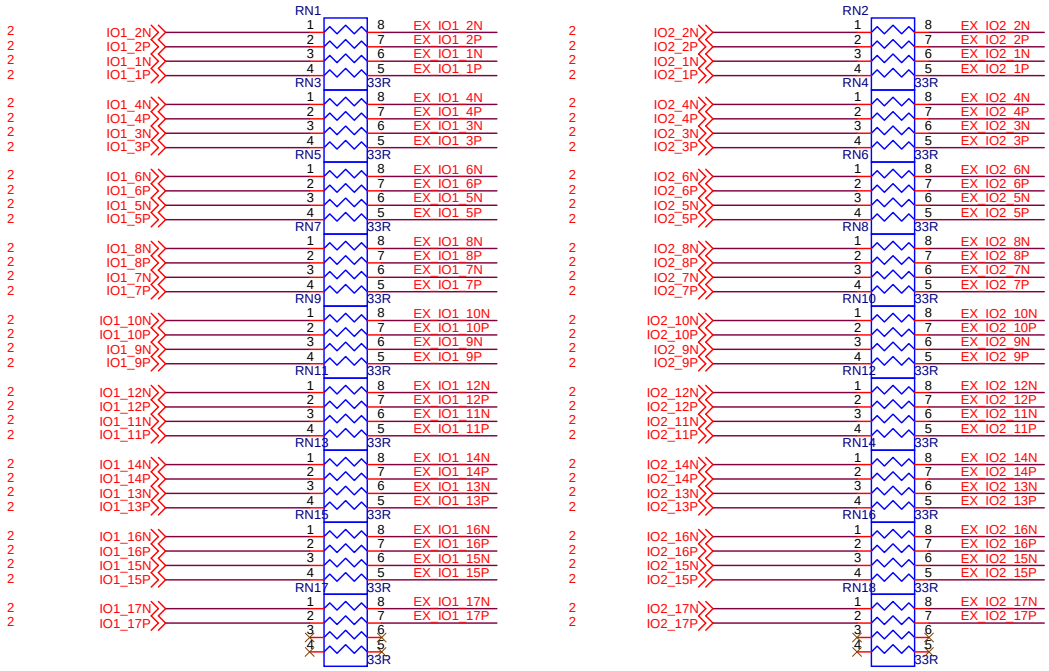
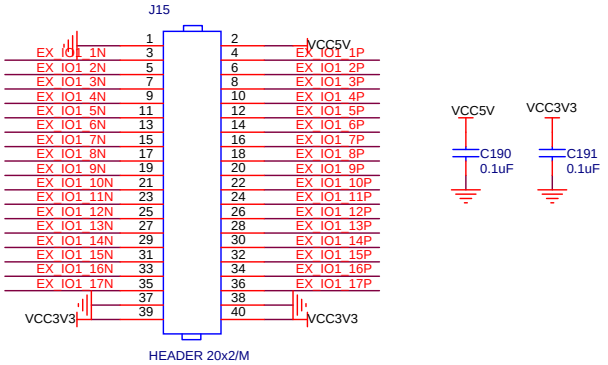
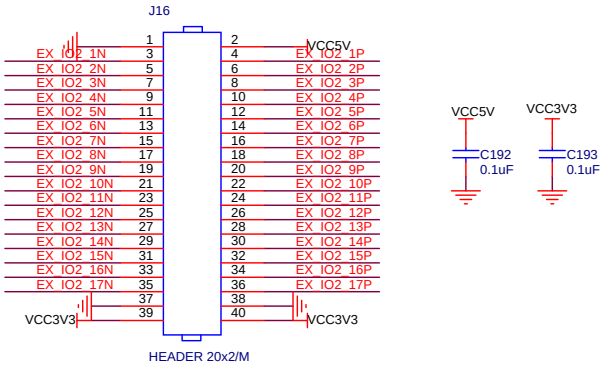


FPGA 40 PIN External IO



FPGA 40 PIN External IO



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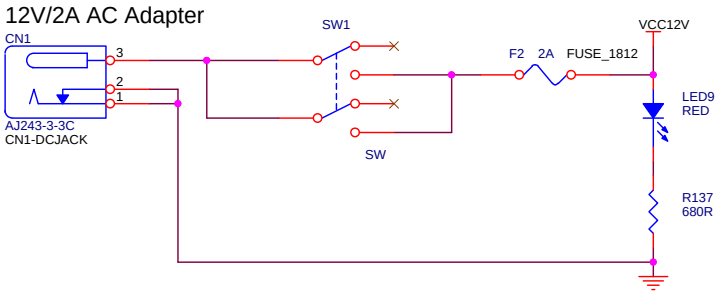

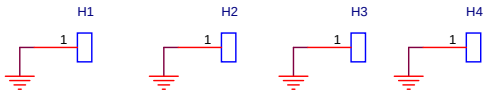

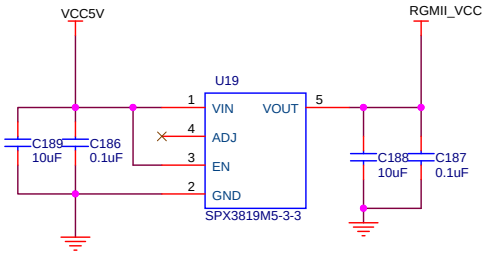

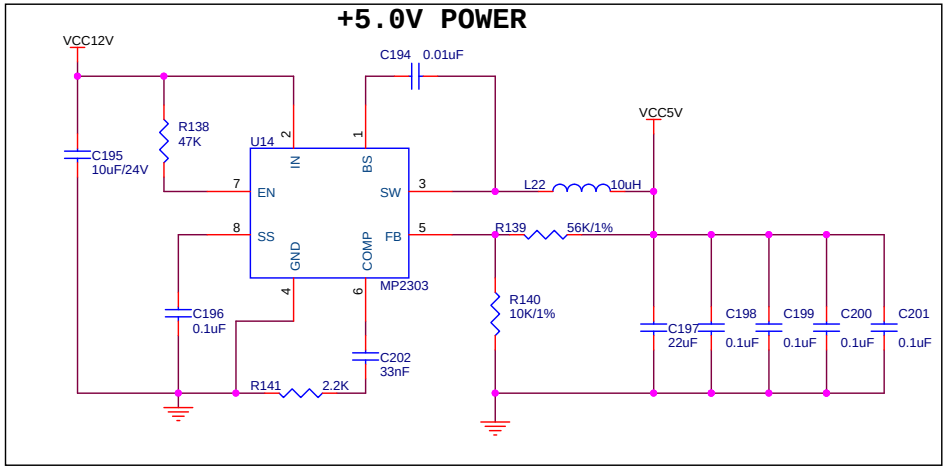

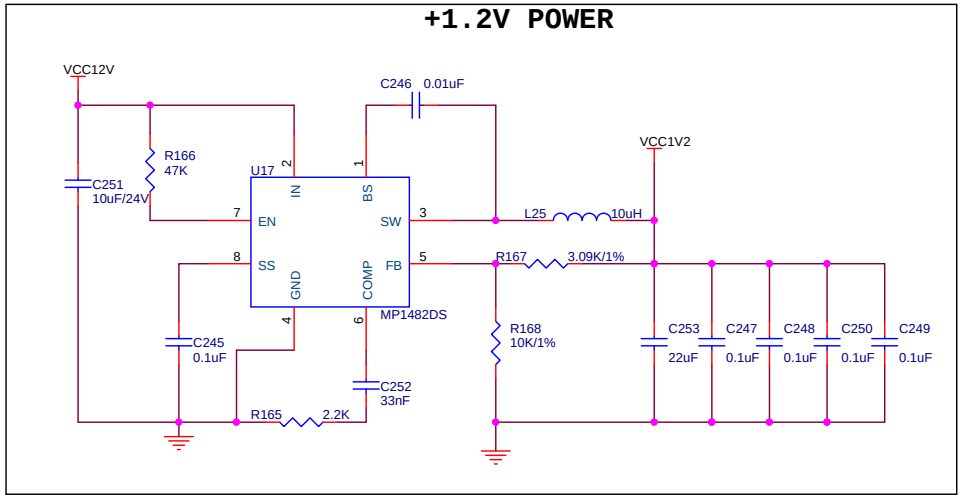

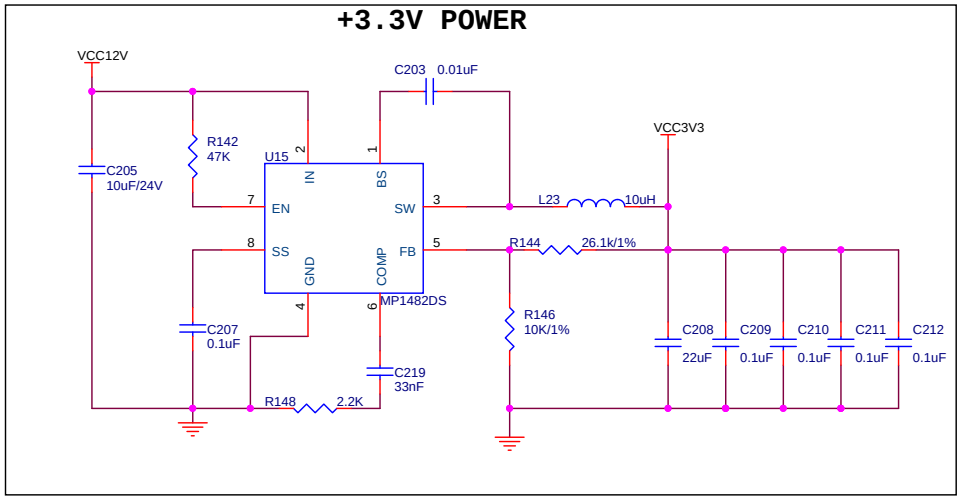

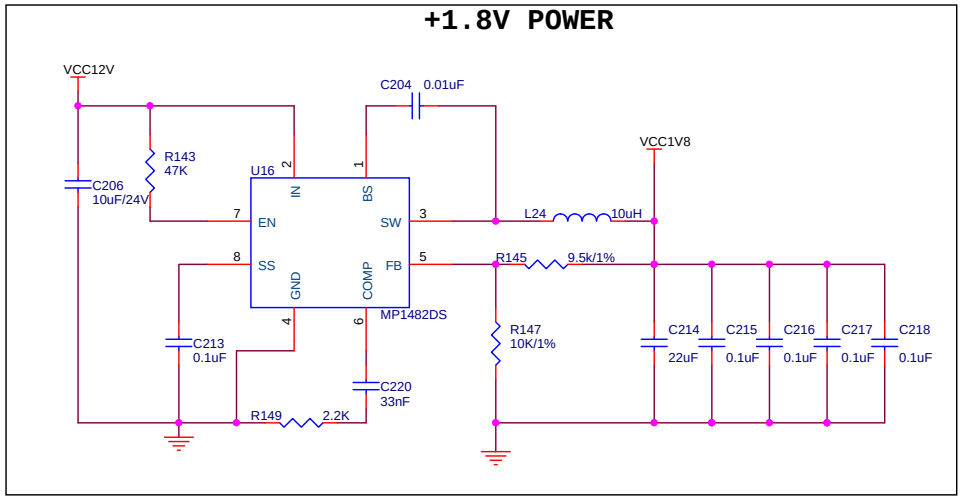

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Title: PAGE14 Power

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