





# Cloudifier Virtual Apps

Virtual desktop predictive analytics apps environment based on GPU computing framework

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Abstract — The need for systems capable of conducting inferential analysis and predictive analytics is ubiquitous in a global information society. With the recent advances in the areas of predictive machine learning models and massive parallel computing a new set of resources is now potentially available for the computer science community in order to research and develop new truly intelligent and innovative applications. In the current paper we present the principles, architecture and current experimentation results for an online platform capable of both hosting and generating intelligent applications.

Keywords— artificial intelligence, machine learning, virtual desktop, predictive analytics

#### I. INTRODUCTION

The entire field of Artificial Intelligence tends to become more and more about improving predictions and predictive models as Goldfarb et al [1] argue in the paper "Managing the Machines". Predictive modelling can now be used for extremely varied tasks ranging from constructing expert system chat-bots [2] based on Long Short Term Memory deep recurrent neural networks that would use natural language capability (reading and writing) up to melanoma recognition in dermoscopy images [3].

In our paper, we will propose several approaches: (i) a webservices based model for the deployment of model training and prediction on GPU massive parallel computing backend and (ii) employment of automatic software design, development and deployment for user experience modules based on a pipeline shallow machine learning model [4] and (iii) a unified architecture and underlying framework for generation and hosting predictive analytics applications.

Within out paper we will argue that our proposed research and experimentation will fuel the development and implementation of a production-grade system capable of addressing in the same time the needs of several categories of customers such as:

- users in need of a online application marketplace fully oriented on predictive analytics applications;
- software developers in need of a legacy apps automatic software translation tools including machine learning templates;
- scientific users both from academic and commercial environment looking for an online application framework and execution environment in order to conduct predictive experiments in areas such as healthcare, finance and environment;

#### II. RELATED WORK

Our work relates to the most recent advances both in the field of High Performance Computing and Machine Learning.

# A. GPU based High Performance Computing

The usage of GPU based computing is currently mostly applied in deep learning – both research experiments and production development – however little to no attention is given to the employment of GPU parallel computing resources in shallow machine learning techniques such as decision trees, multi-variate linear and logistic regression, Naive-Bayes models. Nonetheless the current advances and the state-of-the-art prediction scores achieved by the deep learning scientific experiments give little room to further scientific development and advances of known shallow models techniques. Even more, companies such as Microsoft, Google or IBM currently propose online framework-engines that allow on-demand web-based prediction model creation and experimentation [5] that are entirely used for off-line training and prediction experiments, running on CPU based infrastructures. Taking these presented







aspects into account we are proposing a set of new modern approaches that will potentially empower shallow machine learning models with the capability using GPU based parallel computing resources. Also, another of the supporting aspects that we will use in our approach is based on the most important features of shallow machine learning models: the ability of the model to self-explain itself (albeit at the expense of prediction power shown by deep models with massive hidden and less than explanatory weight spaces).

#### B. Architectural roots

The proposed architecture is based on our previous work conducted in the area of automatic software application migration and machine-learning assisted automatic programming [4]. Based on our automatic software migration architecture [4] we will be able to achieve the following desiderates during the production phase of our project:

- Create a scalable architecture for a user-friendly online environment where external users will be able to quickly generate and customize application starting from existing legacy apps;
- Combine the power of the automatic application generation features with template-based models in order to create a business horizontal application repository that will include apps such as (a) multivertical customer churn prediction models; (b) multi-vertical customer behavior inference model; (c) business flow generated data exploration stub apps; (d) machine learning stub/template applications
- Finally, we are able to create a research applications repository that will continuously grow by accommodating machine learning augmented apps in areas such as medical diagnostics inference/prediction, financial predictive analytics, environment inferential models. The proposed scientific apps will be developed within the "Cloudifier" community and will fully utilize the scalable GPU-augmented machine learning template models;

## III. PROPOSED ARCHITECTURE

The main points of our proposed architecture are the following:

- A. General execution framework based on GPU HPC REST services;
- B. Machine learning augmented applications generated within a provisioned Cloud infrastructure by automated software migration [4];

 C. Virtual desktop predictive analytics apps repository consisting of template-developed business applications and scientific experiments;

#### A. REST based massive parallel computation services

Based on the fact that one of our paper areas of innovation is the augmentation of shallow machine learning models with parallel processing techniques based on GPU processing, we are proposing a REST based service for parallel computation on GPU backend. At present time, based on our knowledge, the advancement of GPU based hardware infrastructures capable of delivering massive parallel computing for machine learning tasks at very low costs has only been exploited by the area of deep learning and deep neural networks in particular. Our proposal addresses this gap between the available computation resources for deep learning and the ones available for scientific shallow machine learning. In our proposed approach for advancing shallow machine learning by GPU parallel computing augmentation we will argue that shallow model's pipelines can achieve comparable results with deep learning models for various applications. Although most of the state-ofthe-art research in the area of machine learning is focused on deep learning models the classic machine learning models with shallow hidden layer structure what two main characteristics that deep models lack: self-explain ability and algorithmic robustness for online and real-time environments.

Currently several programming approach options are available for programming GPU parallel computation engines both for scientific purposes and for engineering/production and probably the most widely used are OpenCL [6] and CUDA [7]. Basically, both mentioned models use the same technique with only slight differences. The two most fundamental aspect in regard to the computational approach consists in the two central entities consisting in *kernel* – that is the function containing the actual code to be run in parallel - and the *devices* – the actual computational devices that will run the *kernel* in parallel. Traditionally the *kernel* is a function described by the the following generic algorithm:

## **Algorithm** 1 *Kernel(Data, BlockInformation)*

cID ⇐ get coordinates tuple from *BlockInformation* Modify *Data*[cID] with hard coded <*operation*>

## Return

Note that in above example *BlockInformation* defines the actual data segment that a particular *device* is processing within *Data* and *<operation>* denotes the hard-coded (compiled) operation that the *kernel* is computing for the particular block of *Data* it receives. For the particular case of matrix dot product we can have each *device* compute with its









*kernel* the dot product of two vectors (the row vector of  $1^{st}$  matrix and the column vector of  $2^{nd}$  matrix).

```
A * B = A^T B = \sum a_i b_i \tag{1}
```

Finally, we have the following two code snippets for computing (1) in OpenCL and CUDA based *kernels*:

#### Code 1 OpenCL Kernel for Matrix-Matrix dot product

```
__kernel void OpenCLMatrixDotKernel(const int
A_ROWS,
    const int BC_COLUMNS,
    const int A_COLUMNS,
    const __global float* A,
    const __global float* B,
    __global float* C)
{
    const int Row = get_global_id(0);
    const int Col = get_global_id(1);

    float acc = 0.0f;
    for (int c=0; c<A_COLUMNS; c++) {
        acc += A[c*A_ROWS + Row] *

B[Col*A_COLUMNS + c];
    }
    C[Col*A_ROWS + Row] = acc;
}</pre>
```

#### Code 2 CUDA Kernel for Matrix-Matrix dot product

```
global__ void CUDAMatrixDotKernel(Matrix A,
Matrix B, Matrix C)
{
    float acc = 0;
    int row = blockIdx.y * blockDim.y +
        threadIdx.y;
    int col = blockIdx.x * blockDim.x +
        threadIdx.x;
    if(row > A.height || col > B.width)
            return;
    for (int e = 0; e < A.width; ++e)
        acc += A.elements[row * A.width +
        e] *
        B.elements[e * B.width +
        col];

    C.elements[row * C.width + col] = acc;</pre>
```

As previously mentioned, in the current paper we will present a REST [8] approach for cloud computing based provisioning of parallel computation services. This approach will enable the implementation and consumption of a web based service that will have the following generic algorithm based on FIFO allocation .

## **Algorithm** 2 ParallelProcessingREST(Request)

For each Kernel, Data pair in Request

 $Kernel \Leftarrow get kernel source code from$  Request  $Data \Leftarrow get dataset from Request$  by
value or by reference from external source  $Available \Leftarrow get available devices from$ existing cluster based on adaptive Algorithm 3
If Available is valid then Off-load Kernel and Data on Available devices Get off-load Result and append to Response

 $Response \Leftarrow Overhead time$ 

exceeds computing time

**Return** Response

The actual GPU execution strategy of the off-loaded kernels will be based on an adaptive algorithm that will take into consideration server-load, task complexity and GPU load/preparation overhead.

## **Algorithm** 3 ResourceAllocation(*Kernel*, *Data*)

Else

 $cFLOPS \Leftarrow$  evaluate needed FLOPS for Kernel and Data

 $Resources \leftarrow Compute needed devices based on <math>cFLOPS$ 

ComputeTime ← Aproximate total execution time based on Resources, cFLOPS

*WaitTime* ← Off-load overhead time + time until devices are available

If ComputeTime > WaitTime

 $Result \leftarrow Queue position or actual allocated devices$ 

else

 $Result \Leftarrow$  allocation invalidated

Return Result

## B. User Apps – migration and fast-development repository

The user created apps will consist of both generated apps and template-based apps. The generated apps will actually be the results of our automatic software migration engine [4], fully hosted within the execution environment proposed by our project.

The template-based apps will mainly consist of online "skeleton" apps that have been designed for a particular horizontal purpose within the area of predictive analytics. The user will be able to configure a certain template or propose a new template for online publication and crowd-testing.









## C. Business Apps and Research experiments

As with the user-apps repository the business apps and research experiments repositories will host various predictive analytics online applications that will be able to run the proposed machine learning models within the proposed GPU processing backend.

#### IV. EXPERIMENTATION AND CONCLUSIONS

At present time we are conducting experimentation activities in the following areas:

- Automatic legacy software migration and provisioning within proposed cloud infrastructure. A set of desktop-based ready-to-migrate applications has been constructed consisting of applications written in FoxPro, Visual Basic, Visual C++. For this particular set of application we are using our automated migration software and both the existing source code of the legacy application. The source code is necessary for the actual validation of resulting automated migration inferential process.
- Predictive analytics horizontal applications for business environment. Currently customer behavior predictive analytics experiments are conducted within our proposed GPU-augmented machine learning framework. Cross-validation and benchmarking of results is conducted using the following scientific and general machine learning frameworks:
  - Python Sci-Kit Machine Learning package
  - o R package "caret" (CRAN hosed)

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#### V. BIBLIOGRAPHY

- [1] A. Agrawal, J. Gans and A. Goldfarb, "Managing the Machines," *HBR*, 2016.
- [2] Vinyals, "A Neural Conversational Model," in *ICML*Deep Learning Workshop 2015, 2015.
- [3] C. V, Q.-B. Nguyen and S. Pankanti, "Deep Learning Ensembles for Melanoma Recognition in Dermoscopy Images," *Journal of Research and Development*, 2016.
- [4] N. T. AI Damian, "Model Architecture for Automatic Translation and Migration of Legacy Applications to Cloud Computing Environments," in *CSCS*, Bucharest, 2017.
- [5] M. Bihis and S. Roychowdhury, "A generalized flow for multi-class and binary classification tasks: An Azure ML

- approach," in Big Data (Big Data), 2015 IEEE International Conference on, 2015.
- [6] J. Stone, D. Gohara and G. Shi, "OpenCL: A Parallel Programming Standard for Heterogeneous Computing Systems," *Computing in Science & Engineering*, vol. 12, no. 3, 2010.
- [7] J. Ghorpade, J. Parande and M. Kulkarni, "GPGPU PROCESSING IN CUDA ARCHITECTURE," *Advanced Computing: An International Journal (ACIJ)*, vol. 3, 2012.
- [8] R. T. Fielding, "Architectural Styles and the Design of Network-based Software Architectures," University of California, Irvine, 2000.