

# Pocket Science Lab V6

## Pin Layout Diagram

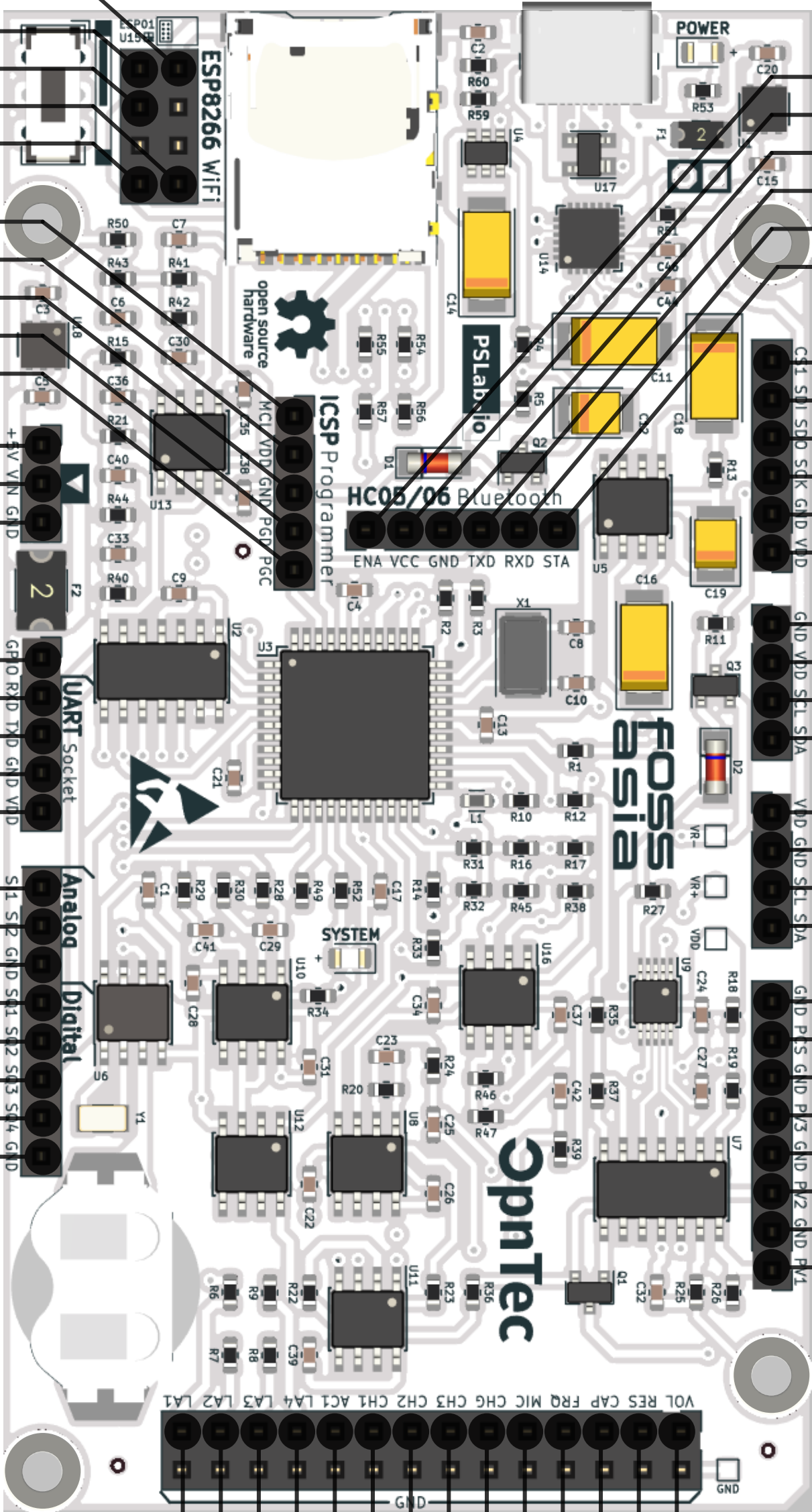
Ground Pin (0 V)	<b>GND</b>
UART Rx Pin for ESP	<b>RXD</b>
ESP Enable Pin / CH_PD	<b>ENP</b>
UART Tx Pin for ESP	<b>TXD</b>
Voltage Supply Pin (+3.3 V)	<b>VDD</b>

Master Clear Pin	<b>MCL</b>
Voltage Supply Pin (+3.3 V)	<b>VDD</b>
Ground Pin (0 V)	<b>GND</b>
Programmer Data Pin	<b>PGD</b>
Programmer Clock Pin	<b>PGC</b>

Voltage Supply Pin (+5 V)	<b>+5V</b>
External Power input Pin (< 15 V)	<b>VIN</b>
Ground Pin (0 V)	<b>GND</b>

General Purpose Input Output Pin (GPIO)	<b>PIN</b>
External UART Rx Pin	<b>RXD</b>
External UART Tx Pin	<b>TXD</b>
Ground Pin (0 V)	<b>GND</b>
Voltage Supply Pin (+3.3 V)	<b>VDD</b>

Wave Generator Pin 1 (5 Hz ~ 5 kHz)	<b>SI1</b>
Wave Generator Pin 2 (5Hz ~ 5kHz)	<b>SI2</b>
Ground Pin (0 V)	<b>GND</b>
PWM Generator Pin 1 (32 MHz)	<b>SQ1</b>
PWM Generator Pin 2 (32 MHz)	<b>SQ2</b>
PWM Generator Pin 3 (32 MHz)	<b>SQ3</b>
PWM Generator Pin 4 (32 MHz)	<b>SQ4</b>
Ground Pin (0 V)	<b>GND</b>



<b>ENA</b>	Bluetooth Module Enable Pin
<b>VCC</b>	Voltage Supply Pin (+5 V)
<b>GND</b>	Ground Pin (0V)
<b>TXD</b>	UART Tx Pin for Bluetooth
<b>RXD</b>	UART Rx Pin for Bluetooth
<b>STA</b>	Bluetooth Status Pin (Not Connected)

<b>CS1</b>	Channel Select Pin for SPI
<b>SDI</b>	SPI Data In
<b>SDO</b>	SPI Data Out
<b>SCK</b>	SPI Clock Pin
<b>GND</b>	Ground Pin (0 V)
<b>VDD</b>	Voltage Supply Pin (+3.3 V)

<b>GND</b>	Ground Pin (0 V)
<b>VDD</b>	Voltage Supply Pin (+3.3 V)
<b>SCL</b>	I <sup>2</sup> C Serial Clock Pin
<b>SDA</b>	I2C Serial Data Pin

<b>VDD</b>	Voltage Supply Pin (+3.3 V)
<b>GND</b>	Ground Pin (0 V)
<b>SCL</b>	I2C Serial Clock Pin
<b>SDA</b>	I2C Serial Data Pin

<b>GND</b>	Ground Pin (0 V)
<b>PCS</b>	Programmable Current Source (3.3 mA)
<b>GND</b>	Ground Pin (0 V)
<b>PV3</b>	Programmable Voltage Source (0 - 3 V)
<b>GND</b>	Ground Pin (0 V)
<b>PV2</b>	Programmable Voltage Source ( $\pm$ 3.3 V)
<b>GND</b>	Ground Pin (0 V)
<b>PV1</b>	Programmable Voltage Source ( $\pm$ 5.0 V)

<b>LA1</b>	Logic Analyzer Pin 1
<b>LA2</b>	Logic Analyzer Pin 2
<b>LA3</b>	Logic Analyzer Pin 3
<b>LA4</b>	Logic Analyzer Pin 4
<b>AC1</b>	Channel for AC Waves (Oscilloscope)
<b>CH1</b>	Channel 1 (Oscilloscope)
<b>CH2</b>	Channel 2 (Oscilloscope)
<b>CH3</b>	Channel 3 (Oscilloscope)
<b>CHG</b>	Channel 3 Gain Control Pin
<b>MIC</b>	Microphone (+) Pin
<b>FRQ</b>	Frequency Counter Pin
<b>CAP</b>	Capacitance Measurement Pin
<b>RES</b>	Resistance Measurement Pin
<b>VOI</b>	Voltmeter Pin

<b>PIN</b>	Description of Pin
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<b>PIN</b>	Power Pins
<b>PIN</b>	Signal Pins
<b>PIN</b>	Function Pins
<b>PIN</b>	Programmable Power Pins
<b>PIN</b>	Special Function Pins
<b>PIN</b>	Oscilloscope Pins
<b>PIN</b>	Special Information

! Double check connections when using **PIN** pins

