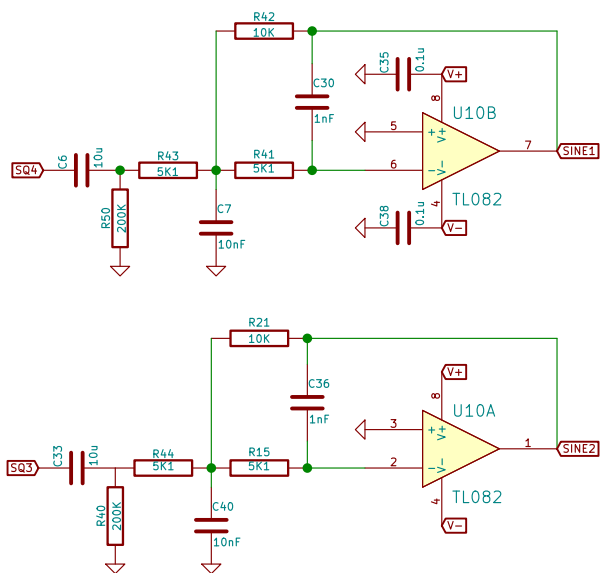
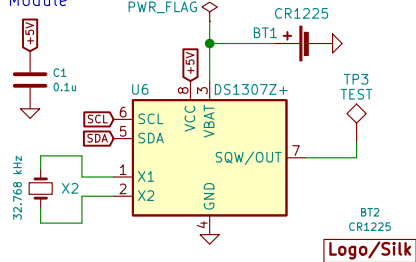


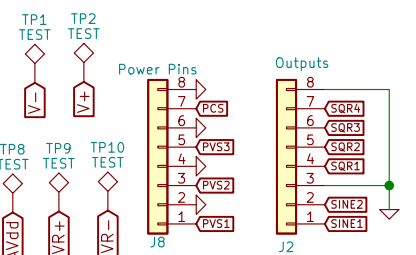
Sine Wave generator filters (Simulated for 5Hz to 5KHz at Okawa-Denshi)



RTC Module



Expansion



Sheet: /I/O Processing/  
File: IO.sch

**Title: PSLab**

Size: A4 Date: 2018-12-29  
KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

Rev: v5.1  
Id: 2/6

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Z7

Logo/Silk

PSLab\_BarCode

Z8

Logo/Silk

QR\_Code

Z9

Logo/Silk

BottomSilk

Z11

Logo/Silk

OpenHardware

ESD-Logo

Logo/Silk

Z10

Sheet: /Logos/  
File: Logos.sch

Title: PSLab






Size: A4

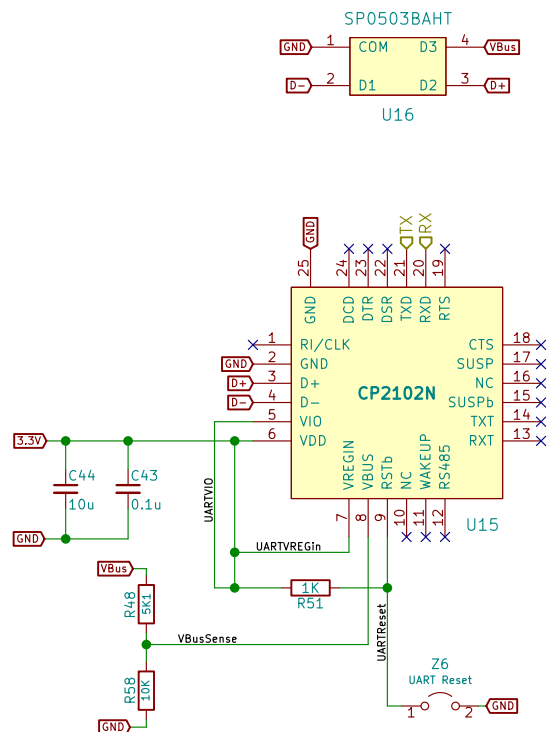
Date: 2018-12-29

KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

Rev: v5.1

Id: 3/6

GND  GND  
 D+  D+  
 D-  D-  
 VBus  VBus  
 3.3V  3.3V

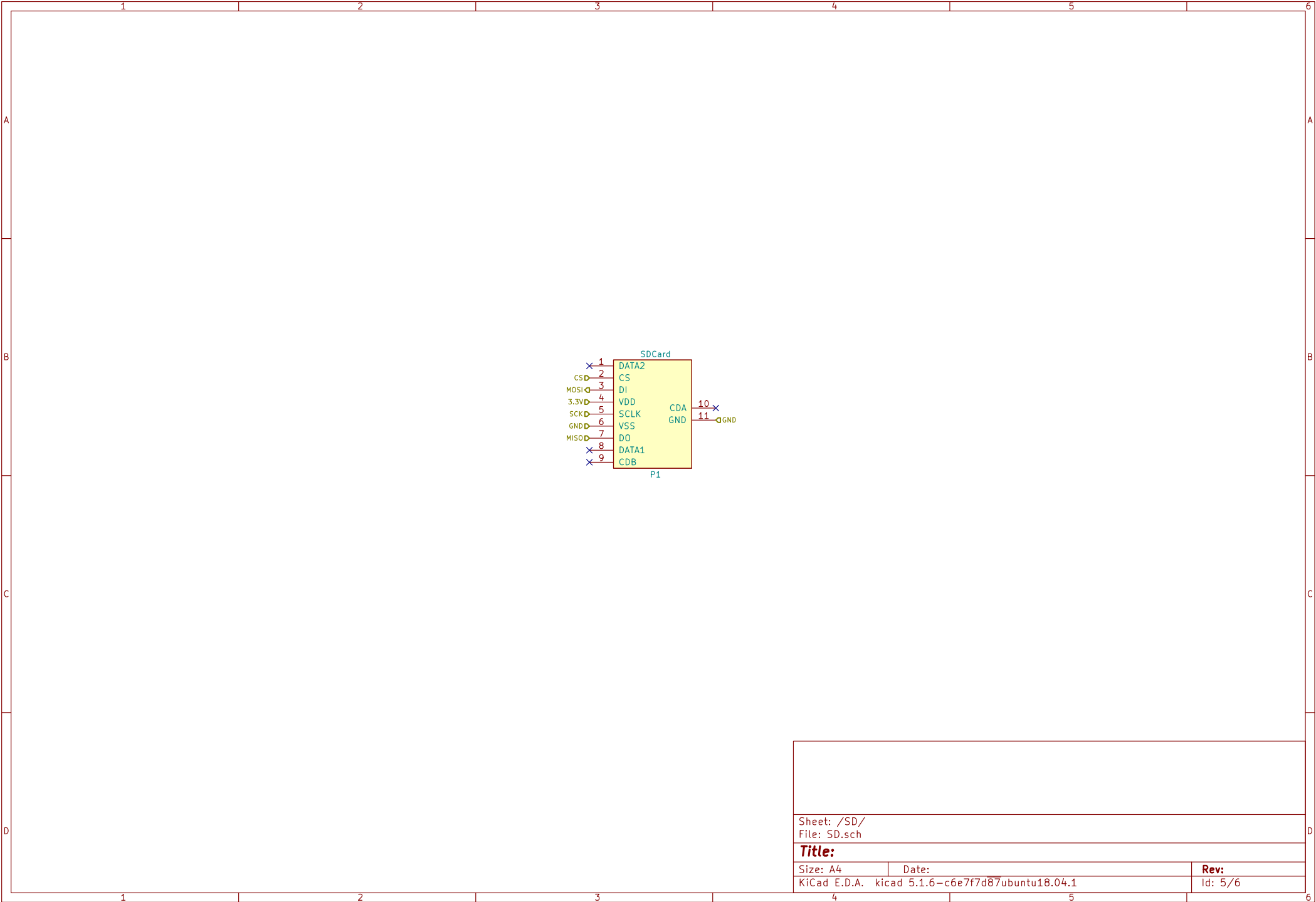


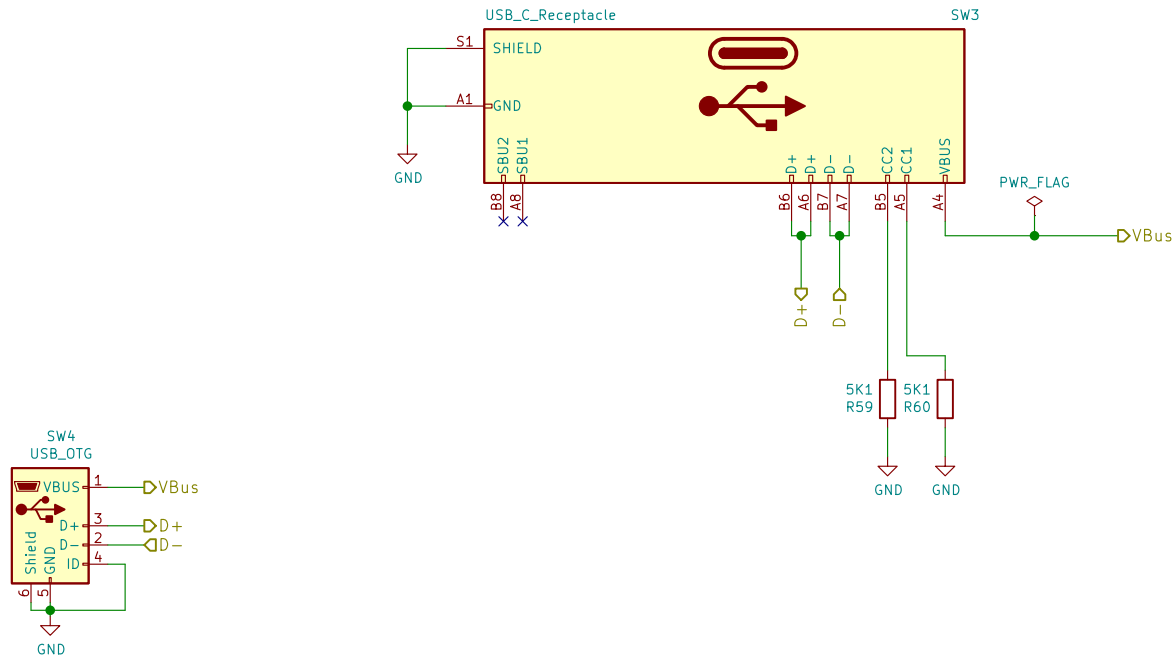
Ref: Datasheet for CPN2102N 4th page  
 Typical connection when internal regulator is not used  
 CPN2102N Datasheet

Sheet: /UARTBridge/  
 File: UARTBridge.sch

# **CPN2102N UART Bridge**

Size: A4	Date: 2019-03-03	Rev: 1
KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1		Id: 4/6





Sheet: /USBC/  
File: USBC.sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1

**Rev:**

Id: 6/6