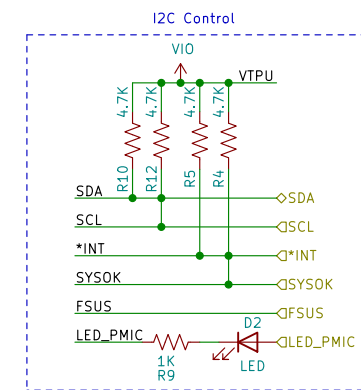


The diagram illustrates the internal wiring of the USB_C_Receptacle (J1). The component is represented by a yellow rectangle with a USB symbol and a shield icon. The internal connections are as follows:

- VBUS:** Connected to pin A4.
- CC1:** Connected to pin A5.
- CC2:** Connected to pin B5.
- D-:** Connected to pin A7.
- D+:** Connected to pin B7.
- D+:** Connected to pin A6.
- D+:** Connected to pin B6.
- RX1-:** Connected to pin B10.
- RX1+:** Connected to pin B11.
- TX1-:** Connected to pin A3.
- TX1+:** Connected to pin A2.
- RX2-:** Connected to pin A10.
- RX2+:** Connected to pin A11.
- TX2-:** Connected to pin B3.
- TX2+:** Connected to pin B2.
- SBU1:** Connected to pin A8.
- SBU2:** Connected to pin B8.

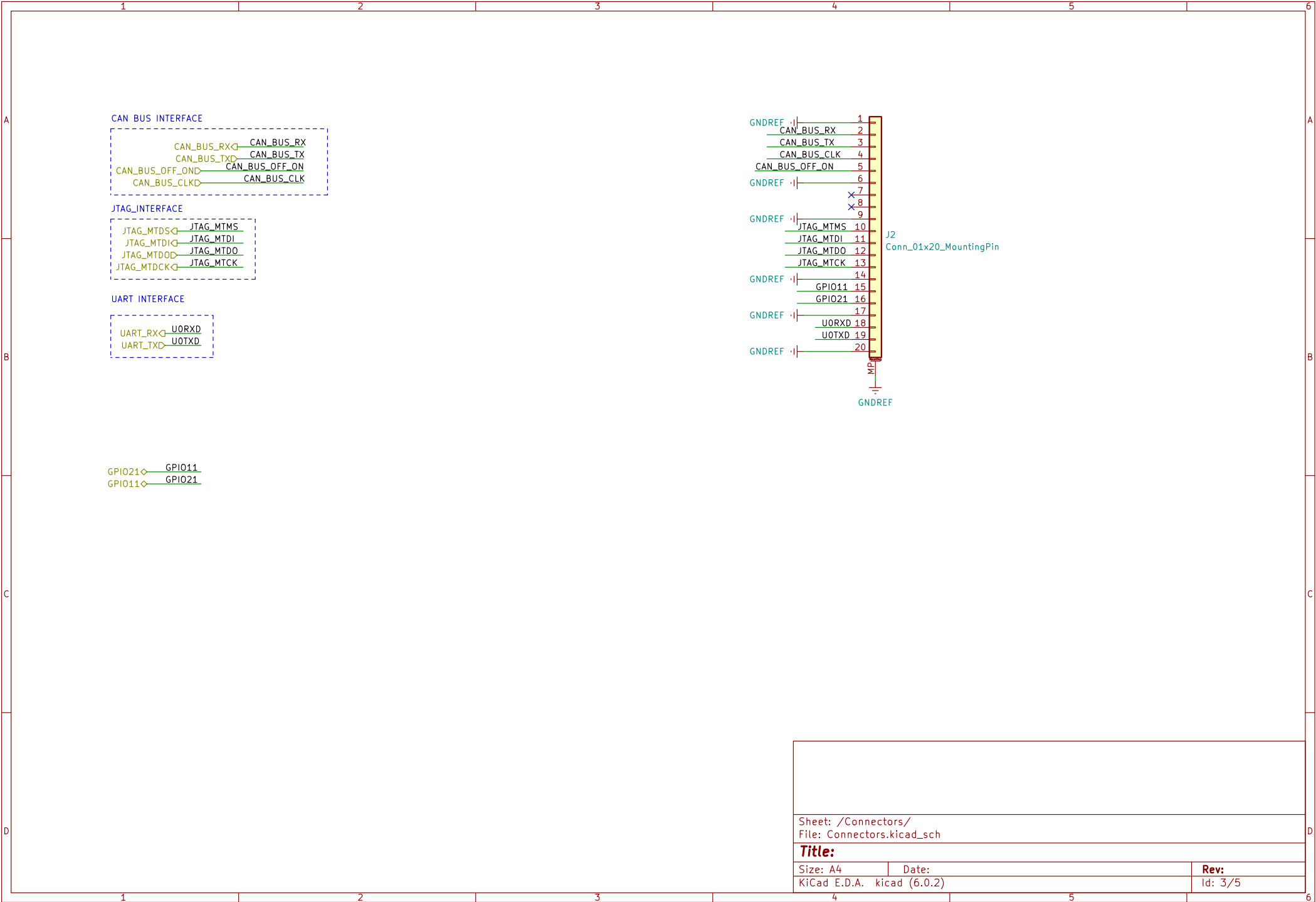
External components and connections include:

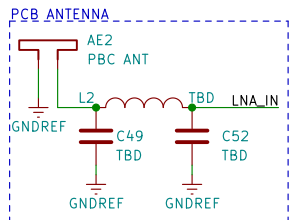
- VBUS_C:** A green line connecting to the VBUS pin (A4) and the CHGIN pin.
- CC1:** A green line connecting to the CC1 pin (A5) and the TPCC1 pin (TP1).
- CC2:** A green line connecting to the CC2 pin (B5) and the TPCC2 pin (TP2).
- USB_D-:** A green line connecting to the D- pin (A7) and the TPCDN TP3 pin (TP3).
- USB_D+:** A green line connecting to the D+ pin (B7) and the TPCDP TP4 pin (TP4).
- CHGIN:** A green line connecting to the VBUS_C pin and the CHGIN pin.
- C1:** A 1uF capacitor connected between the CHGIN pin and GNDREF.
- GNDREF:** A green line connecting to the GNDREF pin and the GND pin.
- R3:** A 1M resistor connected between the SHIELD pin and GNDREF.
- A1:** A green line connecting to the A1 pin and the GNDREF pin.



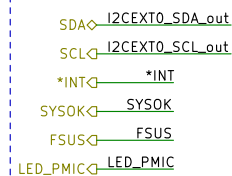
USB_D- ◇ USB_D-
USB_D+ ◇ USB_D+



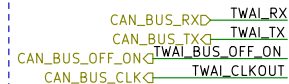




I2C interface for battery charger



CAN BUS INTERFACE



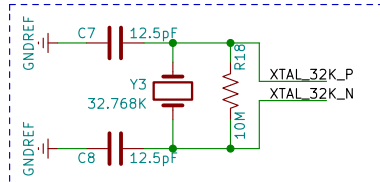
JTAG INTERFACE



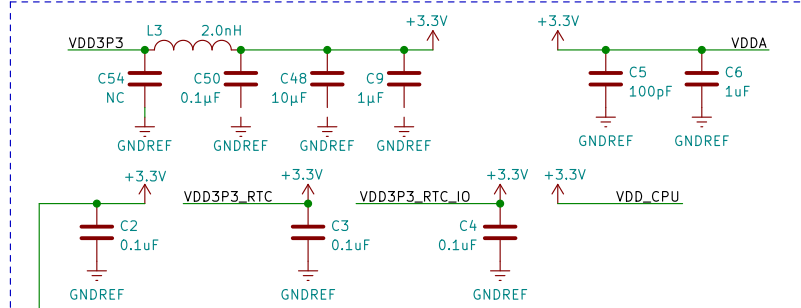
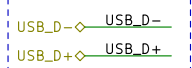
UART INTERFACE



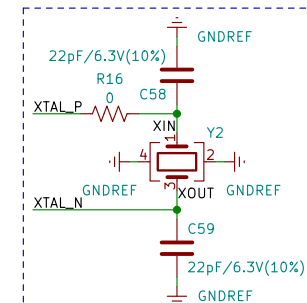
32.768K (+ - 20 ppm) Crystal oscillator



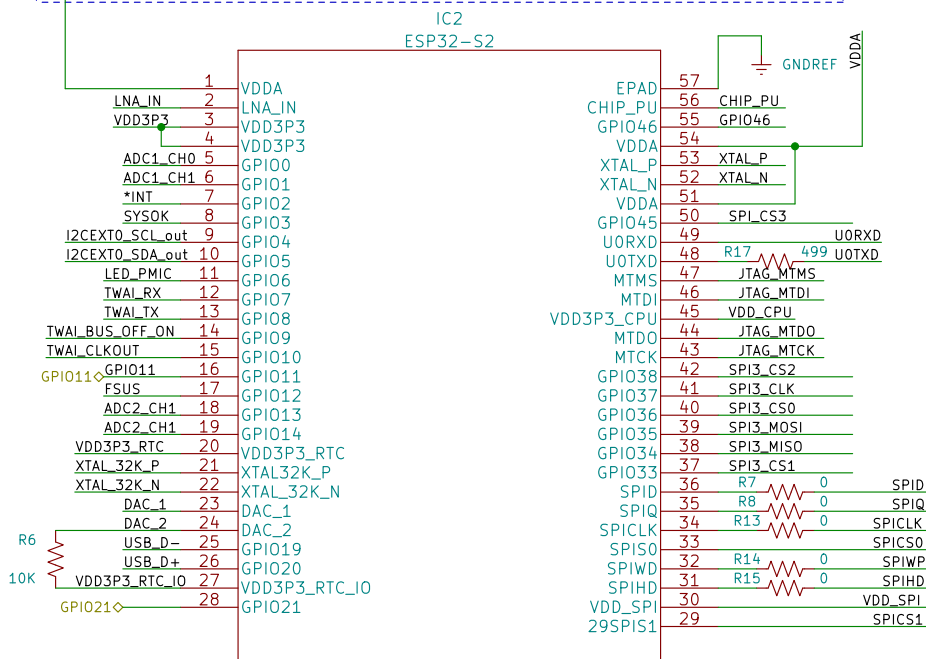
USB OTG INTERFACE (NO INTERM. IC NEEDED)



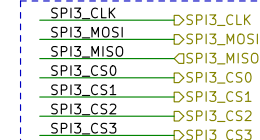
MICROCONTROLLER POWER INPUTS



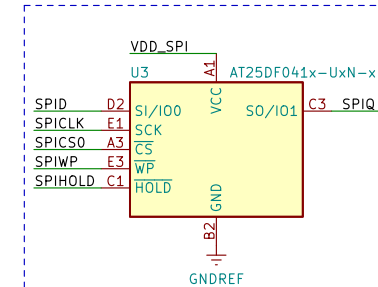
MICROCONTROLLER CRYSTAL FOR SYSCLK 40 MHz (+ - 10 ppm)



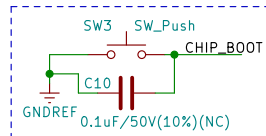
PROBES SPI INTERFACE



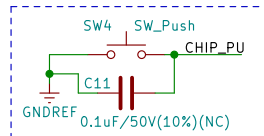
SPI MEMORY INTERFACE



Microcontroller BOOT button



Microcontroller Power-up (UP) button



Sheet: /Microcontroller/
File: microcontroller.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad (6.0.2)

Date:

Rev:

Id: 4/5

