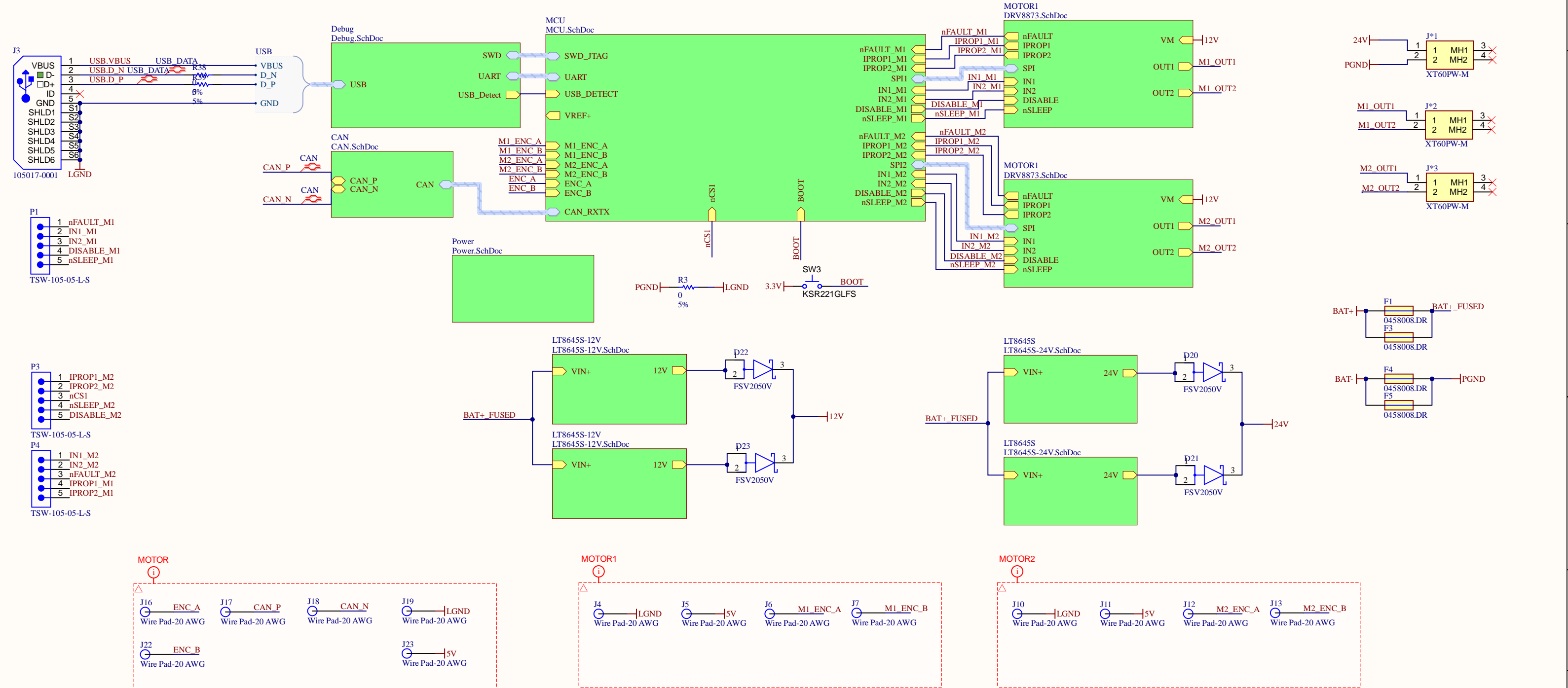


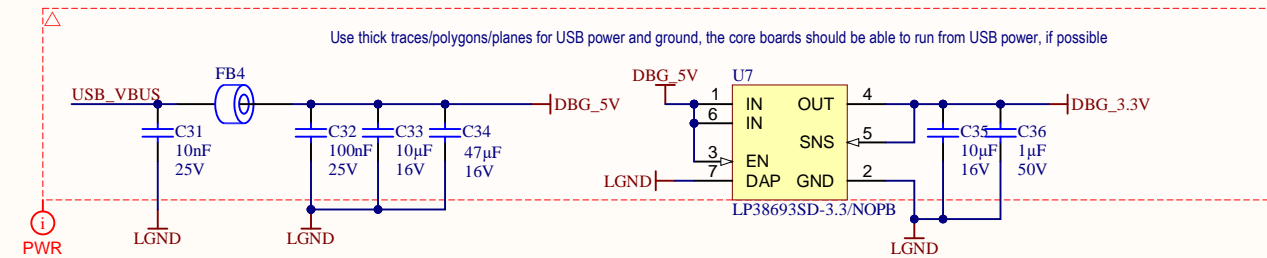
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REVISION	DESCRIPTION	DATE	APPROVED

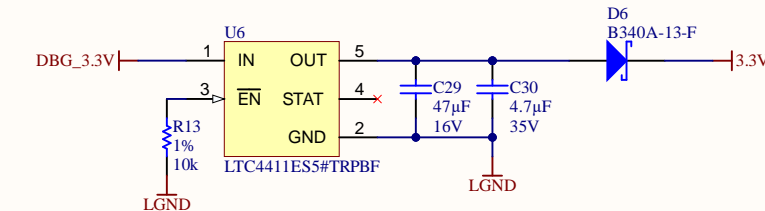


APPROVALS	DATE	PROJECT	Altium	
ENG: *		PROJECT REVISION: *	DOCUMENT REVISION: *	DESIGN ITEM: *
DSN: *		Version control disabled Version control disabled		
CHK: *		TITLE		
REFERENCE DOCUMENTS		*		
BOM:		SIZE	CAGE CODE	DWG NO.
ASSY DWG:		B		
FAB DWG:		SCALE:	FILE NAME	REV
PCB DWG:			OmniTiles.SchDoc	
		SHEET 1 OF 8		

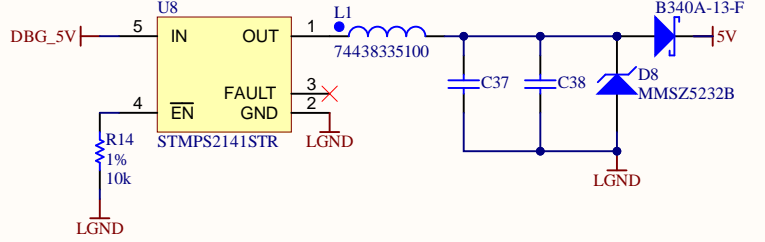
VBUS Filter and Board Power Supply



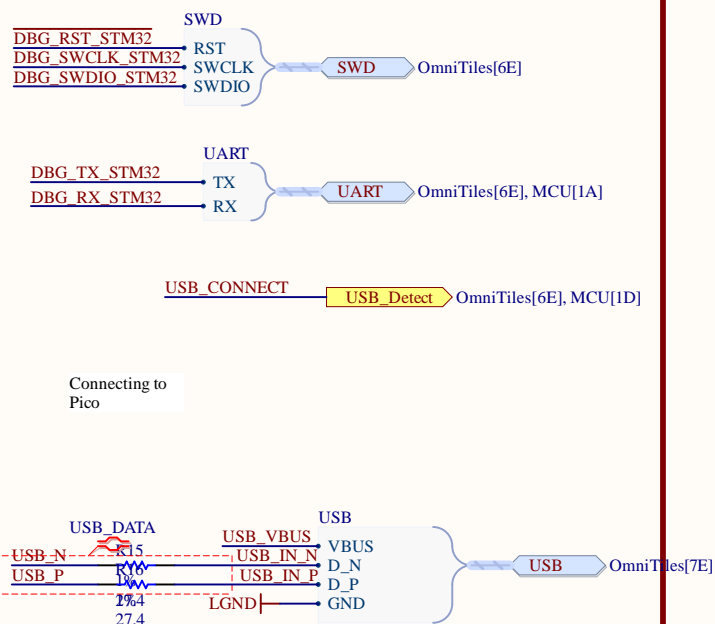
Board 3.3V Supply



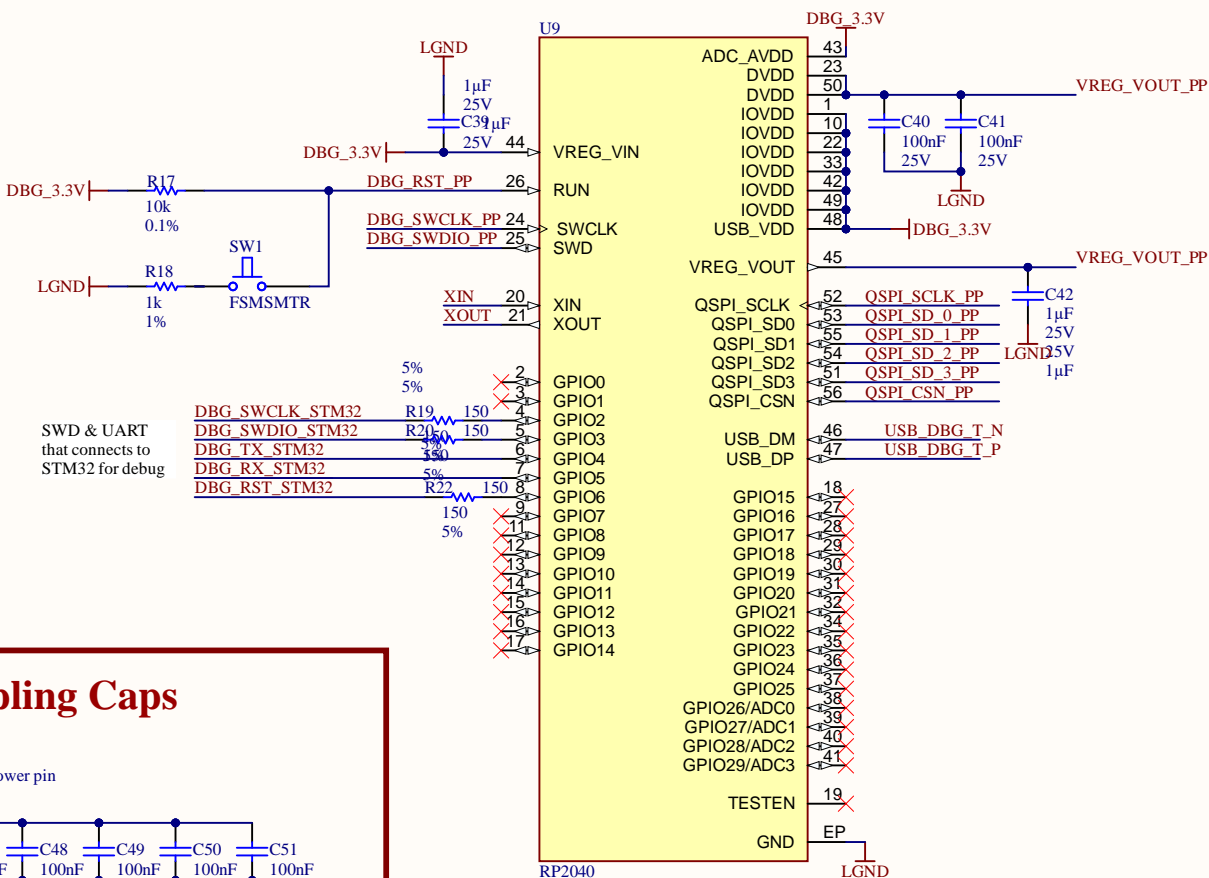
Board 5V Supply



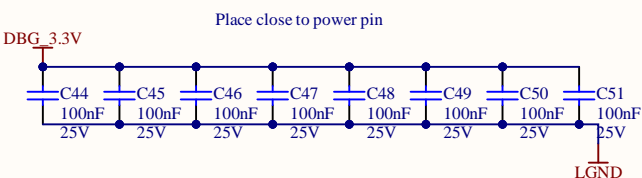
Input/Output



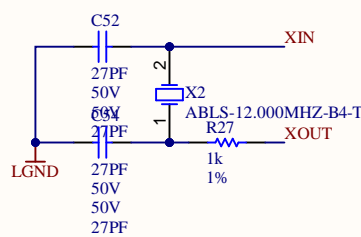
RP2040 (Pico)



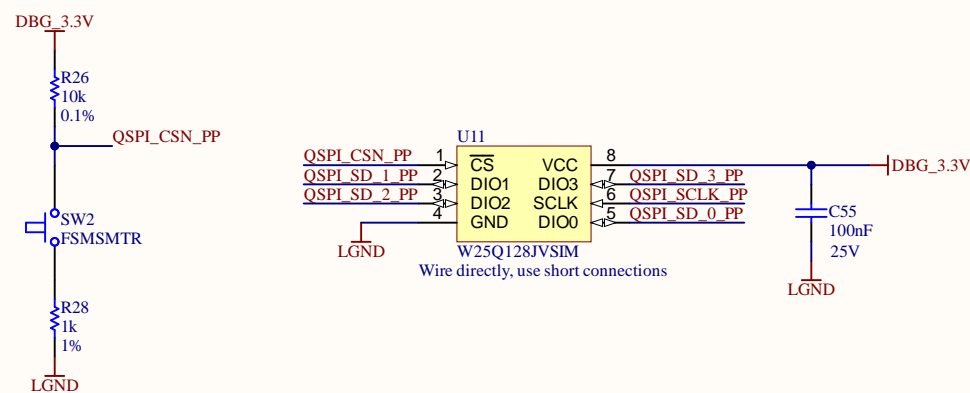
Pico Power Decoupling Caps



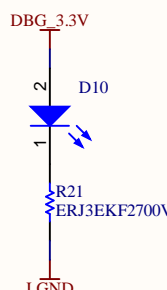
Crystal Oscillator (12MHz)



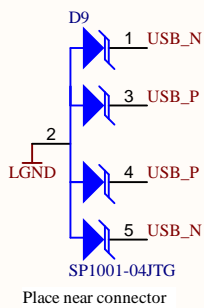
EEPROM



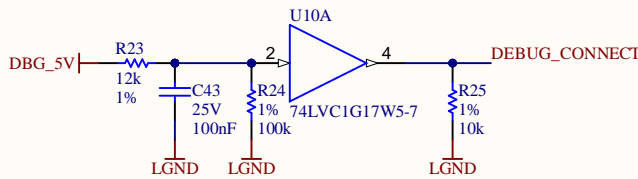
PWR LED



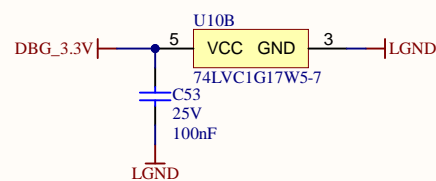
USB TVS Diode



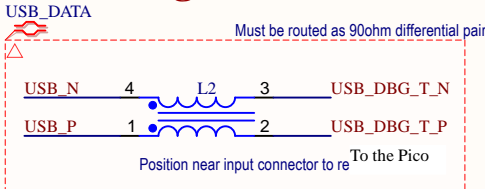
USB Detection Signal



Buffer Power



USB Filtering



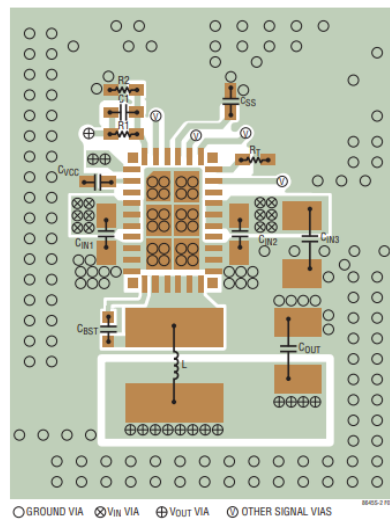
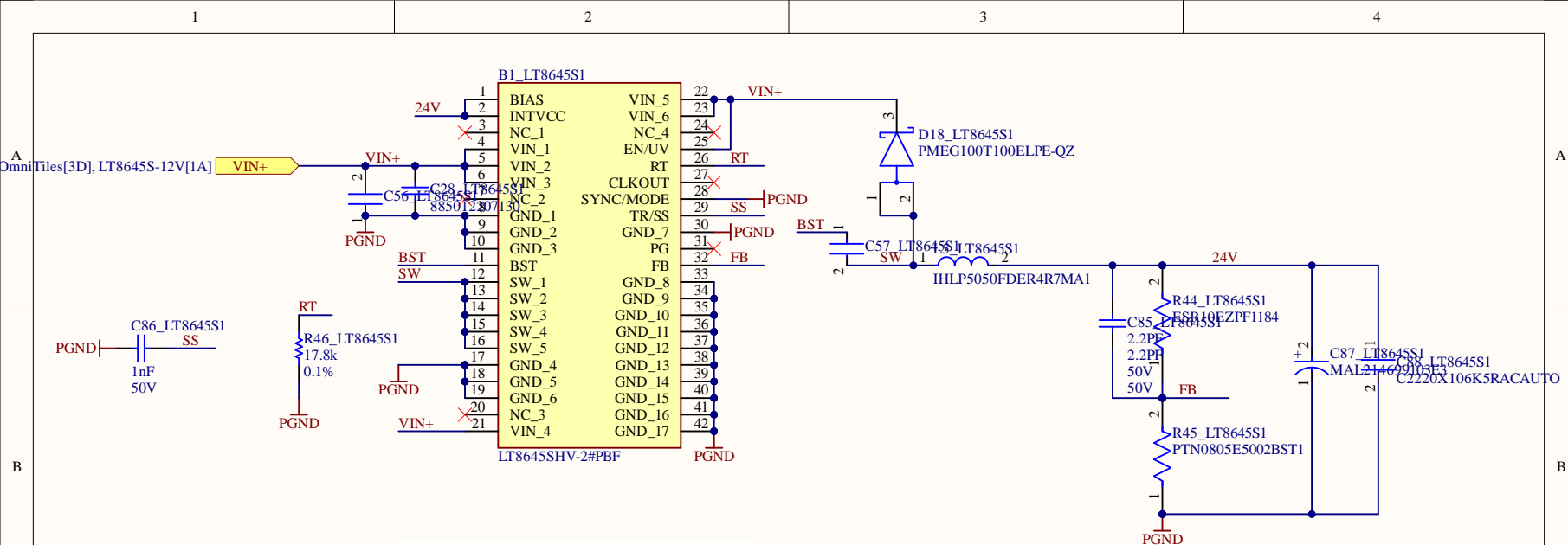


Figure 1. Recommended PCB Layout for the LT8645S-2

Title			
Size A	Number		Revision
Date:	11/12/2025	Sheet of	
File:	LT8645S-24V.SchDoc	Drawn By:	

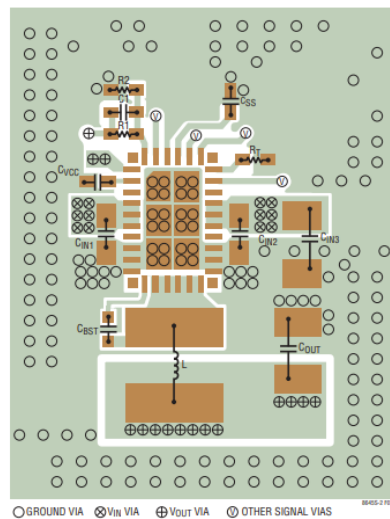
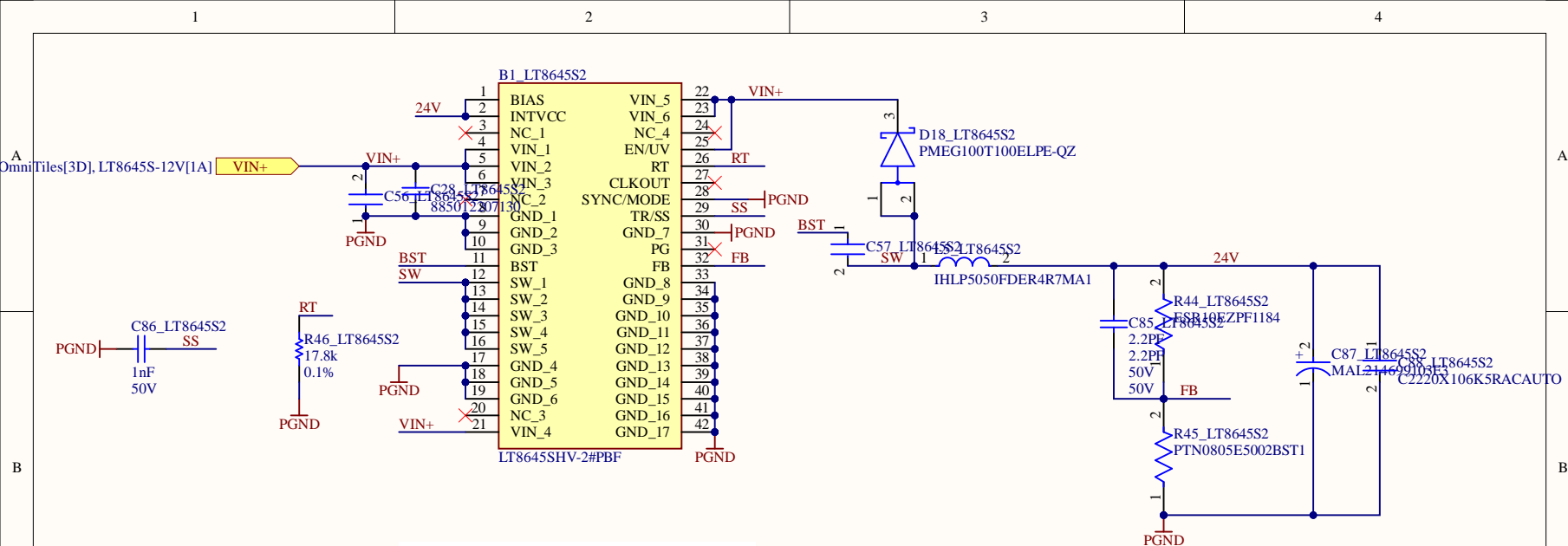
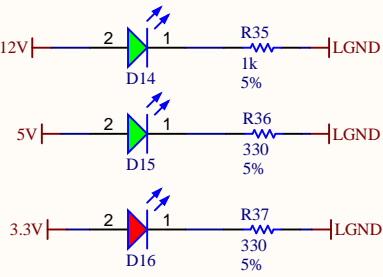
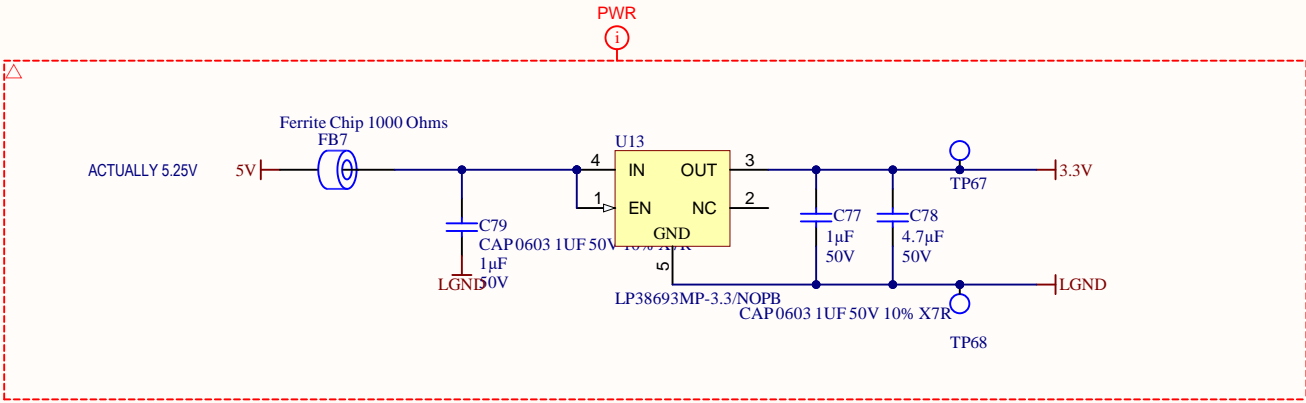
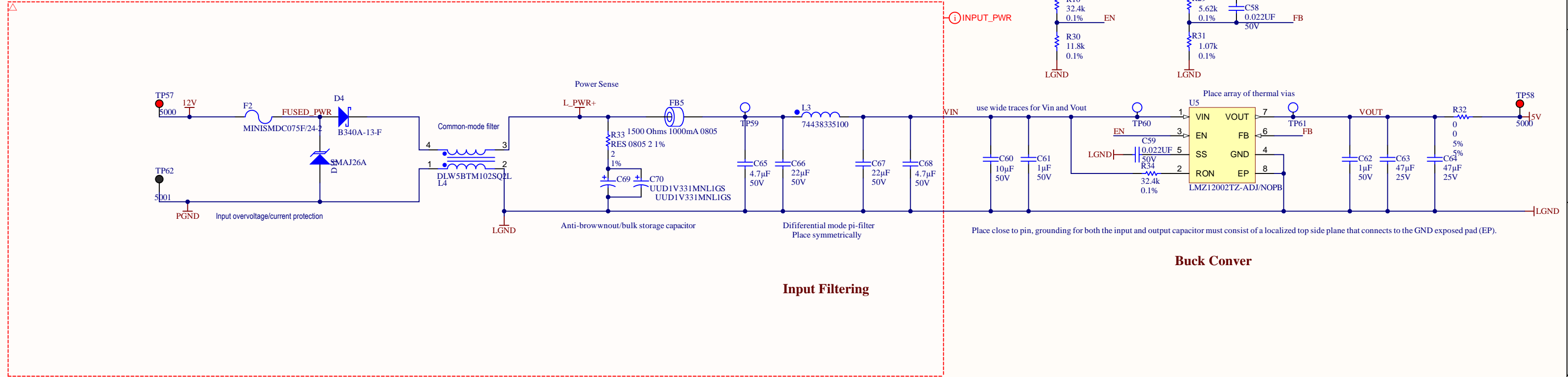


Figure 1. Recommended PCB Layout for the LT8645S-2

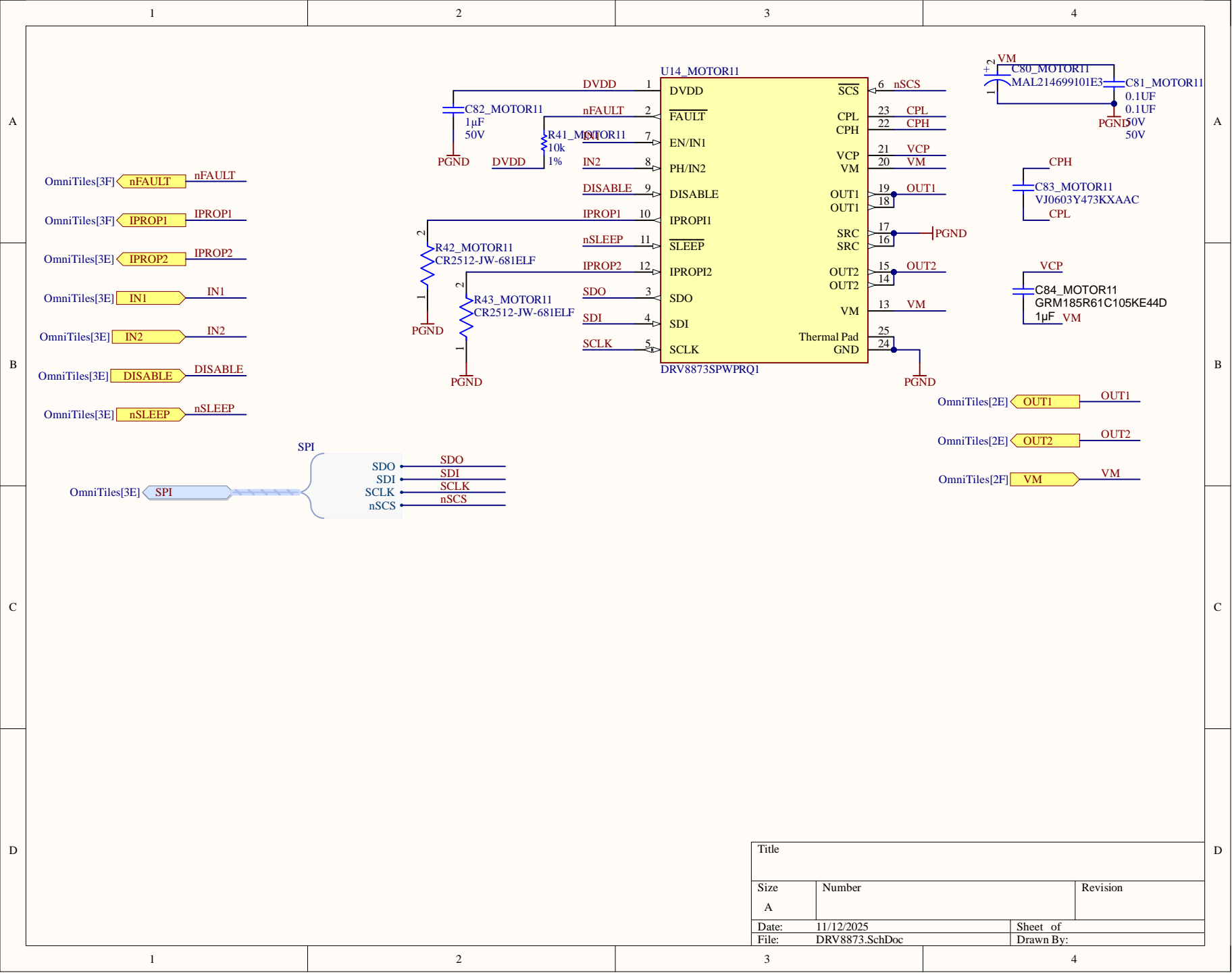
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Date:	11/12/2025	Sheet of	
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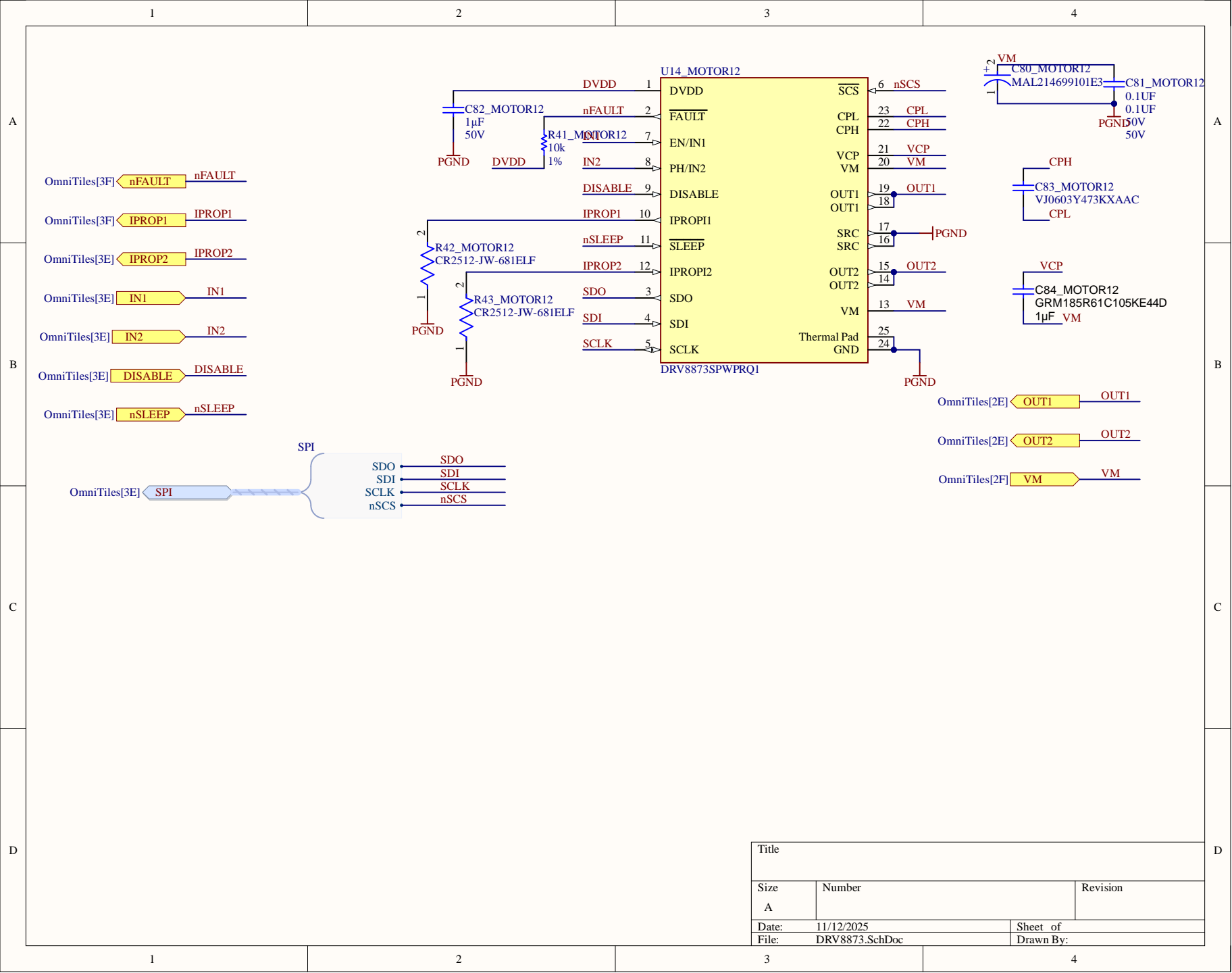
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REVISION	DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	PROJECT	Altium	
ENG: *		PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
DSN: *		Version control disabled Version control disabled		
CHK: *		TITLE		
REFERENCE DOCUMENTS		*		
BOM:		SIZE	CAGE CODE	DWG NO.
ASSY DWG:		B		
FAB DWG:		SCALE:	FILE NAME	SHEET 5 OF 8
PCB DWG:			Power.SchDoc	





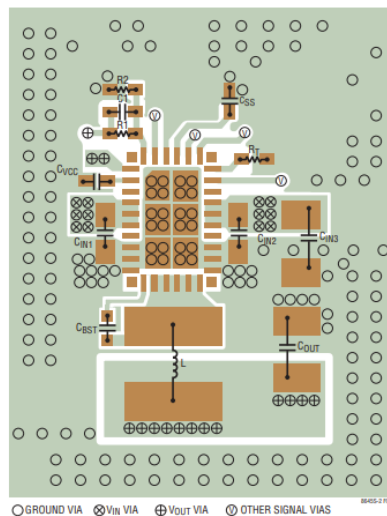
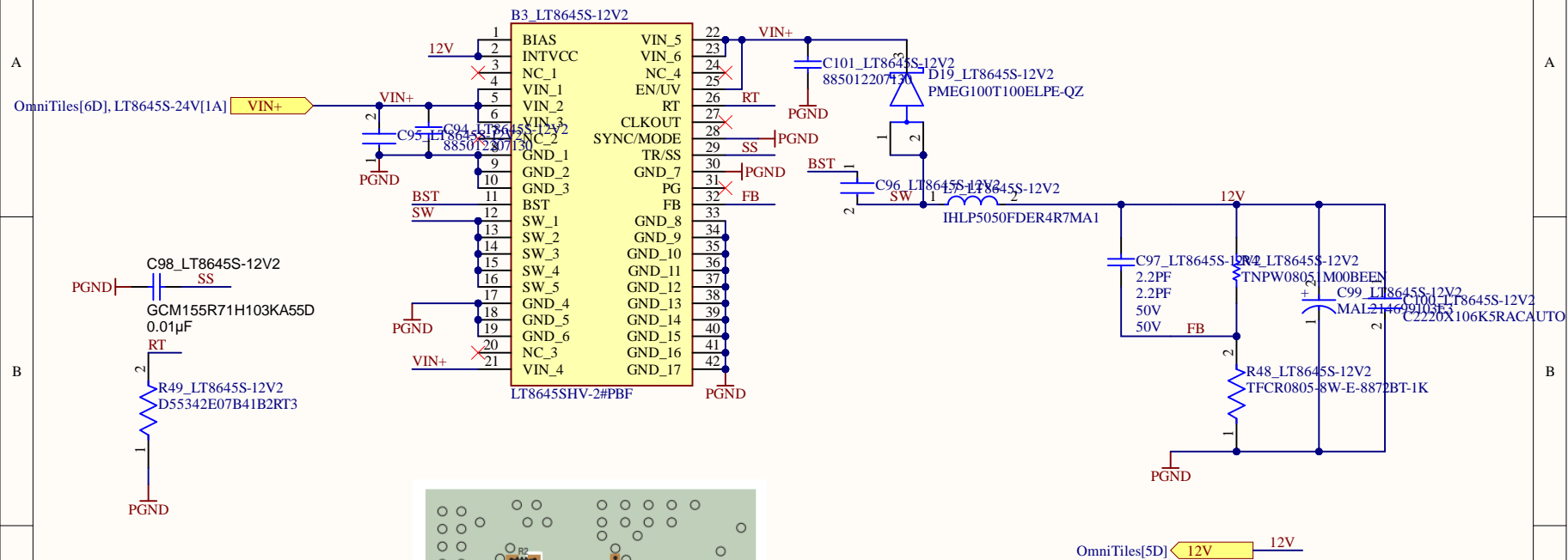
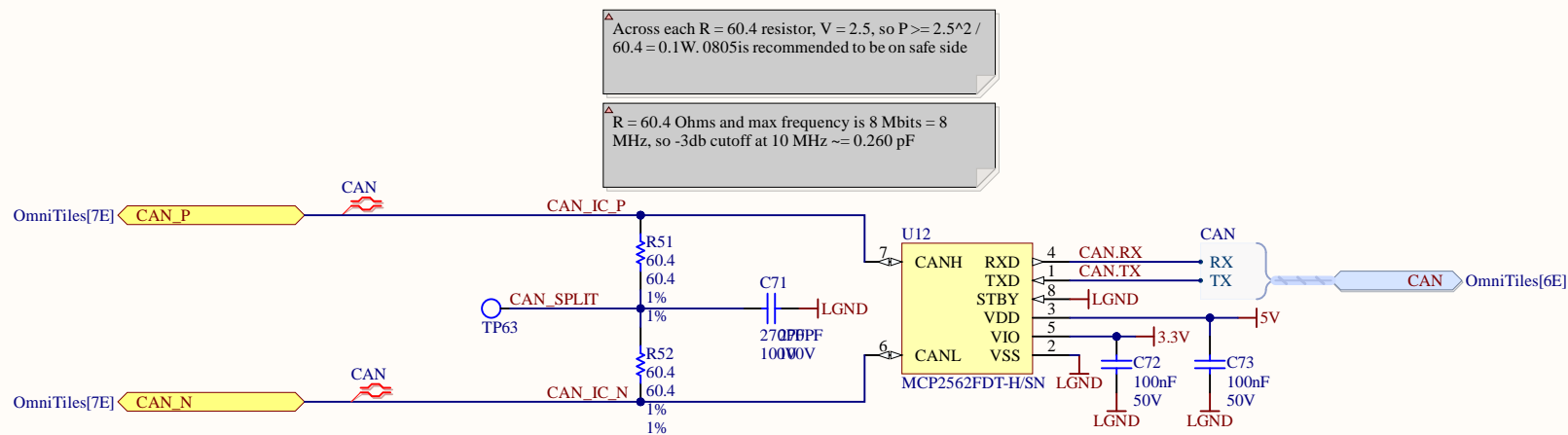


Figure 1. Recommended PCB Layout for the LT8645S-2

Title		
Size A	Number	Revision
Date:	11/12/2025	Sheet of
File:	LT8645S-12V.SchDoc	Drawn By:



Title		
Size	Number	Revision
A		
Date:	11/12/2025	Sheet of
File:	CAN.SchDoc	Drawn By: