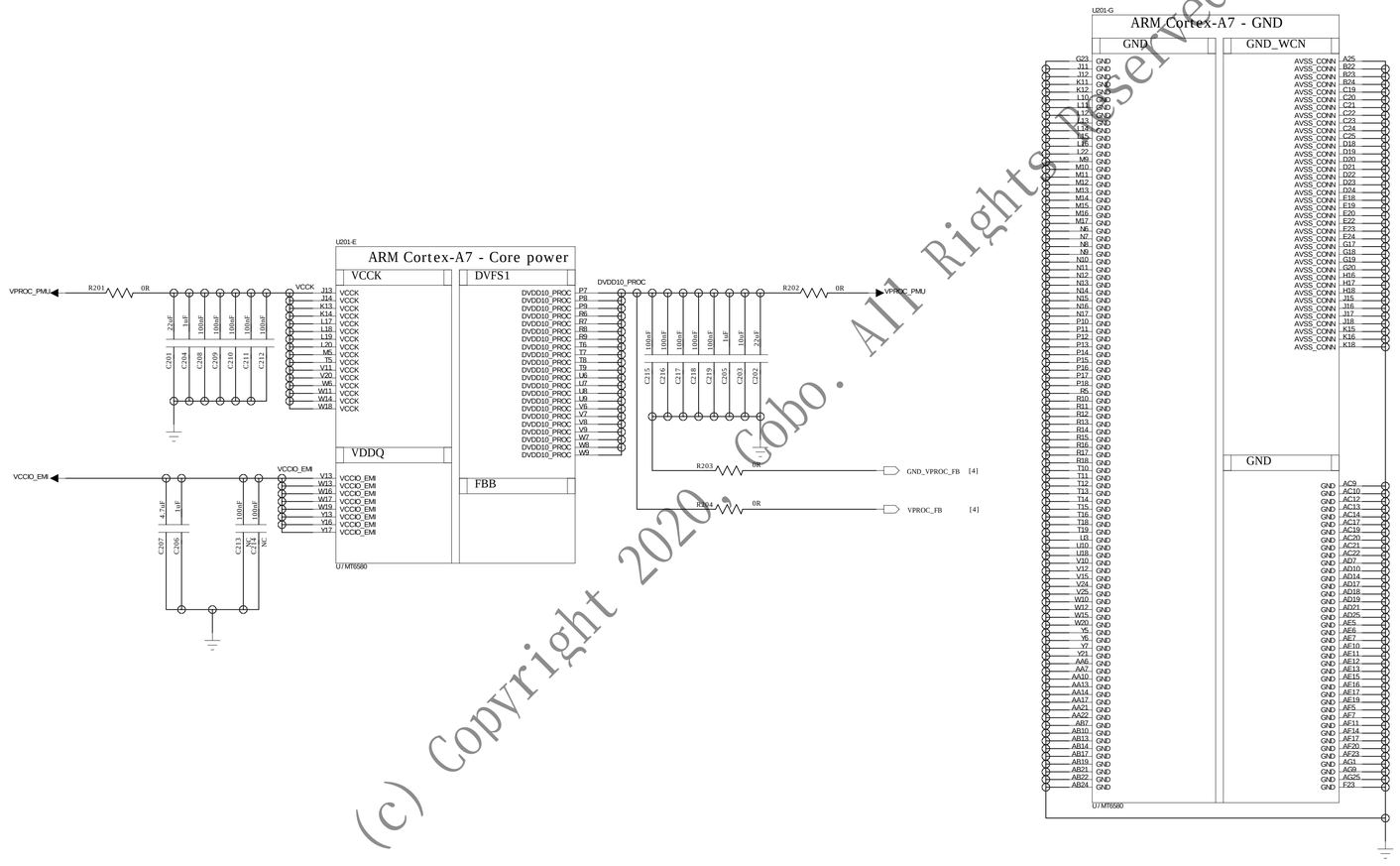
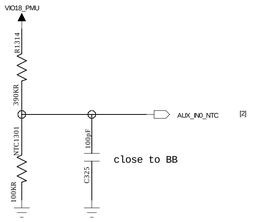
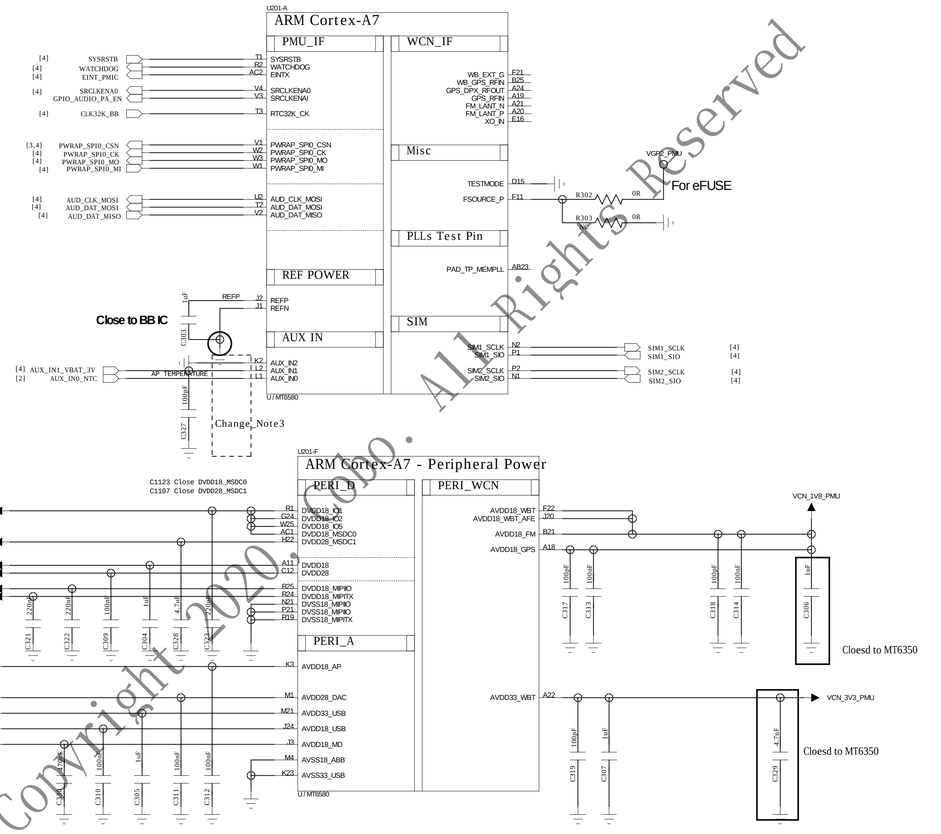
Cobo Vault

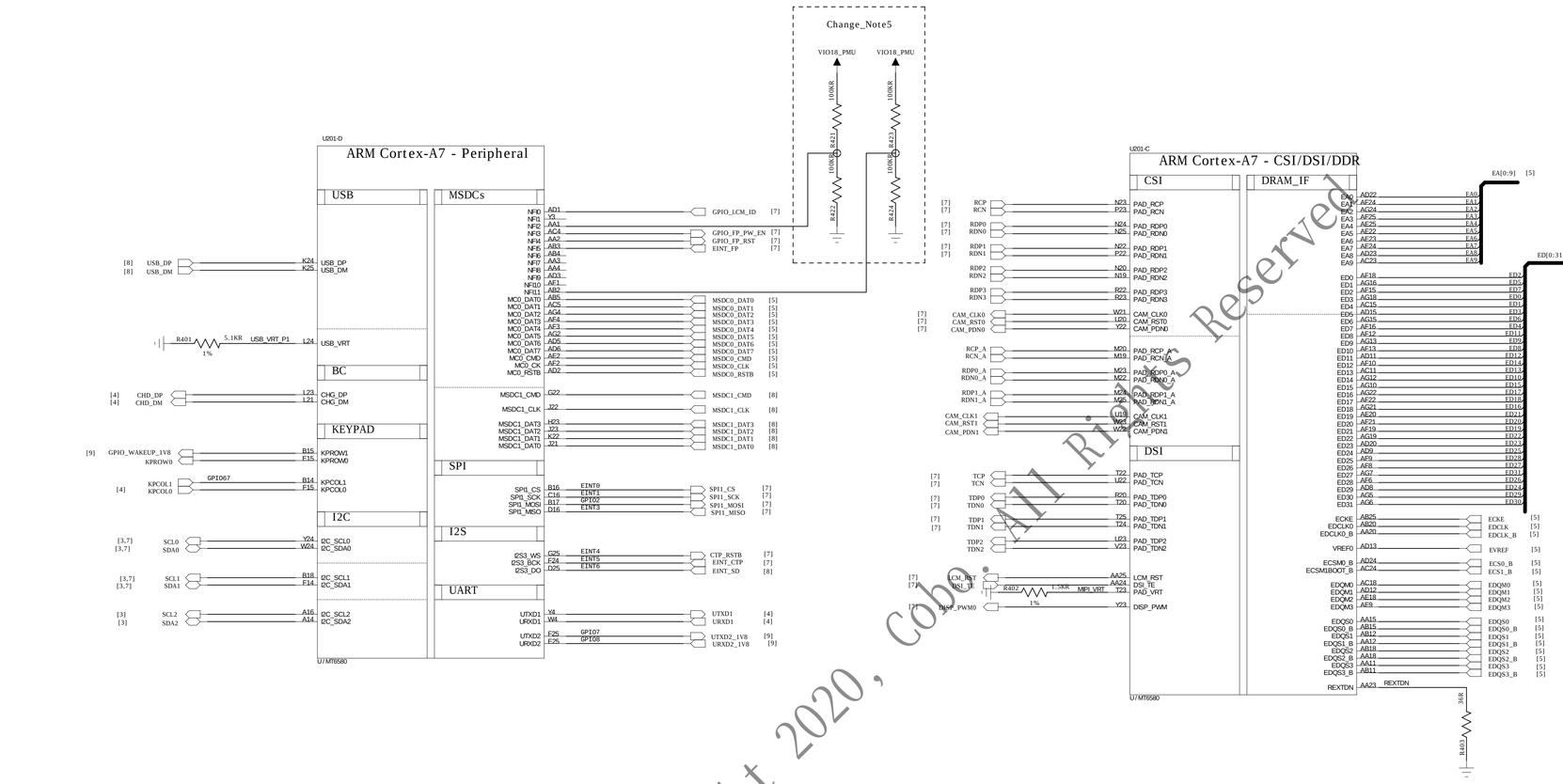
May 2020

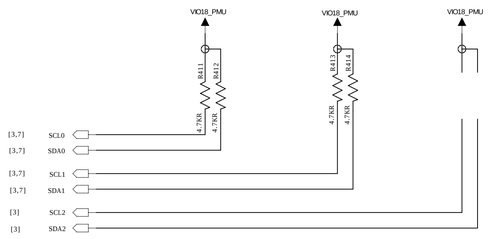




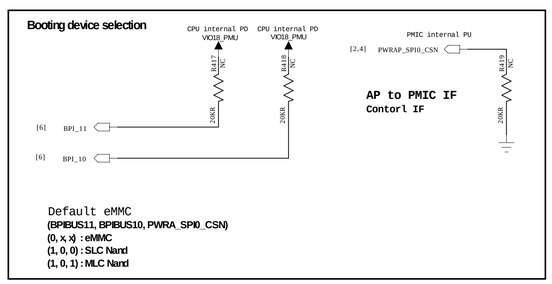
AVDD28_DAC change power source by "VA_PMU".



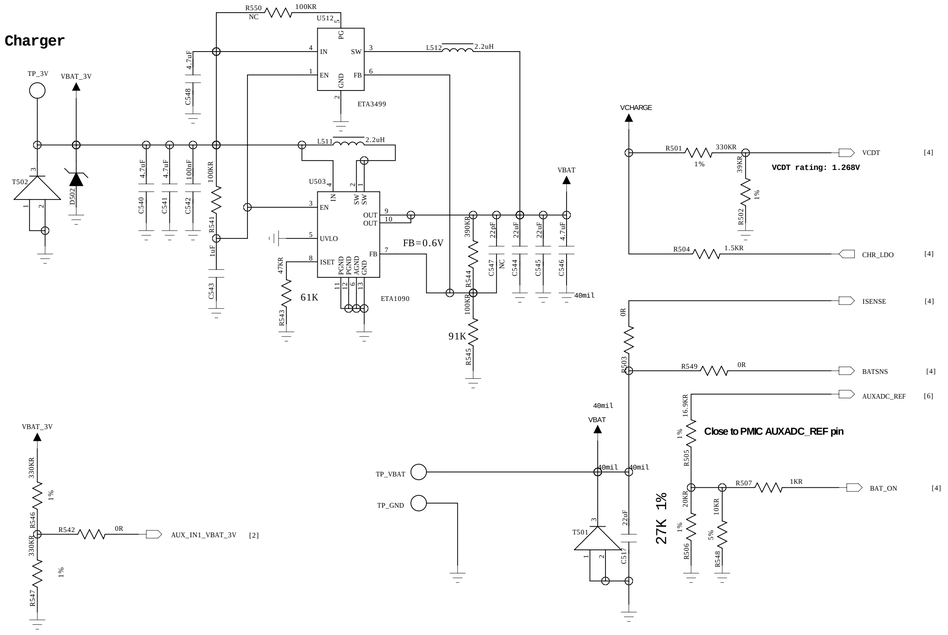




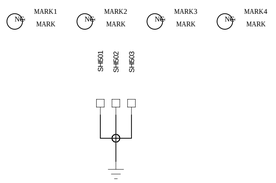
Note: 12-1

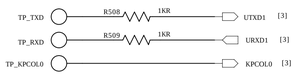


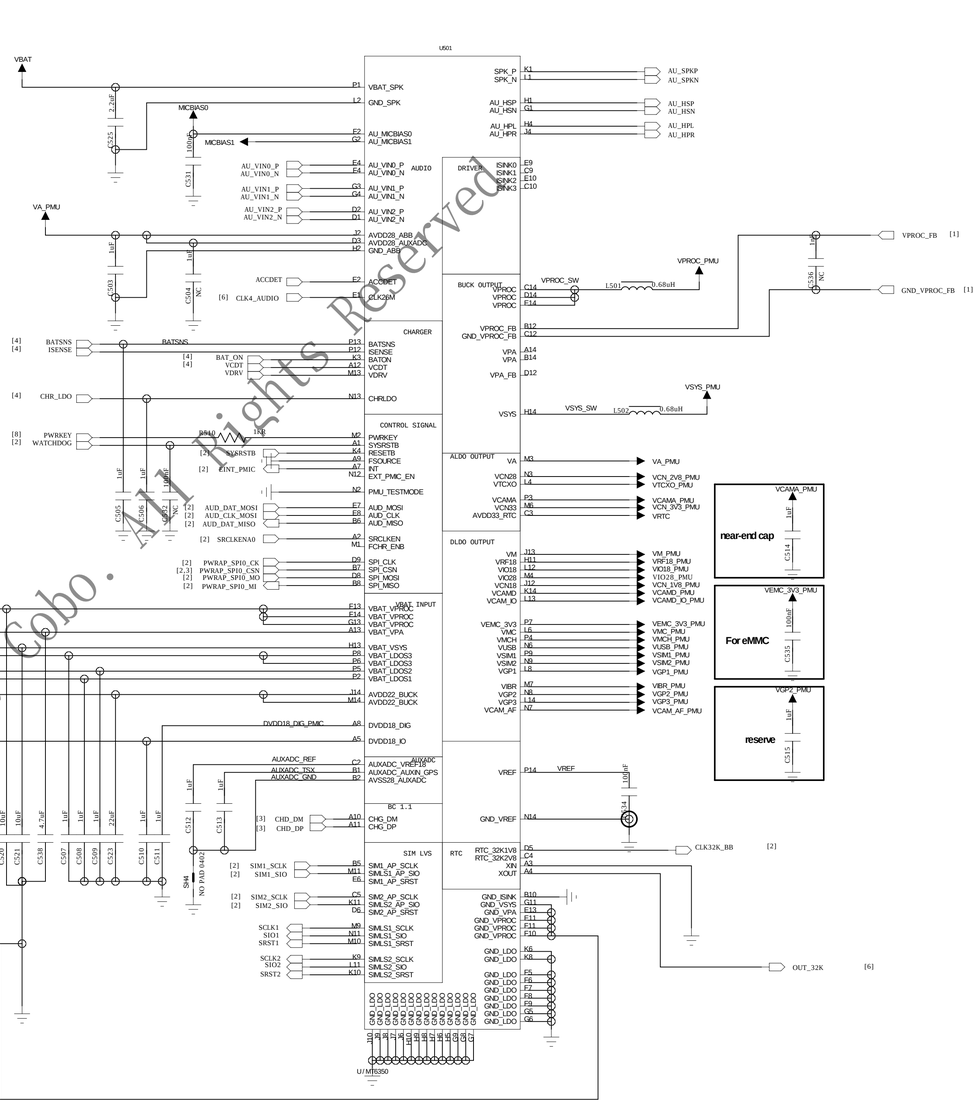
Charger

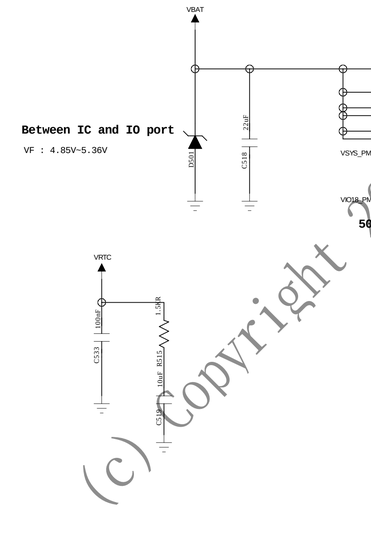


if battery NTC is 10kohm, R2107=16.9K, R2109=27K
if battery NTC is 47kohm, R2107=61.9K, R2109=100K

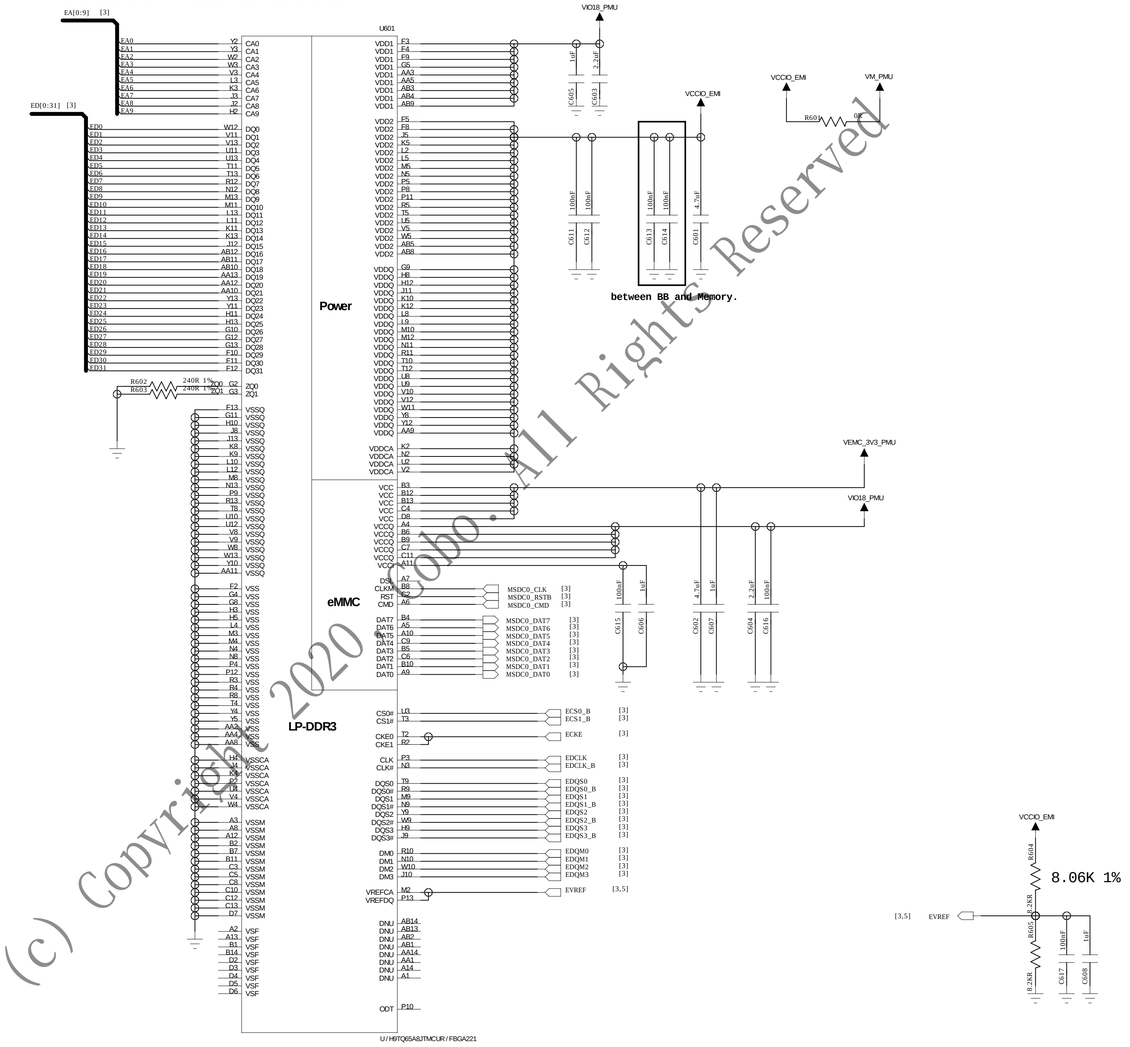


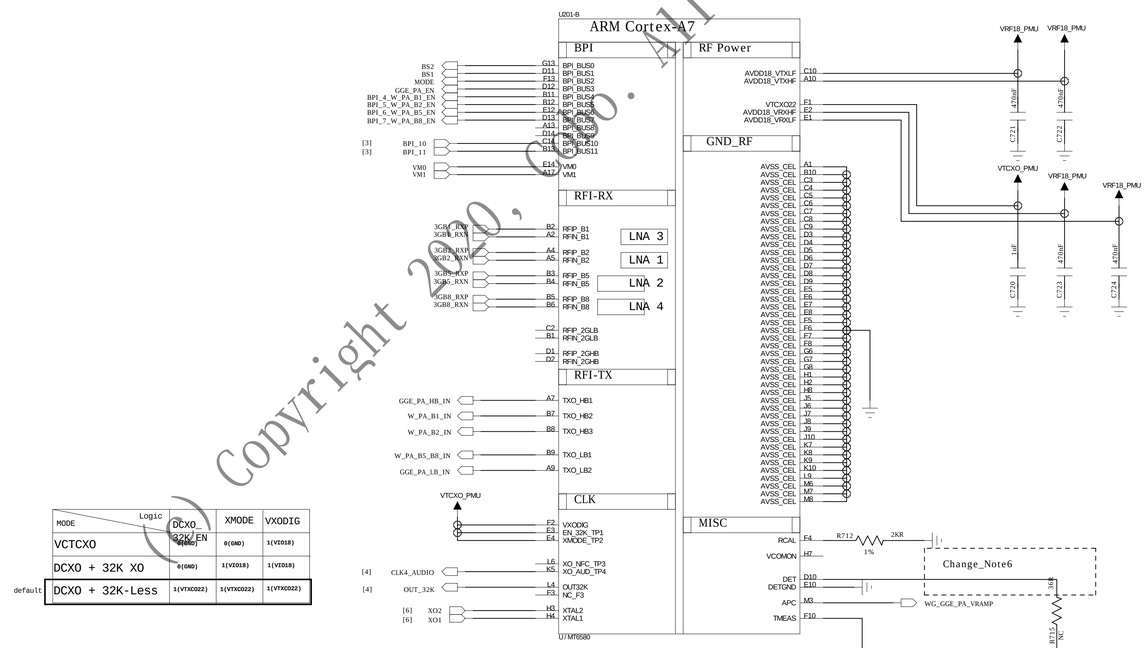




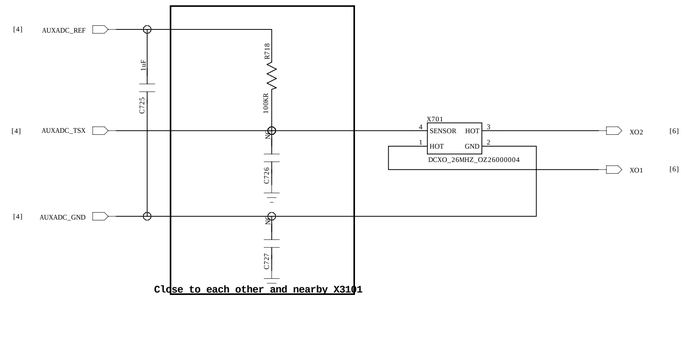


eMMC+LPDDR3





MODE \ Logic	DCXO_32K_EN	XMODE	VXODIG
VCTCXO	0(GND)	0(GND)	1(VIO18)
DCXO + 32K XO	0(GND)	1(VIO18)	1(VIO18)
default DCXO + 32K-Less	1(VTXCO22)	1(VTXCO22)	1(VTXCO22)

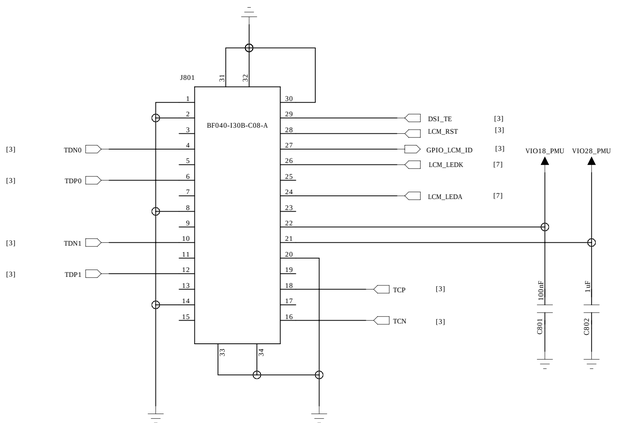


Close to each other and nearby X3101

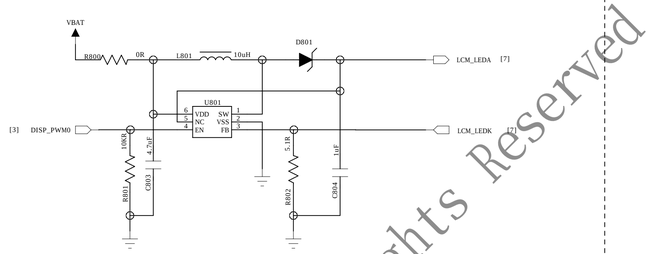
1.Route AUXADC_REF/AUXADC_TSX with 3mil trace width
2.Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding
3.Route AUXADC_GND with 15mil trace width under AUXADC_REF/AUXADC_TSX trace to provide return current path

1. Keep the crystal > 10 mm and < 30 mm away from the heat sources (Priority: 2/3G PAs > charger).
2. Keep-out at least the first two inner layers of metal including all crystal circuits.
3. Keep the crystal > 2.5 mm away from MT6580 Co-TMS.
4. Keep-out all crystal components > 0.25 mm away from the surrounding metal.
5. Do not place the TMS directly under the heat sources that on the opposite side (i.e., CPU and PAs).
6. Keep all TMS routing away from high-speed and power traces.

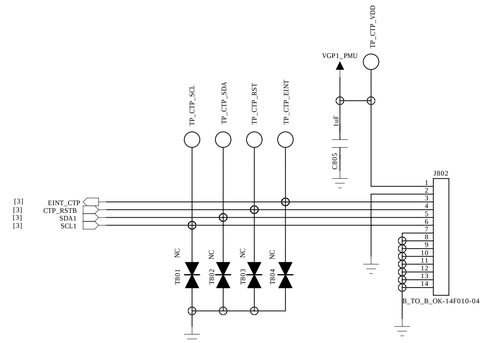
LCD



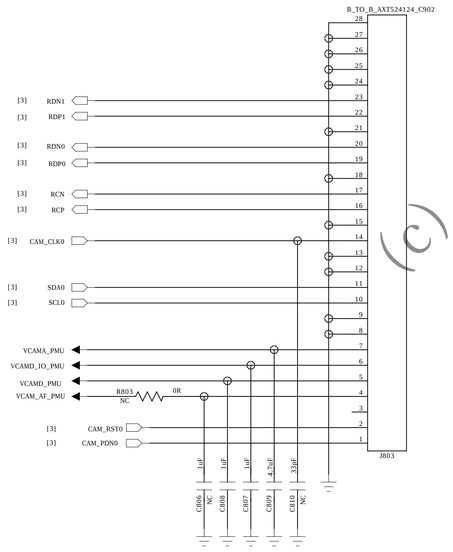
BackLight



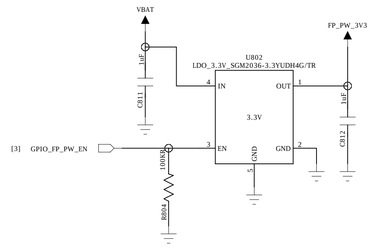
CTP

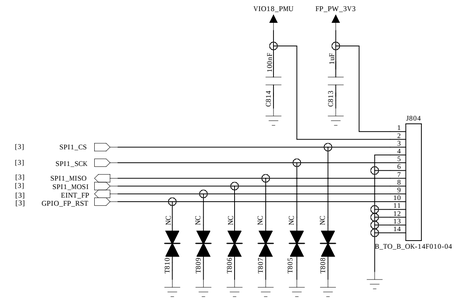


Rear Camera

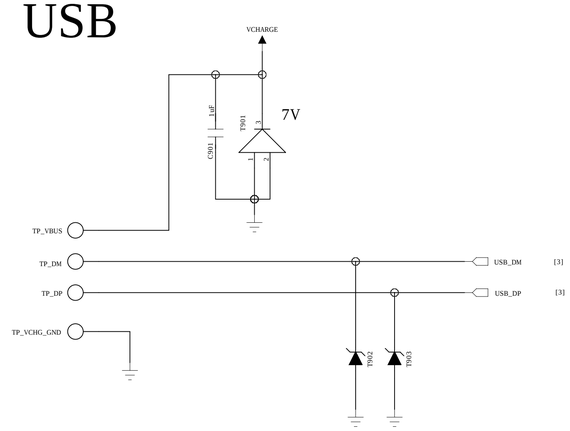


Finger Printor





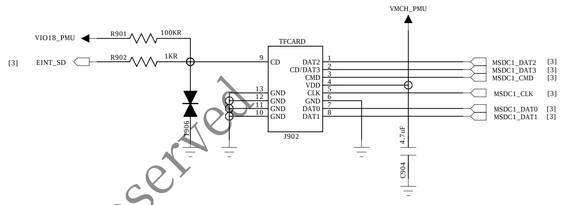
USB



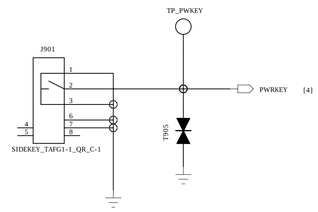
T Card

W/O Card CD=H

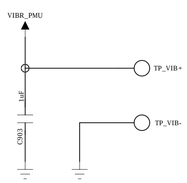
W Card CD=0



KEYS



VIB



BackUp Battery