This module contains four full duplex serial interface channels. Two channels are 50 - 9600 baud V 24 data lines. One channel is a high speed 300 - 19 200 baud V 24 line and one channel is a high speed PE tape cassette interface (1200 - 4800 baud).

CPU interfacing is performed through 2 Z-8Ø A SIO-1 controllers and one Z-8Ø A CTC.

I/O addresses are:

14H	CTC	channel A	- V 24 A timer
15H		channel B	- V 24 B timer
16H		channel C	- V 24 C timer
17H		channel D	not used by interface, all purpose system counter
18H	SIO	CASSETTE	data
19H		CASSETTE	control
1AH		V 24 A	data
1BH		V 24 A	control
1CH	SIO	V 24 B	data
1DH		V 24 B	control
1EH		V 24 C	data
1FH		V 24 C	control

high speed

To produce an exact baudrate, the CTC needs to be programmed properly. The CTC clkØ input receives a 921.6 kHz signal, the CTC clk1 and clk2 inputs receive both a 46Ø.8 kHz signal. Those signals have to be divided down. It has to be kept in mind, that the SIO needs a higher clock rate for asynchroneous protocol scanning.

The cassette interface circuit contains a phase inverter switch for recorders which invert the signal.

Transmit cable: