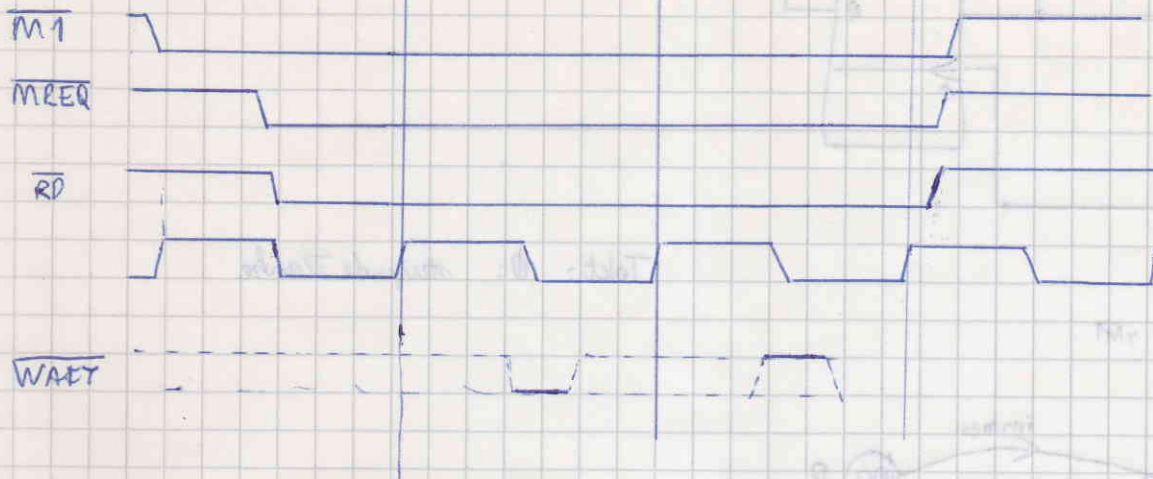


Timing diagram (M1 with wait)



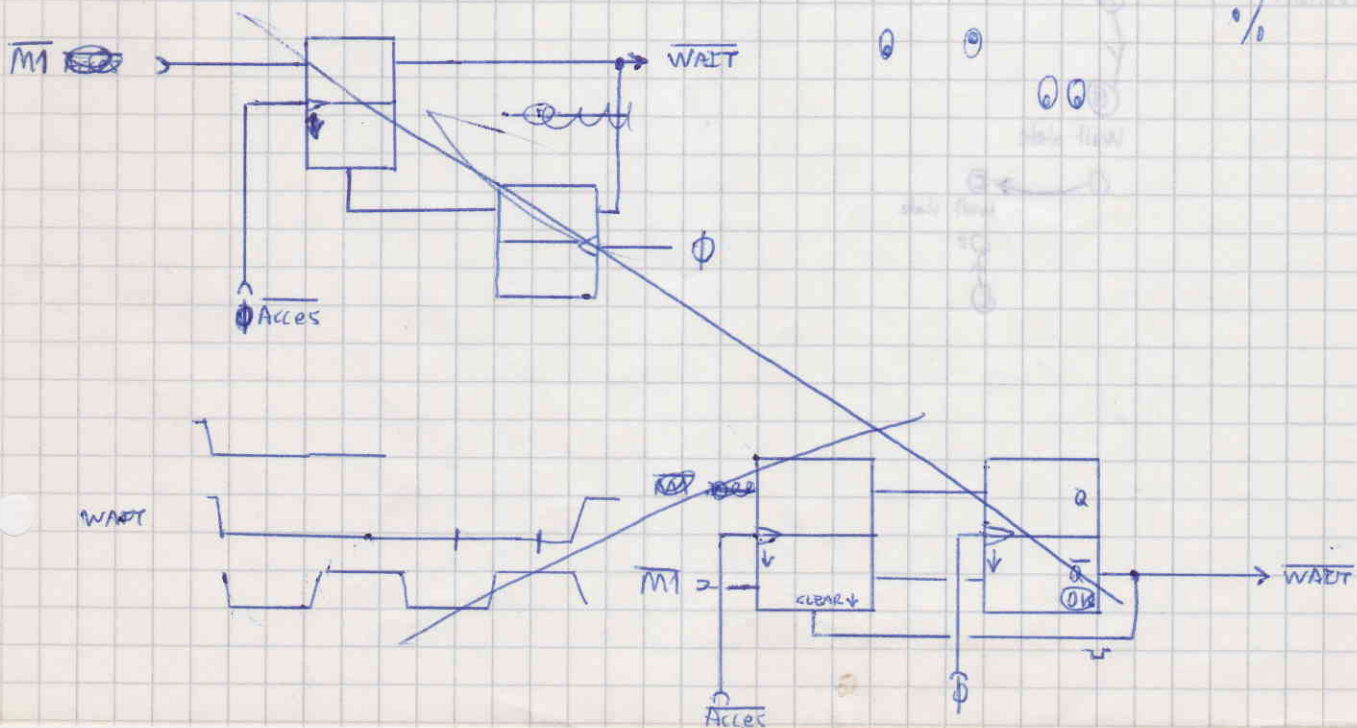
2732 EPROMS may not have a fast enough access-time to stand M1- fetch cycles. However, it will keep up with memory-cycles.

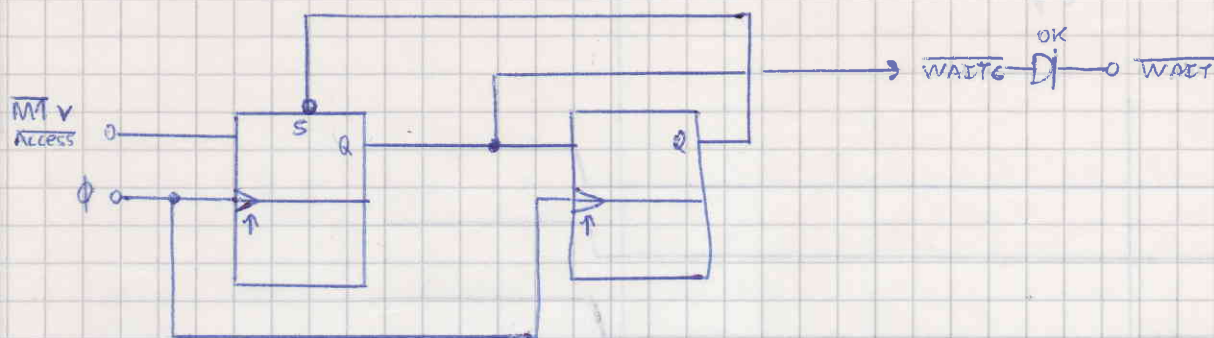
CPU - Timing:

Memory read: $\sim 450 \text{ ns}$

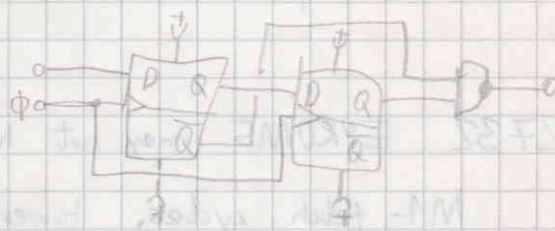
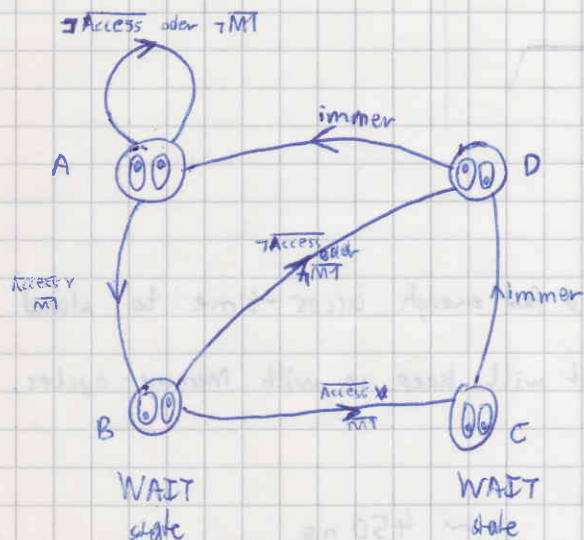
M1 read: $\sim 300 \text{ ns}$

M1 with wait: $\sim 600 \text{ ns}$





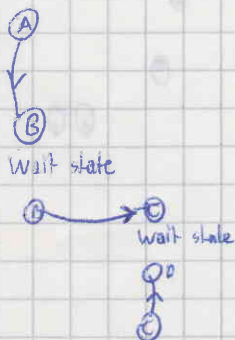
Takt: ϕ : steigende Flanke



normal cycle:



activation:



recommended: 7474 Flip-Flops