

#### **MARWADI UNIVERSITY**

#### **Faculty of Technology**

Computer Engineering / Information Technology B.Tech

SEM: 4 MU FINAL REMEDIAL DECEMBER: 2022

Subject: - (Operating System) (01CE0401)

Date:- 27/12/2022

Total Marks:-100 Time: - 02:00 to 05:00 PM

## **Instructions:**

1. All Questions are Compulsory.

2. Make suitable assumptions wherever necessary.

3. Figures to the right indicate full marks.

	stion: 1				
(a). Answer the Following MCQs. (All Questions are Mandatory)				[10]	
1 What is the disadvantage of the two level direct are two two 2					
1.	What is the disadvantage of the two-level d i. it does not solve the name	irector ii.			
	i. it does not solve the name collision problem	11.	it solves the name collision problem		
	iii. it does not isolate users from one	iv.	it isolates users from one another		
	another	1,,	it isolates asers from one another		
2.	. In a tree structure, when deleting a directory that is not empty?				
	i. The contents of the directory are	ii.	The contents of the directory are		
	safe		also deleted		
	iii. contents of the directory are not	iv.	none of the mentioned		
_	deleted				
3.	For effective operating system, when to che				
	i. every time a resource request is	ii.	at fixed time intervals		
	made iii. both (A) and (B)	•			
	iii. both (A) and (B)	iv.	none of the mentioned		
4.	. Device drivers are implemented to interface				
	i. character devices	ii.	block devices		
	iii. network devices	iv.	all of the mentioned		
5.					
	then it is called				
	i. port-mapped I/O	ii.	controller-mapped I/O		
	iii. bus-mapped I/O	iv.	none of the mentioned		
6.	Which of the following are the types of Pat		es?		
	i. absolute & relative	ii.	local & global		
	iii. global & relative	iv.	relative & local		
7.	Which one of the following is the deadlock	avoida	nnce algorithm?		
	i. banker's algorithm	ii.	round-robin algorithm		
	iii. elevator algorithm	iv.	karn's algorithm		
8.	The circular wait condition can be prevented be				
	i. defining a linear ordering of	ii.	using thread		
	resource types				
	iii. using pipes	iv.	all of the mentioned		
9.	PCB stands for				

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	i. Peer Control Block	
	ii. Process Control Block	
	iii. Process Control Batch	
10		
10.	The keeps state information about the use of I/O components.	
	i. CPU ii. OS	
	iii. Kernel iv. Shell	
(1-)	A	[10]
(b).	Answer the Following Questions in One Line. (All Questions are Mandatory)	[10]
1.	In UNIX, what is a link?	
2.	Explain Belady's anomaly	
3.	Define Multiprogramming	
4.	What is throughput?	
5.	What is turnaround time?	
6.	List out 5 scheduling criteria	
7.	what is Non-premitive scheduling?	
8.	Define Thread	
9.	Define intruder.	
10.	Define context switching	
Que	stion: 2.	
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(c)	Consider the following RAG graph and find the safe sequence if there is no deadlock		[4]	
	$P_1$ $P_2$ $P_3$ $P_4$			
One	estion: 4.			
Que	estion: 4.			
(a)	Define Security and explain it's goals	and threa	ts	[8]
(b)	What is System call? Explain types of			[8]
		OR		
(a)	Explain with the help of diagram: Dis			[8]
(b)	Enlist the services of OS and explain a	any 4 of t	hem.	[8]
Oue	ortion. 5			
Que	estion: <u>5</u> .			
(a)	Explain working of DMA with suitable	le diagran	n.	[6]
(b)	Explain kinds of file structure.	ic alagran		[6]
(c)	What is a page and what is a frame. H	ow are th	e two related?	[4]
		OR		
(a)	What is Linked allocation? Enlist adva			[6]
(b)	Compute Average Turn Around Time (TAT) and Average Waiting Time (AWT) using Round Robin by taking TQ =3 scheduling method on data given below.			[6]
	Process ID Arriv	val Time	Burst Time	
	1 5	ai iiiic	5	
	2 4		6	
	3 3		7	
	4 1		9	
	5 2		2	
	6 6		3	
(c)	Explain various Disk-Scheduling Alge	orithms ir	detail?	[4]
Que	estion: 6.			
(a)	Explain the paging hardware in detail.	ı		[8]
(b)	Differentiate User and Kernel mode.		6 1. 11 17	[4]
(c)	Given a physical memory with two fra processing the following reference str		• • •	[4]
	algorithms. Also, show which pages w			
	completion of reference string.	vould be p	resent in the memory arter the	
	1,2,3,4,2,1,5,6,2,1,2,3,7,6,3,2,1,2,3,6			
	Remember all frames are initially emp	oty, so vo	ur first unique pages will all cost one	

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	fault each.	
	OR	
(a)	Explain IPC problem known as Dinning Philosopher Problem with the help of	[8]
	suitable code.	
(b)	What is Resource? Explain its type.	[4]
(c)	A process references 5 pages A, B, C, D, E in the following order A; B; C; D; A; B;	[4]
	E; A; B; C; D; E. Assuming that the replacement algorithm is FIFO, find out the	
	number of page faults during the sequence of references, starting with an empty main	
	memory with 3 frames.	

---Best of Luck---

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## - Bloom'S Taxonomy Report -

Sub: Operating System (01CE0401)

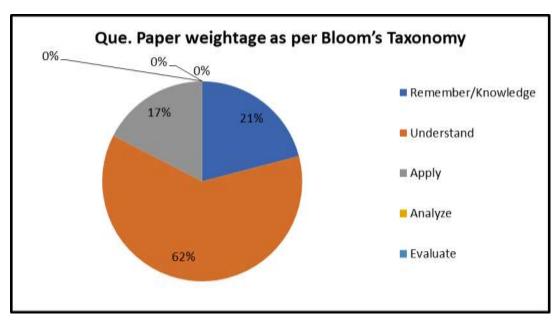
Sem.: 4th

**Branch: Computer Engineering / Information Technology** 

Que. Paper weightage as per Bloom's Taxonomy

LEVEL	% of weightag e	Question No.	Marks of Que.
Remember/Knowledg e	21%	1a(1,4,9,10),1b(1,2,3,6,8,9,10),4b	36
Understand	62%	1a(2,3,5,7,8,)1b(4,5,7),2b,3(a,b,c),4a,5(a,b,c)6 (a,b)	106
Apply	17%	2a,3c,5b,6(b,c)	30
Analyze	0%		
Evaluate	0%		
Higher order Thinking/ Creative	0%		

# Chart/Graph of Bloom's Taxonomy



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