

Assignment – 3

Subject Name	Subject Code	Issue Date	Due Date
Computer Organization and Architecture	01CE1402	21-03-2024	05-04-2024

Que. No.	Question
1	i. Draw space time diagram for 3 segment pipelines with 5 tasks. ii. A pipeline system task can be processed in a 7 segment pipeline with a clock cycle of 10 ns. Determine the speedup ratio of the pipeline for 80 tasks.
2	$X = 0.8321 \times 10^2$ and $Y = 0.5406 \times 10^3$ performs arithmetic pipeline for addition operation.
3	Explain 4 segment instruction pipeline with flow chart.
4	Draw the flowchart for Booth algorithm multiplication operation
5	Perform multiplication using booth algorithm for $(-13) \times (+11)$.
6	Explain with diagram circuit for BCD adder