Assignment – 1

Subject Name	Subject Code	Issue Date	Due Date
Computer Organization and Architecture	01CE1402	15-01-2024	30-01-2024

Que. No.	Question
1	Explain 4 register with 4 bits size common bus system with diagram.
2	Draw and explain 4-bit binary adder circuit.
3	Draw and explain 4-bit arithmetic circuit with all operations.
4	Explain Selective set, selective clear, selective complement operation with example.
5	Explain 16-bit common bus system with diagram.
6	Draw neat and clean flowchart of instruction cycle and explain it.
7	Explain memory reference instructions by taking suitable value for example.
8	Draw neat and clean flowchart of interrupt cycle and explain it.
9	How many 32K x 1 RAM chips are needed to provide a memory capacity of 256K-bytes?