Enroll. No._____

Marwadi University

MARWADI UNIVERSITY
MU-FOT
CE-FOT1 (MU), IT-FOT1 (MU)
Semester 4 - Summer

Subject : COMPUTER ORGANIZATION AND ARCHITECTURE (01CE0402)

Date : 07-Jan-2022 **Time : 1 Hours 15 Minutes** **Total Marks : 30**

Instructions :

1. **Attempt all questions.**
2. **Make suitable assumptions wherever necessary.**
3. **Figures to the right indicate full marks.**

Que.1 Answer the following questions. **[6]**

(A)

(1) What is Accumulator

(2) What do you understand by Memory Address?

(3) What is significance of Addressing mode bit in instrunction

(4) what is size of PC, DR, INPR

(5) What is need of R flag in interupt cycle.

(6) Enlist major components of CPU.

Que.2

(A) Explain 4 bit binary incrementer with figure. [6]

(B) Explain Logical Shift and Circular Shift. [6]

OR

(B) List and explain basic computer instructions in brief. [6]

Que.3

(A) Explain hardware implementation of common bus system using three state buffers. Mention assumptions if required. [8]

(B) With example demonstrate selective complement and insertion. [4]

OR

(A) Create Arithmetic Logic Shift Circuit with diagram. [8]

(B) Draw flowchart for instruction cycle. [4]

---Best of Luck---

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Difficulty Level	Weightage		No of Question	Total Marks	Question List
	Recommended	Actual			
High	20	16.67	1	8	3(A)
Low	20	0.00	0	0	
Medium	60	83.33	12	40	1(A), 2(A), 2(B), 3(A), 3(B)

Module Name	Weightage		No of Question	Total Marks	Question List
	Recommended	Actual			
Computer Data Representation & Register Transfer and Micro-operations:	50	66.67	5	32	2(A), 2(B), 3(A), 3(B)
Introduction to Computer Organization and Design:	50	33.33	8	16	1(A), 2(B), 3(B)

Blooms Taxonomy	Weightage		No of Question	Total Marks	Question List
	Recommended	Actual			
Remember / Knowledge	20	10.42	5	5	1(A)
Understand	30	43.75	5	21	1(A), 2(A), 2(B), 3(B)
Apply	25	45.83	3	22	2(B), 3(A)
Analyze	15	0.00	0	0	
Evaluate	10	0.00	0	0	
Higher order Thinking	0	0.00	0	0