
 Marwadi University Marwadi Chandarana Group 	Marwadi University Department of Computer Engineering	
Subject: Fundamental of Processors (01CE0509)	Aim:(i)Introduction to 8085 microprocessor (ii) Study of DYNA-85 trainer kit. (iii) Perform any 8085 program and learn timing diagram.	
Experiment No: 01	Date:	Enrollment No: 92201703058

Experiment-1

THEORY:

What is a microprocessor?

Microprocessor is programmable, clock driven, register based electronic device that reads binary information from storage device called memory, accepts binary data as input and process the data according to instructions and produces binary result as an output.

Why 8085 microprocessor?

It is a microprocessor with simple architecture and adequate instruction set that enable to understand the programming concepts of hardware. Having learned the basic concepts of 8085 microprocessor, student can adapt to advance microprocessor and microcontroller environments very easily.

About 8085 microprocessor


The 8085 is an 8-bit microprocessor capable of addressing 64KB of memory. The device has 40 pins, requires +5V supply, and generally operate with a 3 MHz single-phase clock.

What is DYNA -85?

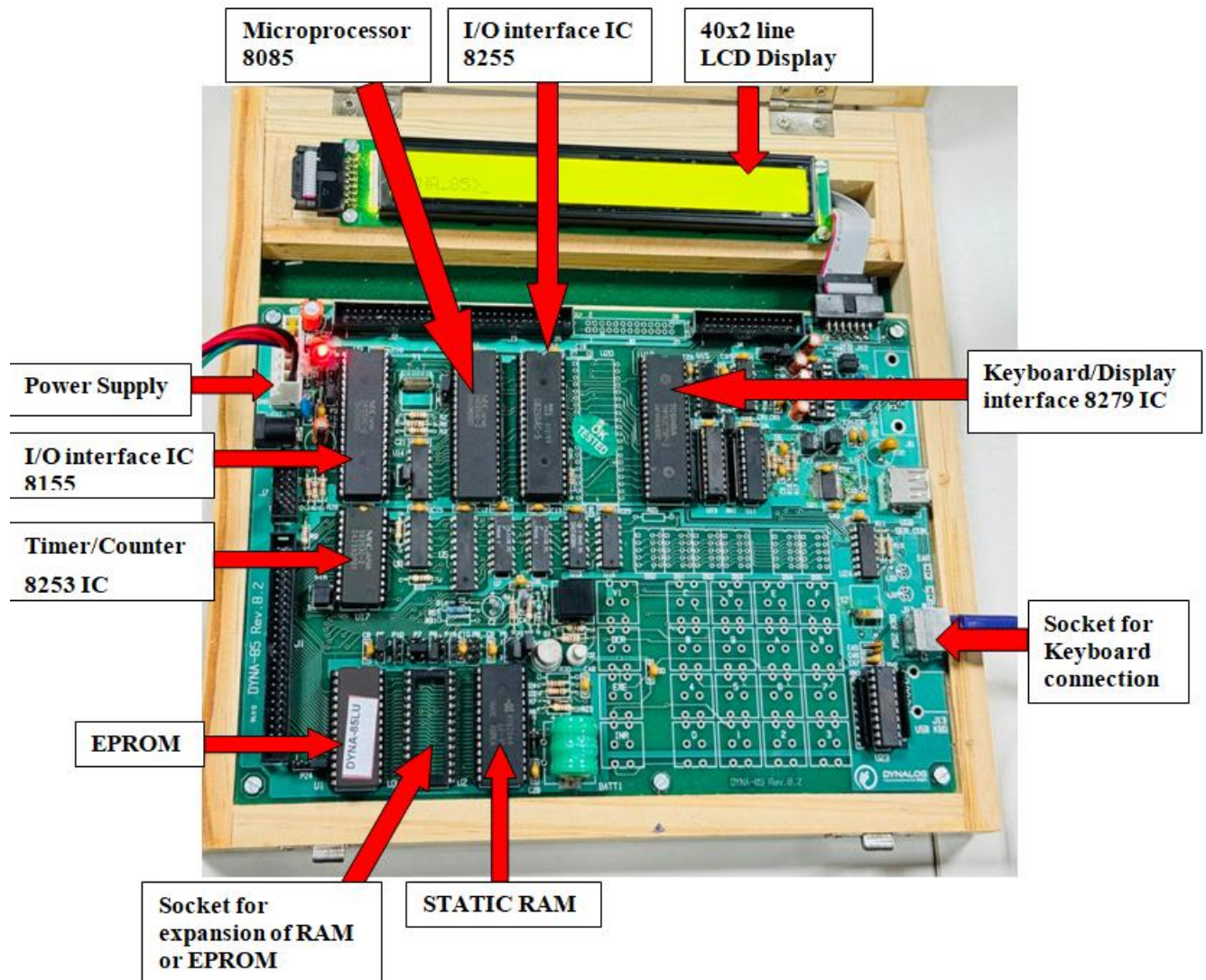
MICROFRIEND DYNA-85LU is a single board computer based on 8085A CPU designed especially for training and development applications. It is equally useful for novice as well as development engineers for studying the 8085A CPU and developing various products based on the 8085A.



APPARATUS:

1. Computer system

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MICROFRIEND DYNA-85LU HARDWARE OVERVIEW:



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FEATURES OF MICROFRIEND DYNA-85LU KIT:

➤ **CENTRAL PROCESSING UNIT:**

MICROFRIEND DYNA-85LU is based on the Intel 8085A high performance CPU operating at 3 MHz

➤ **MEMORY:**

Powerful system monitor has been provided on a **27128 EPROM** covering 16K bytes. This monitor included all standard command, functions, utility subroutines and serial interface routines as well as line assembler/ dis-assembler.

A 62256 **battery backup RAM** has been provided on board for inputting and executing programs.

One 28 pin socket is provided for memory chips so that further expansion of RAM / ROM is possible up to a maximum of 56K.

➤ **LCD DISPLAY AND KEYBOARD INTERFACE:**

An IBM compatible keyboard with 101 keys and 40x2 line LCD display is provided for interface with the system using **89C2051** micro-controller interface module. This interface is using SID/SOD lines of CPU, to simulate it in virtual serial mode.



➤ **PARALLEL I/O INTERFACE:**

46 parallel I/O lines are provided on board, 22 from 8155 and 24 from 8255. These lines are brought onto connector J2 (for 8155) and J3 (for 8255) and J8 (for 8255) optional.

➤ **SERIAL I/O INTERFACE:**

Serial I/O interface is available through RS232C compatible port. The SID and SOD lines are used under software control for serial operation i.e. for hooking up this trainer kit to serial port of a PC.

➤ **TIMER:**

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Three channels of 8253 chips, a 16 bit TIMER / COUNTER and one channel of 14 bit TIMER /COUNTER of 8155 are provided on board. All lines from 8253 are provided on connector J7 and lines from 8155 are provided on J2.

➤ **EXPANSION SLOT:**

All address, Data, Control and Hardware interrupt lines are brought on to a 50 Pin FRC connector for system expansion. These lines are un-buffered so user has to take care while expanding their system.

➤ **MEMORY MAPPING:**

The MICROFRIEND DYNA-85 has a flexible memory map, and for your convenience for program development, the RAM has useful features such as battery back-up.

FFFF	USER RAM IC 62256
C000	
BFFF	EXPANSION EPROM / RAM
4000	
3FFF	MONITOR EPROM
0000	

0000H TO 3FFFFH :


Monitor EPROM socket. Monitor 27128 is located at 0000H to 3FFFFH

4000H to BFFFFH:

This Socket is used for user expansion of EPROM & RAM. EPROM' s like 2716 / 2732 / 2732 / 2764 / 27128 / 27256 or RAM' s like 6116 / 6264 / 62256 can be installed by suitable strapping.

C000H to FFFFFH:

User RAM socket. The 8K user RAM IC 62256 is located at C000H to FFFFFH

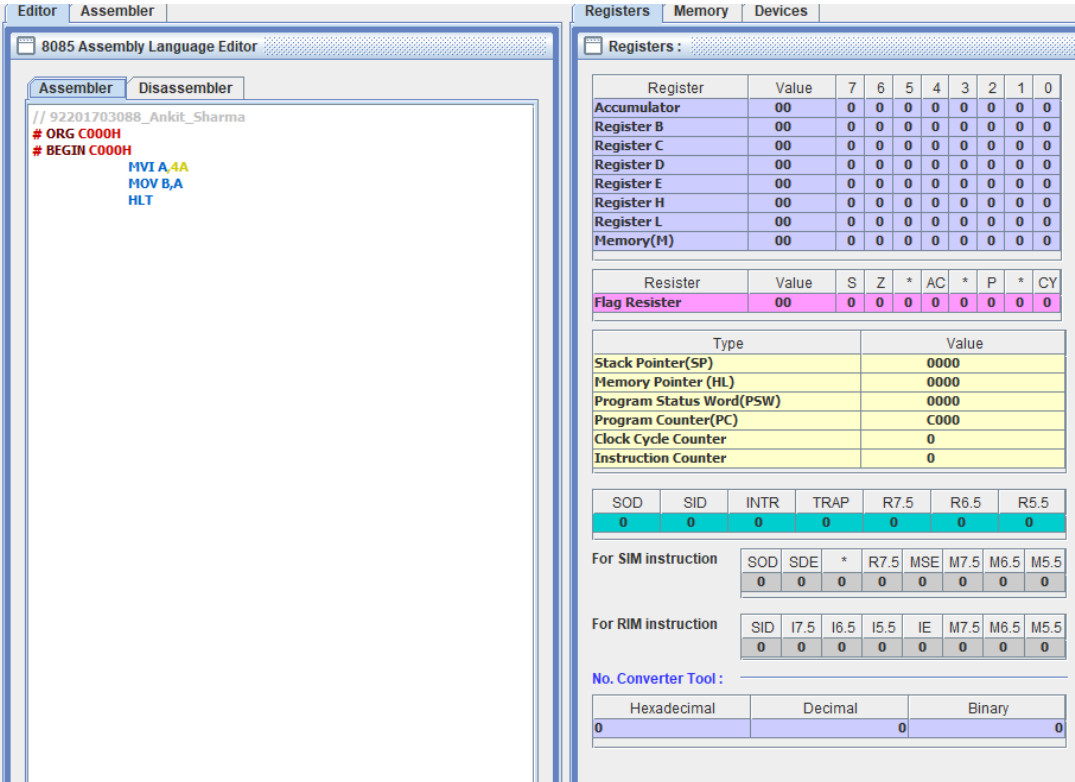
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SAMPLE PROGRAM TO LOAD AND EXECUTE ON MICROFRIEND DYNA-85LU KIT

Load the Accumulator with 44h and copy the content of register A into register B.

Address	Hex Code	Label	Mnemonics	Comments

Programing in 8085 simulator:



The screenshot shows the 8085 Assembly Language Editor and the Registers window. The Editor displays the following assembly code:

```
// 92201703088_Ankit_Sharma
# ORG C000H
# BEGIN C000H
    MVI A,4A
    MOV B,A
    HLT
```

The Registers window shows the status of the 8085 registers and flags. The Accumulator (A) contains 00, and Register B contains 00. The Flag Register shows the status of various flags (S, Z, *, AC, *, P, *, CY) as 0.


The Stack Pointer (SP) is 0000, the Memory Pointer (HL) is 0000, the Program Status Word (PSW) is 0000, the Program Counter (PC) is C000, the Clock Cycle Counter is 0, and the Instruction Counter is 0.

The SOD, SID, INTR, TRAP, R7.5, R6.5, and R5.5 registers are all 0.

The For SIM instruction fields (SOD, SDE, *, R7.5, MSE, M7.5, M6.5, M5.5) are all 0.

The For RIM instruction fields (SID, I7.5, I6.5, I5.5, IE, M7.5, M6.5, M5.5) are all 0.

The No. Converter Tool shows the conversion of 0 from Hexadecimal to Decimal (0) and Binary (0).

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8085 Assembler:

Assembler

* Address	Label	Mnemonics	Hexcode	Bytes	M-Cycles	T-States
✓ C000		MVI A,44	3E	2	2	7
C001			44			
✓ C002		MOV B,A	47	1	1	4
✓ C003		HLT	76	1	2	5

Registers :

Register	Value	7	6	5	4	3	2	1	0
Accumulator	00	0	0	0	0	0	0	0	0
Register B	00	0	0	0	0	0	0	0	0
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	00	0	0	0	0	0	0	0	0
Register L	00	0	0	0	0	0	0	0	0
Memory(M)	00	0	0	0	0	0	0	0	0

Register	Value	S	Z	*	AC	*	P	*	CY
Flag Register	00	0	0	0	0	0	0	0	0

Type	Value
Stack Pointer(SP)	0000
Memory Pointer (HL)	0000
Program Status Word(PSW)	0000
Program Counter(PC)	C000
Clock Cycle Counter	0
Instruction Counter	0

8085 Assembler:

Assembler



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✓ C003		HLT	76	1	2	5

Registers :

Register	Value	7	6	5	4	3	2	1	0
Accumulator	44	0	1	0	0	0	1	0	0
Register B	44	0	1	0	0	0	1	0	0
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	00	0	0	0	0	0	0	0	0
Register L	00	0	0	0	0	0	0	0	0
Memory(M)	00	0	0	0	0	0	0	0	0


Register	Value	S	Z	*	AC	*	P	*	CY
Flag Register	00	0	0	0	0	0	0	0	0

Type	Value
Stack Pointer(SP)	0000
Memory Pointer (HL)	0000
Program Status Word(PSW)	4400
Program Counter(PC)	C003
Clock Cycle Counter	16
Instruction Counter	3

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MVI A,44 timing diagram:

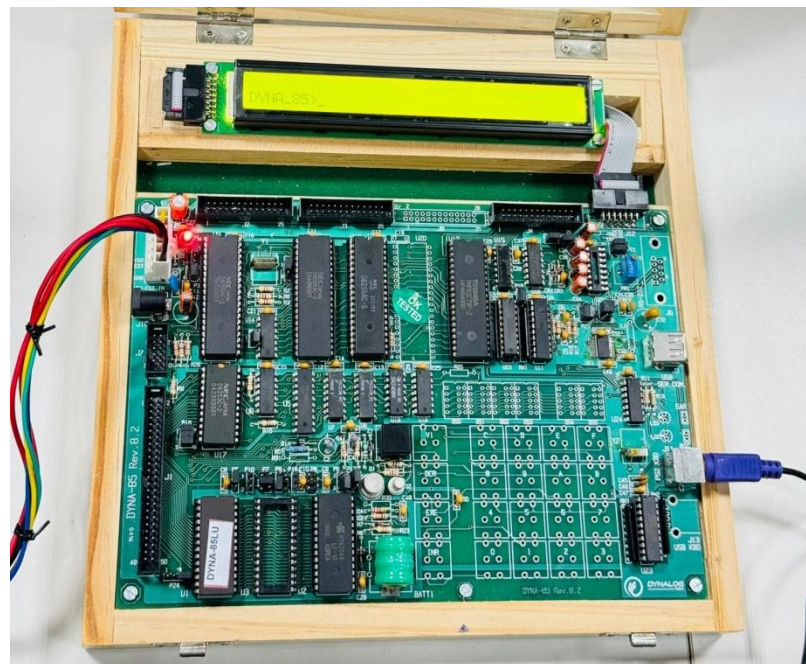
MOV B,A timing diagram:

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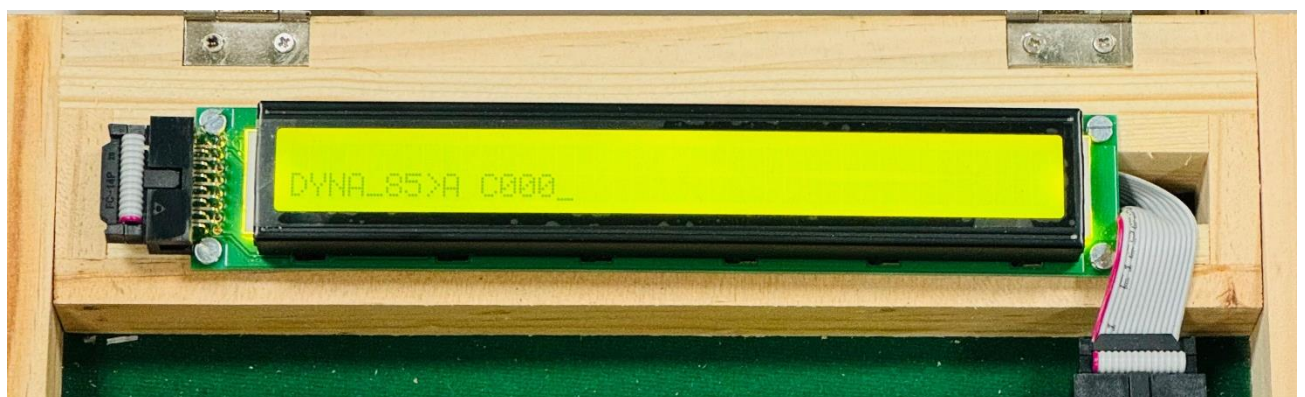
PROCEDURE TO LOAD PROGRAM ON MICROFRIEND DYNA-85LU KIT

STEP 1: Connect the output line of SMPS to the Microprocessor kit

Obs. 1: DYNA_85 appears on the LCD display screen




Step 2: Press A followed by the address location. For ex: A C000



Step 3: Write the programming

MVI A, 44 <Enter>

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MOV B, A <Enter>



RST 1 <Enter> <Esc>



Step 4: To run the program. Write GO "Address" For Ex: GO C000. Press Enter and Space



Step 5: To observe the output in the register, Type R and press enter

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Obs 2: The output will be displayed on LCD display



Conclusion: