




KULDEEP SINGH

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OBJECTIVE

Electronic engineer aspiring to work in a VLSI domain. I have sound knowledge of Verilog and currently I am learning System Verilog. I have excellent problem-solving skills and ability to perform well in a team. Seeking a job to help a company in their product development, as well as grow and develop my own skills.



EDUCATION

M.Tech | Jaypee Institute of Information Technology

2019 – 2021

Specialization in Microelectronics system and Embedded technology having CGPA 8.1

B. Tech | Ajay Kumar Garg Engineering College

2011 – 2015

Specialization in Electronics and communication having CGPA 7 or percentage 66.5%



EXPERIENCE

Intern | Semi Design

2020 – 2021

Taking In-Depth knowledge in the area of Verilog, System Verilog and UVM



SKILLS

- Verilog INTERMEDIATE
- System Verilog BEGINNER
- Python BEGINNER
- Embedded C INTERMEDIATE



SOFTWARE

- ModelSim HDL Simulation
- Tanner T-Spice
- µVision IDE – Keil
- Proteus Design Suite
- Sentaurus TCAD