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FROM RESUME TO RECRUIT: AI'S ROLE IN PLACEMENT

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RESUME TO RECRUIT: AI FOR PLACEMENTS

AI is revolutionizing every aspect of our lives, and the hiring process is no exception. Companies are moving away from traditional recruitment methods and are enthusiastically adopting AI tools to handle this critical part of talent acquisition. The theme for this issue, 'From Resume to Recruit: AI's Role in Placement,' has been selected to shine a light on this significant transformation.

Now that placement season is around, students must be ready for the impending AI-centered recruitment trends. This theme aims to update students on these trends and persuade them to change the mode of their preparation endeavors so that they are bowel ready for the emerging recruitment system. The use of technology in this area has brought automation and intelligence not only in resume shortlisting and candidate assessment but even in the conduct of interviews. There are currently documents and tools such as AI-enabled ATS that are used at the resume level, while ML algorithms are used to analyze the candidates that the company intends to employ. Moreover, AI-driven platforms offer interview simulations, helping candidates practice and improve their responses, while companies use AI to assess communication skills, emotional intelligence, and technical knowledge.

A large number of companies have started using AI-driven platforms for conducting coding tests wherein they evaluate not only the technical skills of a candidate but also their problem-solving ability, efficiency, and ingenuity undertaken by aspiring minds. The coding round is usually proctored through AI tools that help to keep an eye on the candidates if any malpractice is being done, hence maintaining fairness in evaluation. Also, AI algorithms may consider the quality of code by looking at factors such as time complexity, space efficiency, and adherence to best practices. Therefore, students must be well-prepared not only to solve coding challenges but to be able to write clean and efficient reusable dynamic memory management code as well- some of the evolving standards AI platforms are now enforcing.

As AI keeps transforming the hiring scenario, students need to accept technological aspects to change their strategies of preparation. The shift from traditional recruitment methods to AI-driven processes offers both challenges and opportunities. With clear knowledge of the AI tool influences in each stage throughout the hiring process — right from resume screening to interviews — students can align themselves properly for success. This issue's theme, 'From Resume to Recruit: AI's Role in Placement,' serves as a timely reminder to stay informed, stay prepared, and leverage AI to navigate the evolving placement journey with confidence.



Prachit Paralikar
Deputy Student Chief Editor

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EDITORIAL

This quarter has been marked by a series of outstanding achievements and notable events that highlight the continued excellence and innovation at VESIT. The quarter began with the exciting news that VESIT secured an impressive 2nd place in Maharashtra and 21st across India in the EducationWorld ranking. This recognition stands as a testament to the institute's unwavering commitment to excellence in education, research, and innovation. Adding to this wave of accomplishments, Anurag Tripathi (TE-CMPN) made the institution proud by securing an impressive 67th rank in GATE 2024. His remarkable performance in this highly competitive exam is a reflection of the talent and determination fostered within our community. Continuing with a strong spirit of participation, fifteen MCA students from VESIT represented the college at the prestigious Version 24 event hosted by NIT Trichy from 21st to 23rd March 2024. Their efforts were rewarded with accolades, contributing to the growing reputation of our institution.

Innovation was at the forefront with the Department of Electronics & Telecommunication Engineering hosting 'IDEAthon-2024', an event dedicated to nurturing creative project ideas. Distinguished speakers Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering), Mrs. Smita Jangale (Former Deputy Head, Department of Information Technology), and Mrs. Shoba Krishnan (Former Head, Department of Electronics and Telecommunication Engineering) inspired the students to think beyond conventional boundaries. On 23rd March 2024, the Department of Electronics and Computer Science hosted the 'Sci-Tech Fair', an event aimed at promoting creativity and scientific inquiry. Over 50 students presented innovative projects at this event. Furthering the spirit of discovery, the Department of Artificial Intelligence and Data Science, in collaboration with VESIT-IIC and VESIT-IQAC, organized an exciting project exhibition titled 'Discover the Future'. Final-year students exhibited over 15 cutting-edge projects, reflecting the latest advancements in AI, Machine Learning, and Data Science. 'TechTube' was an competition organized by the Department of Artificial Intelligence and Data Science.

In the same vein, the Department of Computer Engineering hosted 'Pradarshini 24', their annual Department Technology Day. Held on 22nd-23rd March 2024, the event brought together innovative projects from third-year and final-year

students, providing them with valuable feedback on their ideas. It was a learning experience for the participants, helping them refine their skills and preparing them for future challenges in the tech world. 'BuzzPro'24', an initiative by the Department of Computer Engineering, encouraged second-year students to delve into the world of technical writing. Organized by the CMPN Research Forum Team, this event saw active participation from both students and faculty, with a total of 180 student participants over a month-long period.

Another notable event, 'Avinya-2024', organized by the Electronics and Telecommunication Department, was celebrated as Technology Day. The event showcased the harmonious fusion of technology and innovation, further solidifying VESIT's reputation as a hub for technological advancements. Creativity and innovation took center stage at the 'Technovation 2024' event, hosted by the Department of Automation and Robotics. Held over two days in February and March 2024, this 18-hour project exhibition pushed the boundaries of engineering creativity.

As technology continues to evolve, so too does the need to stay informed about its latest developments. In April 2024, the Department of Electronics and Computer Science organized a 'Session on Scope Opportunities of VLSI in India'. This informative session was a valuable opportunity for third-year students to engage with the newest technologies in the VLSI field. Workshops played a key role in expanding students' knowledge this quarter. 'The Data Science Bootcamp', conducted by the Department of Artificial Intelligence and Data Science, took place from 20th May to 21st June 2024. The workshop focused on the theme of Data Processing and Visualization and ML (Machine Learning) with MATLAB. Distinguished speakers of this workshop were K. Grace Kennedy, and Andrea Bayas (Training Engineer, Mathworks).

The Master of Computer Applications department contributed with their workshop, 'Mastering the Internet of Things'. Held from 18th to 21st June 2024, this hands-on course offered practical insights into IoT applications and was a hit among the students. In collaboration with VESIT-IIC and VESIT-IQAC, an online Microsoft Azure workshop was organized by Codecell Tinkerers from the Department of Computer Engineering, where participants were not only exposed to the platform but also received \$100 in Azure credits to continue exploring its features post-workshop. The



Gaurang Desai
Student Chief Editor

'Road to GDSC' session, 'Build with AI' workshop & 'LLM Insights with Gemini Pro' all three organized by GDSC and CodeCell Tinkerers, added further value to the tech-driven initiatives at VESIT.

As the academic year drew to a close, ISA-VESIT and Coding Ninjas teamed up to host the 'Campus Hero Webinar' on 22nd June 2024. The webinar, followed by a coding contest, was a hit among participants, reinforcing the culture of continuous learning and competitive spirit at VESIT. The quarter culminated in the grand 'Degree Distribution Ceremony', celebrating the hard-earned accomplishments of graduates from all streams. Distinguished guests honored the occasion, leaving students inspired for the next phase of their journey.

On 10th May 2024, the Training and Placement Cell organized the 'VESIT Job Fair'. The event aimed at bridging the gap between job seekers and potential employers by providing opportunities for career exploration, offering career development resources, and assisting employers in candidate identification. The 'POSH Awareness Programme' organized by the VESIT Women Development Cell and Internal Committee in association with VESIT-IQAC and VESIT-IIC was held on 7th June 2024. The event took place at the VESIT Auditorium, aiming to educate attendees about the Prevention of Sexual Harassment (POSH) Act at the workplace. VESIT Library organized a 'Quiz Competition' on 21st June 2024 on the occasion of National Yoga Day. More than 100 participants participated in the same. Finally, VESIT's technical societies wrapped up the year with a vibrant 'Symposium', marking the end of an era for their leaders and members. This symposium not only celebrated teamwork and dedication but also set the tone for future leaders to take charge and continue the legacy of excellence at VESIT. Mayuresh Sapotdar, an alumnus of VESIT, currently serving as the Executive Director at Morgan Stanley, shared valuable insights into his career journey with us which we have documented in VESIT Diaries.

HIGHLIGHTS

V.E.S. Institute of Technology (VESIT) has earned remarkable recognition, securing 2nd place in Maharashtra and 21st across India in the EducationWorld ranking. This achievement underscores the institute's commitment to excellence in critical domains. The institute fosters a research-intensive environment, encouraging groundbreaking projects that address real-world challenges.

[Read More on Page 06](#)

March marked the calendar with the most prestigious Degree Distribution Ceremony, celebrating the remarkable journey of graduates from all streams. The event was highlighted by the presence of the distinguished chief guests, who captivated the audience with their enlightening speeches and guidance. It was a moment of immense pride and joy, honoring the transformative academic journey that these graduates embarked upon at VESIT.

[Read More on Page 17](#)

Pradarshini'24 was a standout event for the Department of Computer Engineering. It featured stellar student projects judged by industry experts, including VESIT alumni. Winners were announced after thorough assessments, the event being lauded for its insightful evaluations and high student and faculty engagement, culminating in a successful and impactful showcase.

[Read More on Page 09](#)

Electronics and Telecommunication Department made a powerful mark with Avinya-2024 on Technology Day, highlighting groundbreaking projects. Top honors went to Team B5 for their Ethernet protocol using FPGA and Team B17 for their adaptive battery management system, setting a high bar for future innovations at VESIT.

[Read More on Page 10](#)

New career boulevards were opened up for VESIT students with the Job Fair conducted by the Training And Placement Cell. The event was aimed at bridging the gap between companies and talented students seeking employment opportunities. These interactions offered students a chance to discuss their career goals, learn more about company cultures, and gain advice on industry trends. Representatives from various industries took the stage to put forth key aspects of their organizations such as company culture, future career prospects, and recruitment processes. The Job Fair emerged as a highly effective platform that seamlessly aligned the objectives of career exploration, professional networking and recruitment assistance.



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VESIT Job Fair organised by the VESIT Training and Placement Cell

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Department of Electronics and Computer Science organized a session titled "Scope/ Opportunities of VLSI in India" for third-year students. Led by Mr. Yogesh Pandit and featuring Ms. Archita Malgaonkar from IIT Hyderabad, the session covered the latest VLSI technologies, semiconductor technology, and career prospects in the VLSI industry.

[Read More on Page 11](#)

Capping off a year rich in innovation and teamwork, the technical societies hosted a vibrant Symposium to bid farewell to their leaders and members. Distinguished guests engaged the audience in insightful discussions, celebrating the dedication that drove each society's success. The self-organized events showcased the creative spirit making this a truly memorable finale.

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IDEAthon-2024 organized by the Department of Electronics and Telecommunication provided participants with hands-on experience in teamwork, innovative thinking, and effective communication. The event emphasized the importance of entrepreneurship and critical thinking, making it a resounding success that reinforced the department's commitment to nurturing innovation and creativity.

[Read More on Page 07](#)

The POSH Awareness Programme on 7th June 2024, organized by VESIT's Women Development Cell and Internal Committee, educated attendees on the Prevention of Sexual Harassment (POSH) Act. Keynote speaker Swayamjyoti Char covered the legal aspects, forms of harassment, and best practices for a safe workplace.

[Read More on Page 21](#)



Speaker of the event - Mrs. Swayamjyoti Char



VESIT Faculty Members along with Dr. J. M. Nair (Principal, VESIT)

ACHIEVEMENTS

VESIT ranked 2nd in Maharashtra State Ranking

V.E.S. Institute of Technology (VESIT) has earned remarkable recognition, securing **2nd place in Maharashtra** and **21st across India** in the EducationWorld ranking. This achievement underscores the institute's commitment to excellence in critical domains.

A significant factor in VESIT's high ranking is the competence and dedication of its faculty. Renowned for their expertise and innovative teaching methods, the faculty enhances the learning experience and prepares students to excel. The institute also prioritizes faculty welfare and development, ensuring educators have access to professional growth opportunities and resources. Research and innovation are central to VESIT's academic philosophy. The institute fosters a research-intensive environment, encouraging groundbreaking projects that address real-world challenges.

This commitment to research excellence has played a pivotal role in VESIT's esteemed ranking.

VESIT's curriculum reflects a well-rounded educational approach, incorporating the latest industry trends and technological advancements.

Rank	Maharashtra	Total score (2300)
India 2024		
18 1	Bharatiya Vidya Bhavan's Sardar Patel College of Engineering, Mumbai	1925
21 2	Vivekanand Education Society's Institute of Technology, Mumbai	1893
21 2	KJ Somaiya College of Engineering, Somaiya Vidya Vihar University, Mumbai	1893
23 3	Mukesh Patel School of Technology, Management and Engineering (NMIMS Deemed University), Mumbai	1891
26 4	Vishwakarma Institute of Information Technology, Pune	1850

V.E.S. Institute of Technology (VESIT) secured 2nd place in Maharashtra and 21st across India in the EducationWorld ranking.

VESIT's robust industry interface bridges the gap between academia and industry, providing students with valuable internship opportunities

-Rohil Gujarathi

and practical experience, enhancing their employability. The institute's impressive placement record, supported by a dedicated placement cell, secures job opportunities for graduates in top-tier companies. Additionally, VESIT offers state-of-the-art infrastructure and facilities, supporting both academic and extracurricular activities.

VESIT's commitment to internationalism highlights its global outlook, encouraging international collaborations and exchanges. Effective leadership and governance maintain high standards of academic and operational excellence. The diversity of programs offered caters to various interests and career aspirations, further contributing to its prestigious ranking.

VESIT's dedication to providing a high-quality, innovative, and industry-relevant education has rightfully earned it a top position in Maharashtra and a prominent rank in India, establishing its status as a leading engineering college in the country.

AIR 67 in GATE'Cs

The Gratitude Aptitude Test in Engineering (GATE) is a prestigious national-level examination that assesses candidates for a comprehensive understanding of various undergraduate-level subjects in Engineering, Technology, Science, Architecture, and Humanities. GATE 2024 was organized by the Indian Institute of Science (IISc), Bengaluru.

We are thrilled to share the news of **Anurag Tripathi**'s (TE-CMPN) exceptional achievement in the GATE 2024. Anurag Tripathi from the Department of Computer Engineering, has secured an impressive **67th rank** in this highly competitive exam. Anurag's success is a testament to his dedication, perseverance, and academic excellence. His exemplary performance not only reflects his commitment to learning but also highlights the quality of education and support provided by our institution.

VESITConnect connected with Anurag Tripathi to know about his GATE exam journey. He started his GATE preparation by the end of the 3rd semester, when he joined offline classes at VisionGATE . He adds that "Initially, my target

was to crack GATE in 2025 with a good rank to get into one of the older IITs. However, after a couple of months, I began to realize that I could achieve it in one year itself."



Anurag Tripathi (TE-CMPN) secured an impressive 67th rank in GATE

Speaking about his study strategy he stated "My study strategy was simple and easy for anyone to follow. I didn't adhere to the stereotype that I needed to study for 10-14 hours to succeed. Instead, I studied whenever I had free time (free time being when I wasn't doing any college-related work). My first priority was to revise in a round-robin fashion. Another important factor that helped me secure this rank was my peer group, which is crucial."

The key study resources he relied on for effective preparation for major subjects like OS,

-Priyanka Tiwari

DSA, CN, and COA were GATE CSE PYQs from the last 22 years, test series and the textbook questions from reference books When asked about the most challenging parts of the GATE syllabus , and how did he overcome them , he answered "I treated every subject as equally important, but I can assure you that the subject of Computer Networks will require some extra time to understand compared to other subjects. It's a vast subject with crucial concepts in every chapter, so you can't think of skipping anything, as all the topics are interconnected."

As Anurag talks about his future plans he states "Now that I have this rank, I am considering pursuing an M.Tech in CSE from IIT Bombay. Dreams really do come true! Although I couldn't do my B.Tech at an IIT, I now have the chance to do my M.Tech at the best university in India, according to this year's QS Ranking."

His advice to all students preparing for GATE is " You should trust your teacher completely and follow what they tell you to do. Complete all the questions and assignments they give you on the same day. They have much more experience than us."

VESIT Excels at Version 24

National Institute of Technology (NIT) Trichy hosted **Version 24**, a premier event for MCA students, from **21st March to 23rd March 2024**. Version 24 went beyond just competitions, offering an immersive experience that fostered talent, healthy competition, and creativity.

The event featured various activities, including coding challenges, hackathons, workshops on Generative AI, and interactive games like the rapid-fire Squid Quiz and the digital treasure hunt Map Quest. A thought-provoking guest lecture on Generative AI kicked off the event, setting the stage for intellectual exploration.

Fifteen MCA students from VESIT



(L-R): Aman Shrivastav (MCA-FY), Rohit Jha (MCA-FY), Shreya Walavalkar (MCA-FY), Prerna Jaiswal (MCA-FY) and Lucky Vaswani (MCA-FY)

-Tejasvini Bachhav

participated in the event and achieved impressive results. **Aman Shrivastav** (MCA-FY) secured the **first position** and **Rohit Jha** (MCA-FY) secured the **second position** in Map Quest, while **Prerna Jaiswal** (MCA-FY) secured the **first position** and **Shreya Walavalkar** (MCA-FY) secured **third position** in the Immerse Craft workshop. Additionally, **Lucky Vaswani** (MCA-FY) placed second in the Game Fiesta. Version 24 concluded with "**Destinite**," a mock interview round for the top performers, providing valuable career preparation. Overall, Version 24 was a memorable experience that showcased talent, encouraged collaboration, and offered a glimpse into the future of computer science.

TECH COMPETITIONS

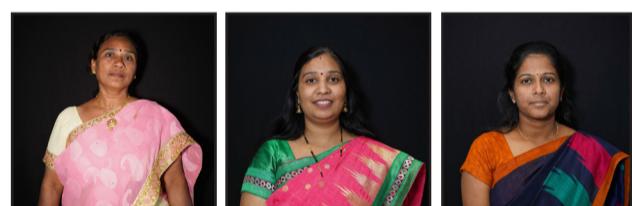
Unleashing Innovation at EXTC

The Department of Electronics & Telecommunication Engineering recently hosted IDEAthon-2024, an innovative project idea competition aimed at fostering a culture of innovation among students. This event, organized as an Exhibition and Contest under the theme of Design Thinking was a platform for students and faculty to showcase their creativity and problem-solving abilities.

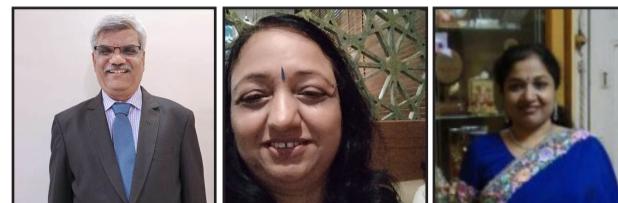
Distinguished speakers including Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering), Mrs. Smita Jangale (Former Deputy Head, Department of Information Technology), and Mrs. Shoba Krishnan (Former Head, Department of Electronics and Telecommunication Engineering), graced the occasion, providing valuable insights and inspiration.

The event was meticulously coordinated by Dr. (Mrs.) Ranjan Bala Jain (Professor, Department of Electronics and Telecommunication), along with Mrs. Anuradha Jadiya (Assistant Professor, Department of Electronics & Telecommunication) and Mrs. Himali Patel (Assistant Professor, Department of Electronics & Telecommunication). With a duration of six hours, IDEAthon-2024 saw enthusiastic participation from 60 students and 21 faculty members.

The competition aimed to encourage participants to think beyond boundaries and envision futuristic ideas. The event not only honed their presentation skills but also provided them with valuable knowledge about recent trends in technology and innovation. The competition aimed to encourage participants to think beyond boundaries and envision futuristic ideas. Through group activities and presentations, participants demonstrated their ability to tackle real-world problems with innovative solutions.



(L-R): Coordinators of the event, Dr. (Mrs.) Ranjan Bala Jain (Professor, Department of Electronics and Telecommunication), Mrs. Anuradha Jadiya (Assistant Professor, Department of Electronics & Telecommunication), Mrs. Himali Patel (Asst. Professor, Department of Electronics and Telecommunication Engineering)



(L-R): Judges of the Event, Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering), Mrs. Smita Jangale (Former Deputy Head, Department of Information Technology), Mrs. Shoba Krishnan (Former Head, Department of Electronics and Telecommunication)

The Winners of IDEAthon 2024 event are:

In Senior Category:

1. Development of Ethernet protocol using FPGA for sensor networks :

Team Members : Patkar Pratik Pradeep (TE-EXTC), Rajeshirke Aayushi Santosh (TE-EXTC), Dsouza Alrich Xavier (TE-EXTC), Petkar Atharva Ajay (TE-EXTC).

2. Ear Biometric-based Human Identification and Authentication System :

Team Members : Siddhesh Gajare (TE-EXTC), Hemraj Jeetarwal (TE-EXTC), Aditya Sonawane (TE-EXTC).



(L-R): Mr. Mrugendra Vasmatkar (Assistant Professor of Department of Electronics and Telecommunication Engineering), and Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering) presenting award to Dsouza Xavier (TE-EXTC), Ayushi Rajeshirke (TE-EXTC), and Pratik Patkar (TE-EXTC) Senior Category First Prize winners



(L-R): Dr. (Mrs.) Ranjan Bala Jain (Professor Department of Electronics and Telecommunication Engineering), and Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering) presenting award to Heemraj Jeetarwal (TE-EXTC), Siddhesh Gajare (TE-EXTC), and Aditya Sonawane (TE-EXTC) Senior Category Second Prize winners

~ Chirag Pedamkar

In Junior Category:

1. Pipe Inspection and Profile Evaluation Rover (PIPER):

Team members : Kaustubh Vilas Natalkar (SE-EXTC), Kaustubh Worlikar (SE-EXTC)

2. Truck battery anti theft system:

Team members : Manasi Maloji Erande (SE-EXTC), Shravani Shripad Kale (SE-EXTC) and Yash Jalindranath Sangle (SE-EXTC).



(L-R) : Mrs. Smita Jangale, (Former Deputy Head, Department of Information Technology), Mrs. Shoba Krishnan (Former Head, Department of Electronics and Telecommunication) and Mrs. Himali Patel (Assistant Professor, Department of Electronics & Telecommunication) felicitating Junior Category Winners- Kaustubh Worlikar (SE-EXTC), Kaustubh Natalkar (SE-EXTC), Manasi Erande (SE-EXTC), Shravani Kale (SE-EXTC) and Yash Sangle (SE-EXTC).

Special Judge's Mention award :

Bharatiya Bhasha Mini Sangathan:

Team members : Atharva Dharmadhikari (TE-EXTC), Renu Dharamkar (TE-EXTC), Anuraag Rao (TE-EXTC) and Aman Yadav (TE-EXTC).



(L-R): Mr. Mrugendra Vasmatkar (Assistant Professor, Department of Electronics & Telecommunication) and Dr. Ramesh Kulkarni (Principal, Atharva College of Engineering) felicitating Special mention team - Aman Yadav (TE-EXTC), Atharva Dharmadhikari (TE-EXTC) and Anurag Rao (TE-EXTC)

In conclusion, IDEAthon-2024 was a resounding success, reaffirming the department's commitment to nurturing innovation and creativity among its students and faculty.

Frontiers of Innovation: Sci-Tech Fair

~Shruti Thakare

The Department of Electronics and Computer Science, VESIT organized Sci-Tech Fair on 23rd March 2024 to foster creativity, encourage scientific inquiry, and promote technological advancements. The event included Project Exhibition by the Third-year and Fourth-year students.

The event was coordinated by Dr. Jaymala Adsul (Assistant Professor, Department of Electronics and Computer Science Engineering)



(L-R)-Event Coordinators : Dr. (Mrs.) Jaymala Adsul (Assistant Professor, Department of Electronics and Computer Science Engineering) and Mr. Yogesh Pandit (Assistant Professor, Department of Electronics and Computer Science Engineering)

and Mr. Yogesh Pandit (Assistant Professor, Department of Electronics and Computer Science Engineering).

The projects were showcased to faculty and students, facilitating robust and constructive discussions. The interactions allowed for in-depth feedback, fostering a collaborative environment that encouraged exchanging ideas and insights. The judges for the event were Dr. (Mrs.) Sangeetha P. Ram (Head, Department of Automation and

Robotics Engineering) and Dr. (Mrs.) Nandini Ammanagi (Assistant Professor, Department of Electronics and Telecommunication Engineering).

More than 50 students presented a total of 10 projects. Some of the projects were 'IoT based smart water filling system,' 'Temperature based industrial ventilator control system.' The Sci-Tech Fair was a resounding success in nurturing the scientific and technological mindset among the students.



(L-R): Judges of the event: Dr. (Mrs.) Sangeetha P. Ram (Head, Department of Automation and Robotics Engineering) and Dr. (Mrs.) Nandini Ammanagi (Assistant Professor, Department of Electronics and Telecommunication Engineering)

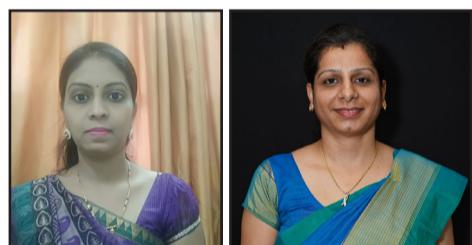


(L-R): Students presenting their project to Dr. (Mrs.) Sangeeta P. Ram (Head, Department of Automation and Robotics Engineering)

Innovision: Pioneering Tomorrow

~Asawari Patil

The Department of Artificial Intelligence and Data Science, in collaboration with VESIT-IIC (Institution's Innovative Council) and VESIT-IQAC (Internal Quality Assurance Cell), orchestrated a dynamic project exhibition titled 'Discover the Future'. This innovative event, meticulously coordinated by Mrs. Smita Mane (Assistant Professor, Department of Artificial Intelligence and Data Science) and Mrs. Himanshi Jiwatramani (Assistant Professor, Department of Artificial Intelligence and Data Science), showcased over 15 pioneering projects by final year students encompassing fields such as Artificial Intelligence, Data Science, Machine Learning, and Deep Learning. The exhibition unfolded over a compelling six-hour period on 23rd March 2024.



(L-R): Co-ordinators of the event - Mrs. Smita Mane (Assistant Professor, Department of Artificial Intelligence and Data Science), and Mrs. Manju Ahuja (Assistant Professor, Department of Artificial Intelligence and Data Science)

A total of 69 enthusiastic students actively participated, each presenting their projects through engaging interactive demonstrations and visually compelling poster presentations. The projects were evaluated by a distinguished panel of juries including Mr. Abhishek Chaudhari (Assistant Professor, Department of Information Technology), Mrs. Jayshree Hajgude (Assistant Professor, Department of Information Technology), and Mrs. Vidya Zope (Assistant Professor, Department of Computer Engineering), who assessed them based on criteria focusing on innovation, practical impact, and adept presentation skills. This assessment culminated in the prestigious awarding of seven trophies to the most outstanding projects, highlighting their excellence and contribution to the technological landscape.



(L-R): Judges of the exhibition, Mr. Abhishek Chaudhari (Assistant Professor, Department of Information Technology), Mrs. Jayshree Hajgude (Assistant Professor, Department of Information Technology), and Mrs. Vidya Zope (Assistant Professor, Department of Computer Engineering)

Projects that showcased innovation, creativity, and practical effectiveness garnered recognition from peers, experts, and stakeholders present at the exhibition. This recognition provided students with a valuable learning opportunity, enabling them to glean insights by observing other projects, receiving feedback, and engaging in discussions with attendees.



(L-R): Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science) and Dr. (Mrs.) M. Vijayalakshmi (Head, Department of Artificial Intelligence and Data Science) felicitating 1st Prize Winners: Predicting malnutrition of baby from pregnant mother - Mayur Pimpude (BE-AIDS), and Deepak Prasad (BE-AIDS)

Additionally, the department organized a student visit to promote the projects, allowing participants to receive constructive criticism. This initiative also facilitated other students in gaining insights into the real-world applications of technology within their respective fields, serving as a source of inspiration for all involved.

The winners of the exhibition were team of Mayur Pimpude (BE-AIDS), and Deepak Prasad (BE-AIDS) with project Predicting malnutrition

of baby from pregnant mother, and 1st runner-up was Manas Lalwani (BE-AIDS) presenting project with title - Predicting maintenance of servers. The project title Visual Speech Recognition using AI by Arya Kurup (BE-AIDS) won the 2nd runner-up prize.



(L-R): Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science) and Dr. (Mrs.) M. Vijayalakshmi (Head, Department of Artificial Intelligence and Data Science) felicitating 1st Runner ups with Project Title: Predicting maintenance of servers - Manas Lalwani (BE-AIDS)



(L-R): Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science) and Dr. (Mrs.) M. Vijayalakshmi (Head, Department of Artificial Intelligence and Data Science) felicitating 2nd Runner ups with Project Title: Visual Speech Recognition using AI - Arya Kurup (BE-AIDS)

The project exhibition event served as a platform for innovation, collaboration, and knowledge exchange. Its conclusion marked not just the end of an event but the beginning of new opportunities, partnerships, and endeavours fueled by the inspiration and insights gained throughout the exhibition. Participants gained invaluable experience in pitching and presenting ideas, enhancing their ability to effectively communicate and sell their vision.

TechTube: Viral Tech Innovations

~Nikunja Sonawane

Department of Artificial Intelligence and Data Science celebrated Technology Day with the TechTube event. The event took place on March 23 2024, at B21 classroom. Over 8 groups participated from Second Year students, covering a field of AI history, Innovative AI applications, Advancements in AI. TechTube

offered a rewarding experience for participants beyond just competition. It provided a platform for participants to showcase their videos and gain recognition from peers and experts in the field.

Students upload their videos on their respective youtube channels showcasing their design thinking, innovation and creativeness. Each

video was judged based on the maximum number of likes and views.

Winners of the event included: 1st Prize Winners: Sanjana Choubey (SE-AIDS), Sanika Jadhav (SE-AIDS), Varad Bhat (SE-AIDS), Ajinkya Akant (SE-AIDS) 2nd Prize Winners: Khushi Manik (SE-AIDS),

Anjali Pillai (SE-AIDS), Naina Sachdeva (SE-AIDS).



(L-R): Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science), Dr. (Mrs.) M. Vijayalakshmi (Vice Principal, VESIT) felicitating 1st Prize Winners - Sanjana Choubey (SE-AIDS), Sanika Jadhav (SE-AIDS), Varad Bhat (SE-AIDS), Ajinkya Akant (SE-AIDS)



(L-R): Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science), Dr. (Mrs.) M. Vijayalakshmi (Vice Principal, VESIT) felicitating 2nd Prize winners - Khushi Manik (SE-AIDS), Anjali Pillai (SE-AIDS), Naina Sachdeva (SE-AIDS)

Attendees received feedback that could be used to refine their videos and improve their skills. TechTube validated the efforts and accomplishments of the participants, boosting their

morale and motivating them to continue creating in the future. The TechTube Youtube video link competition serves as a platform for innovation, collaboration, and knowledge exchange.

Its conclusion marks not just the end of an event but the beginning of new opportunities, partnerships, and endeavors fueled by the inspiration and insights gained throughout the exhibition. Also the winners and runner ups were felicitated by Dr. (Mrs.) M Vijayalakshmi (Vice Principal, VESIT) and Dr. (Mrs.) Anjali Yeole (Deputy Head, Department of Artificial Intelligence and Data Science). Participants gained invaluable experience, enhancing their ability to communicate and sell their vision effectively. The event itself served as a valuable learning experience, contributing to both personal and professional development.

Empowering Innovators with Pradarshini

~Preetika Khilnaney

The Department of Computer Engineering, in collaboration with VESIT-IIC (Institution's Innovative Council) and VESIT-IQAC (Internal Quality Assurance Cell), organized Department Technology Day titled **Pradarshini'24**, on 22nd-23rd March 2024. The event featured a curated selection of projects from third and final-year students and served as a platform for participants to refine their presentation skills, receive invaluable feedback on their ideas, and gain critical insights into innovation and implementation strategies.

It was meticulously coordinated by Dr. (Mrs.) Gresha Bhatia (Deputy Head Of Department, Computer Engineering), Mrs. Vidya S Zope (Assistant Professor, Department Of Computer Engineering), Mrs. Priya R. L (Assistant Professor, Department Of Computer Engineering) and Mrs. Mannat Doltani (Assistant Professor, Department Of Computer Engineering) and was graced by the presence of esteemed dignitaries, many of whom are VESIT alumni.



(L-R): Coordinators of event - Mrs. Vidya S Zope (Assistant Professor, Department Of Computer Engineering), Mrs. Priya R. L (Assistant Professor, Department Of Computer Engineering) and Mrs. Mannat Doltani (Assistant Professor, Department Of Computer Engineering)

Mr. Ali Ansari (Manager, Open Innovation & Corporate Programs TiE Mumbai), Mr. Kevin Maru (Consultant, Deloitte Consulting India Pvt. Ltd. Mumbai), Mr. Anish Vaidya (Software Engineer II, JP Morgan Chase & Co. Mumbai) and Mr. Kapish Madhwani (Tata Institute of Fundamental Research, Mumbai) honored the

event as guests and comprised the expert panel, playing a crucial role in providing assessment to the participants on their presentations and determining the winners.



(L-R): Mr. Anish Vaidya (Software Engineer II at JP Morgan Chase & Co. Mumbai) and Mr. Kevin Maru (Consultant, Deloitte Consulting India Pvt. Ltd. Mumbai)

They evaluated presentations based on three parameters - relevance and validation of the problem as well as solution, feasibility of the solution and finally applicability of the solution. They also emphasized the importance of presenting these project ideas to relevant industries and advocating for their integration into product development and application deployment.

Ultimately, they commended the Department and the Project Team for establishing a platform that allowed students to demonstrate their skills effectively. Taufique Ansari (TE-CMPN), Kinjala Ahuja (TE-CMPN), Devangana Barua (TE-CMPN), Dipanshu Ghime (TE-CMPN), Bhargav Mungekar (BE-CMPN), Aayushi Salunkhe (BE-CMPN), Aarya Lotke (BE-CMPN) and Niranjan Yeole (BE-CMPN) emerged victorious after several rounds of evaluation and bagged the title of Winners of **Pradarshini'24** under the invaluable guidance of their mentors, Dr. (Mrs) Sharmila Sengupta (Associate Professor, Department Of Computer Engineering) and Dr. (Mrs) Gresha Bhatia (Deputy Head, Department of Computer Engineering).

With that, the event spanning over 10 hours through two days came to a successful conclusion, owing to the enthusiastic involvement and admirable dedication of 83 students along with 26 faculty members. Due to its distinctive approach and sophisticated peer review-based evaluation methodology, **Pradarshini'24** established itself as a unique and widely appreciated benchmark event.



(L-R): Dr. (Mrs.) Gresha Bhatia (Deputy Head, Department of Computer Engineering), Dr. (Mrs.) Sujata Khedkar (Associate Professor, Department Of Computer Engineering), Bhargav Mungekar (BE-CMPN), Kinjala Ahuja (TE-CMPN), Taufique Ansari (TE-CMPN), Aarya Lotke (BE-CMPN), Aayushi Salunkhe (BE-CMPN), Dipanshu Ghime (TE-CMPN), Dr. Sharmila Sengupta (Associate Professor, Department Of Computer Engineering) and Devangana Barua (TE-CMPN)
Winners of the event

Buzz It

~Srushti Chopade

BuzzPro'24 conducted by the Department of Computer Engineering, is an opportunity for Second Year Computer Students to explore, investigate and analyze the experience of writing a technical paper.

The event was organized by CMPN

Research Forum Team members Dr. (Mrs.) Sharmila Sengupta (Associate Professor, Department of Computer Engineering), Mrs. Lifna C. S. (Assistant Professor, Department of Computer Engineering) and Mr. Richard Joseph (Assistant Professor, Department of Computer Engineering),

from 15th February 2024 to 22nd March 2024 , there were total 180 student participants and 15 faculty participants.

Entire 2nd year students having a count of around 180 participated in the event out of them 14 groups were shortlisted for the first round.

Nine Second Year Computer Engineering groups were selected for the final round and presented their papers. They were selected among 14 papers through External Reviewer's comments and Plagiarism reports.



(L-R): Judges of the event Sanika Chavan (Batch of 2019, Department of Computer Engineering), Dr. Machhindra Patil (Associate Professor, Department of Computer Engineering)

The judges of the event was an Alumni, Ms. **Sanika Chavan** (Alumni - 2019 CMPN), Senior Development Engineer @ Carwale, and Industry Expert, **Dr. Machhindra Patil** (Associate Professor, Department of Computer Engineering) VESIT.



(L-R): Dr. Prashant Kanade (Assistant Professor, Department of Computer Engineering) felicitating 1st prize winners - Kushl Alve (SE-CMPN), Neelkanth Khithani (SE-CMPN), Vedang Gambhire (SE-CMPN), Atharva Hande (SE-CMPN), Shivam Choubey (SE-CMPN)

The winners of the Technical Paper Presentation are :

1st Prize - Blockchain based intelligent disbursement in National scholarship portal Members of the team: **Kushl Alve** (SE-CMPN), **Neelkanth Khithani** (SE-CMPN), **Vedang Gambhire** (SE-CMPN), **Atharva Hande** (SE-CMPN), **Shivam Choubey** (SE-CMPN).



(L-R): Mrs. Rohini Temkar (Assistant Professor, Department of Computer Engineering) felicitating 1st prize winners - **Kushl Alve** (SE-CMPN), **Neelkanth Khithani** (SE-CMPN), **Vedang Gambhire** (SE-CMPN), **Atharva Hande** (SE-CMPN), **Shivam Choubey** (SE-CMPN)

2nd Prize - **KisanSevak**: Machine learning solutions for crop recommendation yield prediction and price forecasting. Members of the team: **Kushl Alve** (SE-CMPN), **Neelkanth Khithani** (SE-CMPN), **Vedang Gambhire** (SE-CMPN), **Atharva Hande** (SE-CMPN), **Shivam Choubey** (SE-CMPN).

3rd Prize - **GesSpy** : Empowering silence, Embracing diversity (Members (SE-CMPN): **Siddhi Awari**, **Sri Haritha Movva**, **Ananya Parthasarathy**, **Srushti Poriwade**)

Best Presentation Team - **RAILEASE**: Railway concession automation at college (Members

(SE-CMPN) : **Tejas Gadge**, **Deepak Kumbhar**, **Ganesh Shelar**, **Vedant Mhatre**, **Ummulkiram Mahuvawala**.



(L-R): Dr. (Mrs.) Sujata Khedkar (Associate Professor, Department of Computer Engineering) felicitating the 3rd prize winners - (SE-CMPN) **Siddhi Awari** (SE-CMPN), **Sri Haritha Movva** (SE-CMPN), **Ananya Parthasarathy** (SE-CMPN), **Srushti Poriwade** (SE-CMPN)



(L-R): Mr. Richard Joseph (Assistant Professor, Department of Computer Engineering), **Tejas Gadge** (SE-CMPN), **Deepak Kumbhar** (SE-CMPN), **Ummulkiram Mahuvawala** (SE-CMPN), **Ganesh Shelar** (SE-CMPN), **Vedant Mhatre** (SE-CMPN), Mrs. Lifna C. S. (Assistant Professor, Department of Computer Engineering), and Dr. (Mrs.) Sharmila Sengupta (Associate Professor, Department of Computer Engineering), felicitating the Best Presentation team

Avinya-2024: Embracing Innovation

~Arpita Yaligeti

The Electronics and Telecommunication Department of VESIT (Vivekanand Education Society's Institute of Technology), in collaboration with the Institute's Innovation Council (IIC) and Internal Quality Assessment Cell (IQAC), has set a benchmark with the remarkable event, **Avinya-2024**. This one-day program, Celebrated as Technology Day on 23rd March 2024, was a celebration of the perfect synergy of technology and innovation.

Inaugurated by the esteemed Dr. (Mrs.) J. M. Nair (Principal, VESIT), Avinya-2024 featured a judging panel consisting of senior faculty members from various departments, ensuring a broad and impartial evaluation. The event saw a total of 40 projects submitted, out of which 12 were shortlisted for exhibition and demonstration. The involvement of second-year students from the Electronics & Telecommunication department further enriched the event, providing them with invaluable exposure to advanced projects and innovative ideas.

The conclusion of Avinya-2024 culminated in a prestigious valedictory session on 23rd March 2024. This pivotal event celebrated the outstanding

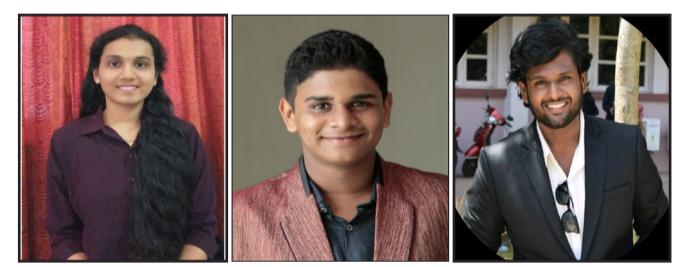
achievements of the winners, who were lauded for their remarkable contributions to Technology Day.

1st prize winner at Avinya-2024 was the 'Development of Ethernet Protocol using FPGA for Sensory Networks' by Team B5, consisting of **Dsouza Alrich Xavier** (BE- EXTC), **Pratik Patkar** (BE- EXTC), **Aayushi Rajeshirke** (BE- EXTC), and **Atharva Petkar** (BE- EXTC).



(Top-Bottom, L-R): 1st prize winner - **Dsouza Alrich Xavier** (BE- EXTC), **Pratik Patkar** (BE- EXTC), **Aayushi Rajeshirke** (BE- EXTC), and **Atharva Petkar** (BE- EXTC)

The 2nd prize was awarded to the 'Adaptive Battery Management System with Thermal Safety' project developed by Team B17, consisting of **Radhika Damle** (BE-EXTC), **Shreyas Sarang** (BE-EXTC), and **Siddesh Shetty** (BE-EXTC).



(L-R): 2nd prize winners - **Radhika Damle** (BE-EXTC), **Shreyas Sarang** (BE- EXTC), and **Siddesh Shetty** (BE- EXTC)

Avinya-2024 was a testament to the innovative spirit of VESIT students and their dedication to addressing real-world problems with advanced technological solutions. This event not only celebrated the students' accomplishments but also laid a foundation for future innovations that will continue to emerge from the fertile grounds of VESIT.

Inspiring Innovation at VESIT

~Chirag Pedamkar

The Year 2024 at VESIT began with an inspiring event organized by Department of Automation and Robotics during

Quarter I: "The Technovation 2024 (Project Exhibition and Competition)". Held on February 9th and March 23rd, 2024, this 18-hour event

showcased student creativity and engineering innovation. The event was coordinated by Dr. (Mrs.) Kadambari Sharma (Assistant Professors,

Department of Automation & Robotics) and **Mr. N. Gopalkrishnan** (Assistant Professors, Department of Automation & Robotics), the event featured distinguished reviewers: **Dr. (Mrs.) Lekshmi Kaimal** (Senior Functional Consultant at Go Digital Technology), **Mr. Ajit Patil** (Senior Technical Engineer at L & T Technology Services) and **Ms. Tejasree Phatak** (Assistant Professor at Rajiv Gandhi Institute of Technology, Versova.)



(L-R): Co-ordinators of the event, Dr. (Mrs.) Kadambari Sharma (Assistant Professor, Department of Automation and Robotics), Mr. N. Gopalkrishnan (Assistant Professor, Department of Automation and Robotics)

The program mentored students to develop high-quality, innovative projects. After receiving feedback during review-I on February 9th, students implemented suggestions and presented their final projects on Technology Day.

The **Winners** of Technovation 2024 are the team with thier Project:

Predictive Maintenance of Rotating Machines.
Team members: **Pradnya Sawant** (TE-AURO), **Shriya Naik**(TE-AURO), **Nikhil Gore** (TE-AURO) and **Vinayak Rasal** (TE-AURO).



(L-R): Dr. (Mrs.) Lekshmi Kaimal (Senior Functional Consultant at Go Digital Technology), Ms. Tejasree Phatak, (Assistant Professor at Rajiv Gandhi Institute of Technology), Mr. Ajit Patil, (Senior Technical Engineer at L & T Technology Services) and Mr. Kader B.T Shaikh (Assistant Professor, Department of Automation and Robotics)

Students developed teamwork and leadership skills, drew inferences from data, and analyzed their solutions' societal and environmental impacts. With **66 students** and **12 faculty**

participants, the event was an absolute thrill. The objective of conducting the program was to make students understand the process of identifying needs and converting them into problems as it is essential to familiarize oneself with the group problem-solving process.

Technovation 2024 at VESIT showcased students innovative spirit and the college's dedication to research, development, and sustainable engineering. It emphasized applying basic engineering fundamentals and the importance of self-learning and research in solving problems effectively.



(L-R): Dr. (Mrs.) Nilima Warke (Associate Professor, Department of Automation and Robotics) with the winning team members, Vinayak Rasal (TE-AURO), Shriya Naik (TE-AURO), Pradnya Sawant (TE-AURO) and Nikhil Gore (TE-AURO)

TECH UPSKILLING

Session on Scope of VLSI

~Priyanka Tiwari

Department of Electronics and Computer Science on 1st April 2024 organized a session on “**Scope/Opportunities of VLSI in INDIA**” which was an opportunity for Third Year Students to get familiar with the latest **VLSI technologies**, it also gave the students exposure to concepts like Semiconductor technology, VLSI technology, Career opportunities in VLSI industry.



Speaker Ms. Archita Malgaonkar (MTech in Semiconductor Materials and Devices Indian Institute of Technology, Hyderabad) talking about skills and expertise in VLSI.

The coordinator of the event was **Mr. Yogesh Pandit** (Assistant Professor, Department of Electronics and Computer Engineering). The session saw participation of **50 student participants** and **01 faculty participants**.

The speaker of the session was **Ms. Archita Malgaonkar** (MTech in Semiconductor Materials and Devices Indian Institute of Technology, Hyderabad).

The session served as a platform for meaningful discussion and knowledge exchange, underscoring the importance of continued exploration and collaboration in harnessing the power of VLSI and improving outcomes. The speaker provided an overview of VLSI, explaining its applications and scope.

She further explained about the Higher studies in the VLSI domain in IITs. The future trends and challenges in the realm of VLSI and Semiconductor Technology were addressed, including the need for scalable circuit understanding, designs, etc.

Overall the session on Scope of VLSI in

India provided a comprehensive overview of the evolution and convergence of VLSI by examining key concepts, trends, and challenges, the session fostered a deeper understanding of the integral role played by the semiconductor industry in the field of VLSI.



(L-R): Speaker Ms Archita Malgaonkar (MTech in Semiconductor Materials and Devices Indian Institute of Technology, Hyderabad) felicitated by Mr. Yogesh Pandit (Assistant Professor, Department of Electronics and Computer Science Engineering).

Bootcamp for Data Wizards

~Asawari Patil

The Department of Artificial Intelligence and Data Science, in collaboration with VESIT-IIC (Institution's Innovation Council) and VESIT-IQAC (Internal Quality Assurance Cell), orchestrated the 'Data Science Bootcamp' workshop. This comprehensive program, featuring self-paced online courses aimed at skill enhancement, took place from 20th May to 21st June 2024. The workshop was skillfully coordinated by **Mrs. Bhavana Chaudhari** (Assistant Professor,

Department of Artificial Intelligence and Data Science) and **Mrs. Mamata Choudhari** (Assistant Professor, Department of Artificial Intelligence and Data Science).

The inaugural segment of the workshop focused on 'Data Processing and Visualization,' guided by **Mrs. K. Grace Kennedy**. This course, facilitated by Mathworks, primarily emphasized using MATLAB to manage, process, and visualize data effectively.



(L-R): Speaker of the event - Mrs. K. Grace Kennedy, and Mrs. Andrea Bayas (Training Engineer, Mathworks)

The next theme explored 'ML (Machine Learning) with MATLAB,' led under the guidance of Mrs. **Andrea Bayas** (Training Engineer, Mathworks). This course provided students with a solid foundation in supervised and unsupervised learning techniques. Particularly valuable was its focus on interpreting and visualizing model results, crucial for understanding decision-making processes and communicating findings to stakeholders.

The final module, also guided by Andrea Bayas, covered 'Statistics,' aiming to enhance analytical skills in exploring relationships between

variables. Topics included calculating correlation coefficients and performing simple linear regression to model and predict data trends.

A notable **138 students** participated actively in the workshop. The outcomes included improved skills, increased productivity, advancements in career and academic pursuits, enhanced research abilities, practical problem-solving skills, boosted confidence, better integration with technology, and broader global competence. This workshop not only equipped participants with proficiency in MATLAB but also opened doors to new opportunities for innovation, collaboration, and career development.



Poster of the event - 'Data Science Bootcamp'

Navigating The IOT Landscape

The Department of Master of Computer Applications collaborated with VESIT - Institution's Innovation Council and VESIT - Internal Quality Assurance Cell and organized "Mastering the Internet of Things", a hands-on course on the practical application of the IoT. The workshop took place from **18th June to 21st June 2024** from at the Department of MCA on the VESIT campus. Enrollment for the course commenced on May 21st, 2024. The course was



Demonstration of different boards online by instructor Dr. Ramesh Solanki (Assistant Professor, Department of Master of Computer Applications)

conducted for a total of **20 hours**. The course was conducted in a hybrid format, allowing students to participate online as well. The workshop saw a participation of **35 students**. Eight students attended in person, while the remaining students joined virtually.

The speakers for the workshop were **Dr. Ramesh Solanki** (Assistant Professor, Department of Master of Computer Applications), **Mrs. Ruchi Rautela** (Assistant Professor, Department of Master of Computer Applications) and **Mrs. Indira Bhattacharya** (Assistant Professor, Department of Master of Computer Applications). They also served as the coordinators of the workshop. The course was specially designed for **first-year MCA students**, enabling them to gain a comprehensive understanding of IoT concepts and practical applications. The course entailed a combination of lectures and hands-on practical exercises on various topics, a few of them being Introduction to IoT, Types Of IoT Boards, Difference Between Raspberry Pi vs Arduino, Installation of Raspberry Pi, Installation of Raspberry Pi and case studies and various examples for applications,

The objective of the workshop was to

empower the students to create practical IoT applications with guidance, knowledge, and illustrative examples. The workshop aimed to encourage participants to undertake real-world projects, facilitating the application of acquired knowledge and nurturing a deeper understanding of IoT principles.



Course Participants with instructors - **Dr. Ramesh Solanki** (Assistant Professor, Department of Master of Computer Applications) and **Mrs. Indira Bhattacharya** (Assistant Professor, Department of Master of Computer Applications)

Hands-On Web Hosting With Azure

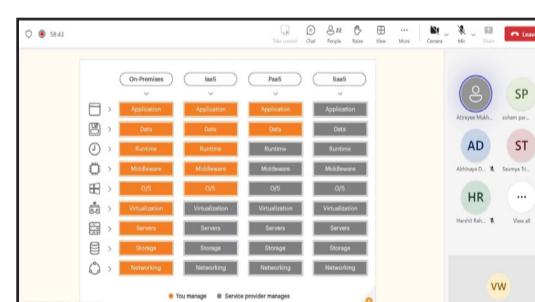
An online Microsoft Azure workshop was organized by **Codecell Tinkerer's** from **Department of Computer Engineering** under the guidance of **VESIT-IQAC (Internal Quality Assurance Cell)** and **VESIT-IIC (Institution's Innovation Council)** on 24th March 2024 from 11:00 am to 12:30 pm. The event was orchestrated by **Mr. Richard Joseph** (Assistant Professor, Department of Computer Engineering) and the Codecell student team. **Attrayee Mukherjee** (TE-CMPN), who is a Microsoft Student Ambassador, was the speaker for the workshop.



Speaker of the session - Attrayee Mukherjee (TE-CMPN)

She began the session with an introduction

to the basics of web hosting and later delved into important topics like virtualisation and the concept of containers.



A detailed explanation of the different models of cloud computing

Since the workshop primarily aimed at educating students on how to deploy a static web page on Microsoft Azure, Attrayee proceeded to provide a detailed, step-by-step demonstration on creating an Azure account, setting up the necessary resources and deploying a static web page. Throughout the presentation, she also highlighted various features and benefits of Azure, such as its scalability, reliability and user-friendly interface.

At the end of the session, each student

received \$100 in Azure credits. This generous offer allowed participants to further explore Azure services and practice hosting their own websites. Certificates of participation were also distributed to all attendees, recognising their efforts and learning during the workshop.



Coordinator of the event - **Mr. Richard Joseph** (Assistant Professor, Department of Computer Engineering)

Overall, the event provided attendees with valuable hands-on experience in web hosting using Microsoft Azure. It also successfully equipped students with practical skills for potential projects and set a benchmark for future technical events.

Path to GDSC Leadership

The "Road to GDSC" session, held on 23rd March 2024, was organized by the **GDSC, CodeCell Tinkerer's of the Department of Computer Engineering** under the guidance of **VESIT-IIC (Institution's Innovation Council)** and **VESIT-IQAC (Internal Quality Assurance Cell)**. It was a focused one-hour workshop led by GDSC Lead, **Saumya Tripathi** (TE-CMPN). Organized by the GDSC VESIT Core Team, the session aimed to provide attendees with valuable insights into the application process for the Google Developer Student Clubs (GDSC) lead role.

Saumya Tripathi began the session by explaining the application process for the GDSC lead role, emphasizing the importance of understanding the role's responsibilities and eligibility criteria.

She discussed the benefits of being a GDSC lead, highlighting how it can enhance leadership, communication, and technical skills. Additionally, she pointed out the networking opportunities and exposure to industry professionals that come with the role.

The session included a detailed overview of the application process, covering the various stages involved and the key components of the application form. Saumya stressed the importance of showcasing relevant experience, skills, and a passion for community building in the application. The session concluded with practical tips for aspiring GDSC leads, where Saumya shared insights on crafting a compelling application by highlighting one's unique strengths and experiences.



Speaker of the workshop - Saumya Tripathi (TE-CMPN)

Overall, the Road to GDSC session provided attendees with actionable guidance and a clearer understanding of the GDSC lead application process. The information shared was well-received by the **38 students** who attended the online meet, helping them feel better equipped to approach their applications effectively.

Exploring Generative AI

The "Build With AI" workshop, jointly organized by **GDSC** and **CodeCell Tinkerer's of Computer Engineering**, was held on **May 24th, 2024**. Under the skillful guidance of **Mr. Piyush Chugeja** (SE-CMPN), **Web Dev Lead at GDSC**, this online session was coordinated by the **GDSC VESIT Core team**, centered on IPR/R&D and Innovation. Its objective was to educate participants on Generative AI and the innovative Google Gemini platform.



Speaker of the workshop - Piyush Chugeja (SE-CMPN)

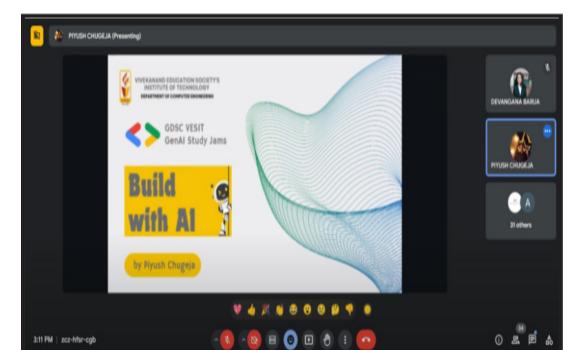
With a total attendance of **40 students**, the workshop provided a comprehensive overview of Generative AI, exploring its foundational concepts, ethical considerations, and cutting-

edge advancements such as GPT-4o, Character.ai, and Pico Apps. Piyush Chugeja's expertise and engaging presentation style was instrumental in delivering complex AI topics in an accessible manner, resonating well with the diverse audience. Participants lauded the workshop for its depth of content and practical insights, particularly appreciating the real-world examples shared, which highlighted the transformative potential of AI technologies in various domains.

The session not only enhanced participants' understanding of AI but also sparked enthusiasm for innovative applications within the field. Attendees left with a clearer grasp of how AI can drive advancements in research and development, emphasizing ethical considerations and compliance. Feedback indicated a strong interest in further exploration of AI platforms introduced during the workshop, with many expressing intentions to integrate these technologies into their academic and professional pursuits. Social media promotion via platforms like Instagram and LinkedIn, under the handles **GDSC_VESIT** and **gdsc_vesit_linkedin** respectively, further amplified the workshop's reach, showcasing the

event's impact within the academic community and beyond.

In conclusion, the "Build With AI" workshop not only served as a platform for learning but also as a catalyst for fostering innovation and ethical awareness among participants. Piyush Chugeja's leadership and the collaborative efforts of GDSC and CodeCell Tinkerer's underscored the workshop's success in bridging theoretical knowledge with practical applications, setting a precedent for future engagements in cutting-edge technologies within the Department of Computer Engineering at GDSC VESIT.



Build With AI presented by Piyush Chugeja

Diving into the LLM's workshop by GDSC

-Nikunja Sonawane

The "LLM Insights with Gemini Pro" workshop was conducted on **22nd May 2024** by **GDSC, CodeCell Tinkerer's of the Department of Computer Engineering**. The event offered a detailed exploration of Large Language Models (LLMs) and the innovative Gemini Pro platform. Led by **Anagha Kulkarni** (TE-CMPN), 60 students attended the session on 22nd May 2024 in the online meet.

Participants were introduced to the basics of LLMs, their types, and their significance in the AI landscape. The session covered the evolution of LLMs over time, highlighting their increasing capabilities and applications. The session provided an overview of Gemini Pro, a comprehensive tool designed to leverage the power of LLMs. Attendees learned about its key features and functionalities, understanding how Gemini Pro can enhance AI projects.

Next an introduction to LangChain, a framework for developing applications powered by language models, was provided. The session explained how LangChain facilitates the seamless integration and chaining of different language model tasks, making the development of LLM-based applications more efficient. The workshop included a live demonstration where a simple LLM-based application was built using Gemini Pro. This hands-on session allowed participants to see the practical application of the concepts discussed earlier.

Students found the workshop highly informative, with a deep dive into informative theory of LLM and a hands on project. Many appreciated Anagha Kulkarni's insightful guidance, where she not only explained complex concepts in an easy-to-understand manner but also shared real-world examples that enhanced their learning experience.

The 'LLM Insights with Gemini Pro' program offered an extensive comprehension of Generative AI, Large Language Models, and the most recent advancements in AI. Attendees acquired hands-on expertise in utilizing tools such as Gemini Pro and LangChain, promoting creativity and cultivating ethical consciousness. The interactive display served as a catalyst for guests to delve into the potential of AI in upcoming projects, highlighting the profound impact of AI technology.



Speaker of the Workshop- Anagha Kulkarni (TE-CMPN)

Coding Ninjas and ISA-VESIT Collaborate

~Harshita Sewani

On June 22, 2024, ISA-VESIT, in collaboration with Coding Ninjas, successfully hosted the "Campus Hero Webinar." The event comprised two main segments: a webinar from 3:00 PM to 4:30 PM, followed by a coding contest from 4:30 PM to 6:30 PM. **Mr. Archit Mittal** (Founder of Automate Algos and a former Software Developer at Amazon), was the keynote speaker. His session focused on: The current hiring landscape, differences between product-based and service-based companies, the importance of Data Structures and Algorithms (DSA) in technical interviews, guidance on selecting the right programming language, and setting professional milestones.

Archit Mittal provided deep insights into the technical job market, stressing the significance of DSA in securing positions at top tech companies. He emphasized the varying dynamics between product-based and service-based companies and advised on strategic career planning. His talk was well-received, with students appreciating the practical advice and strategies offered for navigating their future careers in technology.

The webinar concluded with an interactive Q&A segment. Participants engaged with Archit, seeking career advice and technical insights. They had the opportunity to ask specific questions about their career paths, projects, and DSA, making the session highly beneficial and tailored to their individual needs.



Chief Guest Mr. Archit Mittal (founder of Automate Algos and a former Software Developer at Amazon)

Following the webinar, a coding contest was held to test participants' coding skills under real-time pressure, simulating technical interview conditions. The contest featured a series of

challenging problems designed to assess problem-solving abilities and technical proficiency. The contest winners are as follows: **1st Place: Chinmay Chaudhary** (SE-INFT), **2nd Place: Tarankumar Gatla** (SE-CMPN), **3rd Place: Shubham Bablukumar Jha** (SE-INFT).

The "Campus Hero Webinar" and coding contest were highly successful, offering students valuable insights into the tech industry and a platform to demonstrate their coding abilities. The event effectively blended theoretical knowledge with practical skills, helping attendees better understand the demands of the industry and the skills necessary to succeed.



(L-R): Winner - Chinmay Chaudhary (SE-INFT), 1st runner-up - Tarankumar Gatla (SE-CMPN), and 2nd runner-up - Shubham Bablukumar Jha (SE-INFT)

RESUME TO RECRUIT: AI FOR PLACEMENTS

~Nikunja Sonawne

The recruitment landscape is rapidly evolving with the integration of Artificial Intelligence (AI) into hiring practices. AI is increasingly used to streamline administrative tasks such as resume screening, scheduling interviews, and sending automated messages. AI-driven tools like Applicant Tracking Systems (ATS) allow recruiters to filter candidates based on specific skills, experience, and qualifications. According to MarketingScoop, AI can efficiently narrow down a large talent pool, enabling HR teams to focus on the most qualified candidates and improving the overall hiring process. Many leading global companies have already implemented AI-based recruiting tools and systems.

The VESITConnect team recently discussed with the HR professional from NetcoreCloud about the role of Artificial Intelligence (AI) in the recruitment process for placements. It was revealed that NetcoreCloud is in the early stages of AI integration, primarily using it for routine tasks like sending automated invites and thank-you emails, which helps streamline administrative duties. While AI may play a larger role in the future, the HR professional emphasized that core recruitment, especially interviews, remains focused on assessing skills, knowledge, and personal attributes. AI cannot replace the human judgment required for evaluating factors like body language and attitude. The professional also provided practical advice for students, suggesting that resumes should be concise and highlight key achievements on the front page to pass initial AI screenings. Although AI can aid the process, face-to-face interactions remain crucial for assessing communication skills and personality traits that AI cannot fully capture, ensuring the human element remains central in hiring.

As AI continues to evolve, its influence on recruitment is expected to deepen, particularly in the early stages of the hiring process. With advancements in technology, AI tools will likely become more sophisticated in their ability to

evaluate not only technical skills but also certain behavioral traits, making them more effective in assessing a candidate's overall suitability for a role. For example, natural language processing (NLP) technology is already being developed to analyze candidates' written responses for emotional intelligence, critical thinking, and creativity. This could enable AI systems to go beyond traditional keyword-based filtering to provide a more nuanced understanding of a candidate's personality and problem-solving abilities. Additionally, machine learning algorithms could be trained to recognize patterns in candidate behavior, such as how they communicate or approach problem-solving, offering insights into whether they align with a company's culture or team dynamics.

Despite these advancements, AI's role in recruitment will likely remain most effective in handling administrative tasks and initial screenings rather than making final hiring decisions. For instance, AI can analyze a candidate's technical skills and experience, but it struggles to evaluate critical soft skills like adaptability, leadership potential,

emotional intelligence, and teamwork capabilities. These skills, while harder to quantify, are essential for many roles and are often what distinguishes a good hire from a great one. In this context, AI's predictive capabilities, while impressive, are not foolproof. It might flag a candidate as suitable based on technical criteria, but without understanding the candidate's personality and how they interact with others, AI cannot fully determine if they are the right fit for a team or company culture.

In conclusion, while AI has made significant strides in transforming the recruitment landscape, its role remains primarily supportive. AI-driven tools can effectively streamline administrative tasks and filter candidates based on technical skills and qualifications, making the hiring process more efficient. However, the human element of recruitment is irreplaceable, particularly when it comes to assessing soft skills, emotional intelligence, and cultural fit. The future of recruitment will be a hybrid approach, blending AI's efficiency with the nuanced understanding that only human recruiters can provide.

Company	AI-based recruiting tool	Impact
JP Morgan	HireVue	Reduced hiring time from 4 months to 3 weeks
Vodafone	Entelo	Reduced cost per hire by 35%
Duolingo	Ideal	Improved response rate from 25% to 65%
MCKinsey & Co.	Stella.ai	Raised employee retention rate
Johnson & Johnson	AllyO	95% of candidates rate experience 5 stars
UBS	Harver	50% more candidates matched to ideal roles.
L'Oreal	Ideal	More than half candidates screened per recruiter.

List of companies along with the AI-based recruiting tools and their impact

EXTRACURRICULARS

SYMPOSIUM

In a final crescendo of camaraderie and knowledge exchange, all the technical societies within our institution united to orchestrate one last grand event, the Symposium, bidding a fond farewell to their dedicated members and teams. Eminent chief guests graced the occasion, adding an extra layer of significance as they engaged in meaningful interactions with the eager audience.

The societies, being the architects of their own destiny, meticulously planned and executed the Symposium, not only to mark the commendable efforts and accomplishments of the societies but also to bid a heartfelt adieu to their tireless leaders and heads.

Syposium was organized by the following councils:

1. 'MELANGE'24: Fusion of Tech Minds' by Institute of Electrical and Electronics Engineers-VESIT
2. 'Bridging the Past, Present, and Future' by International Society of Automation-VESIT
3. 'Going Green With GREENOVA' by Computer Society Of India-VESIT
4. 'Odyssey: A Stellar Conclusion' by Indian Society For Technical Education-VESIT

MELANGE'24: Fusion of Tech Minds

~Nikunja Sonawane

IEEE, the Institute of Electrical and Electronics Engineers, is known for its majestic technical event called "MELANGE". This event is held at the end of the year to commemorate the organization's journey and share valuable memories with everyone. IEEE-VESIT's MELANGE took place on **28th April 2024** in our Auditorium from 2:00 pm to 5:30 pm.

The event commenced with the inauguration of IEEE-VESIT's Symposium, Melange by two esteemed branch counselors, **Dr. (Mrs.) Gresha Bhatia** (Deputy Head, Department of Computer Engineering), and **Dr. (Mrs.) Ashwini Sawant** (Assistant Professor, Department of Electronics and Telecommunications) along with our Chief Guest, **Mr. Abhishek Kalgutkar** (Senior Engineer, Quantiphi) with Saraswati Vandana and Lamp Lighting.



Chief Guest, Mr. Abhishek Kalgutkar (Senior Engineer, Quantiphi) addressing young minds



(L-R): Faculty incharges of IEEE-VESIT - Dr. (Mrs.) Gresha Bhatia (Deputy Head, Department of Computer Engineering) and Dr. (Mrs.) Ashwini Sawant (Assistant Professor, Department of Electronics and Telecommunications)



(L-R): Dr. Gresha Bhatia (Deputy Head, Department of Computer Engineering), Rishika Chaubal (TE-EXTC), Khyati Joshi (SE-EXTC), Maithili Latey (SE-EXTC), Avantika Sharma (SE-ECS), Mr. Abhishek Kalgutkar (Senior Engineer, Quantiphi) unveiling of IEEE-VESIT's Magazine 'INNOVATION'

The branch counselors gave a note highlighting the successes in this academic year.

Mr. Abhishek Kalgutkar spoke on Quantum Computing, which was the topic of the evening, and Machine Learning and Artificial Intelligence. His session was for an hour after which IEEE-VESIT's Annual Magazine, **INNOVATION** was released and our brand new Website and Application was launched by our chief guest.

We also awarded all the winners of our events with their certificates and prizes on this occasion. The event ended with our Chairperson, **Sheryl Bellary** (BE-AIDS) and Chief Executive Officer, **Aditi Bombe** (BE-EXTC) signing off by reminiscing their years at IEEE-VESIT. All IEEE VESIT members were a part of this event along with winners of all events and non-members could attend the event with a nominal fee. All participants were given food and refreshments and goodies to thank them for their continued support throughout the year.



(L-R): Sheryl Bellary (Chairperson, IEEE-VESIT) and Aditi Bombe (Chief Executive Officer, IEEE-VESIT)

Bridging the Past, Present, and Future

~Mahek Ahuja

On 26 March 2024, ISA-VESIT organised highly anticipated **Symposium** in the auditorium. The title was **HORIZON '24**. The event turned out so well. The drawing faculties and the students from all the branches and year were part of it.

The symposium conducted by the ISA-VESIT gave the overview of the various sessions and workshops which were conducted during the academic year 23-24. The introduction was given by **Nachiket Suryawanshi** (BE-ETRX) and **Yash Chavan** (BE- EXTC). Then the mic was handed over to **Mr. Gopalakrishnan Narayanan** (Assistant Professor, Department of Automation and Robotics Engineering) honoring the guest speaker, **Mr. Prithvi Shetty** (Batch of 2021 Department of Instrumentation) and **Mr. Nikhil Gore** (President, ISA-VESIT) gave an informative speech. **Avantika Narvekar** (BE-EXTC) and **Pooja Narayanan** (BE-

EXTC), then took the stage to shared the journey of ISA VESIT over the years. The highlight of the event was a presentation by the guest speaker **Mr. Prithvi Shetty** (Software Developer, Nymble).



(L-R): Guest Speaker of the workshop - Mr. Prithvi Shetty (Batch of 2021, Department of Instrumentation), Ms. Hrutika Pahile (Batch of 2023, Department of Automation and Robotics Engineering) and Mr. Jatin Dandela (Batch of 2022, Department of Electronics and Telecommunications Engineering)



(Top to Bottom, L-R): Hosts of the event - Nachiket Suryawanshi (BE-ETRX), Yash Chavan (BE- EXTC), Mr. Nikhil Gore (President, ISA-VESIT), Avantika Narvekar (BE-EXTC), Pooja Narayanan (BE-EXTC), Arya Lotke (BE-CMPN)

Following the presentation ISA-VESIT unveiled their latest initiative, **Akshay Chavan** (BE-EXTC) and **Arya Lotke** (BE-CMPN) introduced the newly developed 'Hardware inventory website'

while **Tanaya Gaikar** (BE-EXTC) launched the annual magazine - **Envisage '24**.

Then the inspiring speech were given by alumini, **Ms. Hrutika Pahile** (Batch of 2023, Department of Automation and Robotics Engineering) and **Mr. Jatin Dandelia** (Batch of 2022, Department of Electronics and Telecommunications Engineering) and were presented by tokens of appreciation. The event

was continued by a prize distribution ceremony, recognising the winners of praxis and post praxis and other competitions conducted by ISA -VESIT.

The symposium concluded with a heartfelt vote of thanks by Mr. Gopalakrishnan Narayanan. To wrap up the event on a positive note, snack boxes were also provided to everyone present there and everyone left with a smile.



(L-R): Mr. Gopalakrishnan Narayanan (Assistant Professor, Department of Automation and Robotics Engineering) honoured the guest speakers and Chief Editor of ISA-VESIT, Tanaya Gaikar (BE-EXTC) unveiled the Envisage '24 magazine

Going Green With GREENOVA

Symposium, the annual farewell and formal sign-off event of the **Computer Society of India - VESIT**, took place on 1st April 2024. The theme of this year's symposium was "Greenova", cleverly selected to shed light on technology helping mankind to push towards a greener and sustainable future. The two guest speakers for the event were **Mr Siddharth Deshmukh** (Founder and CEO of minusco2) and **Mr Ashish Mehta** (Founder of second nature sustainable solutions and Founding member of minusco2), both having massive contributions in the field of Green Energy.



(L-R): Mr Siddharth Deshmukh(Founder and CEO of minusco2), Mr Ashish Mehta(Founder of second nature sustainable solutions and Founding member of minusco2), Mrs Sangeeta Oswal (Assistant Professor, Department of Artificial Intelligence and Data Science), Mrs Lifna C S (Assistant Professor, Department of Computer Engineering) Guest speakers received a warm welcome from faculties

The event was hosted by **Shivani Nikam** (TE-INFT), **Dhruva Choudhari** (TE-CMPN), **Ashish Patil**(TE-AIDS) and **Anish Padhye** (SE-EXT) and gracefully commenced with a Sarawasti Vandana. The speakers were then welcomed by the faculty incharges of CSI, **Mrs. Lifna C S** (Assistant Professor, Department of Computer Engineering)

and **Mrs Sangeeta Oswal** (Assistant Professor, Department of Artificial Intelligence and Data Science). The guest speakers then shed light on the latest developments in solar technology and the positive impact they created through their brainchild, minusco2. Valuable insights on green technology were also provided to the attendees by them.



(L-R): Hosts of the event - **Shivani Nikam** (TE-INFT), **Dhruva Choudhari** (TE-CMPN), **Ashish Patil** (TE-AIDS), and **Anish Padhye** (SE-EXT)



(L-R): Mr. Siddharth Deshmukh(Founder and CEO of minusco2), Mr. Ashish Mehta(Founder of Second Nature Sustainable Solutions and Founding Member of minusco2), **Sakshi Shinde** (BE-CMPN), **Mrs. Sangeeta Oswal** (Assistant Professor, Department of Artificial Intelligence and Data Science), **Mrs Lifna C S** (Assistant Professor, Department of Computer Engineering) launching the CSI-VESIT's Annual Magazine, REDUX GREENOVA

The insightful sessions of the speakers were followed by the much-awaited launch of the CSI-VESIT annual magazine, **REDUX GREENOVA**. The magazine explored various themes like GreenTech which highlights innovation in renewable energy and eco-friendly technologies whereas Future of Work which covers trends in remote work. The magazine also features articles by the council members and editors of CSI as well as an article by **Vemburaj Konar** (BE-AIDS), the winner of the article writing competition of CSI. Additionally, prizes were awarded to the winners of past events CSI-VESIT.

The event drew to a close with a short clip showcasing the council activities carried out by CSI for the academic year 2023-24. Subsequently, all members and attendees were escorted to the amphitheatre to indulge in the snacks meticulously arranged, accompanied by an assortment of exciting goodies. This was followed by the official council sign-off in the auditorium where the council members presented their concluding speeches. Symposium, being the last event of the year, turned out to be special for all the technical societies as it marked the culmination of their year-long efforts. The event was a resounding success for CSI-VESIT.



Vemburaj Konar (BE-AIDS)
Winner of Article Writing Competition

Odyssey: A Stellar Conclusion

~Tejasvini Bachhav

On a high note, ISTE-VESIT's student chapter closed the 2023-24 academic year with a bang! "**Symposium 24 - Odyssey**" a 3-hour event held on 27th March 2024 in the Auditorium, took center stage, with the theme "**India's Ascent into Space**" igniting curiosity among attendees.

The event started with a traditional Saraswati Vandana, a beautiful invocation of the goddess of knowledge. The event was hosted by **Vishwajeet Panaskar** (SE-ECS) and **Adheeti Dalal** (TE-EXT), along with **Ritali Jadhav** (SE-ECS) and **Shreyas Akmanchi** (SE-AIDS). The presence of esteemed faculty in-charges, **Dr. Asawari Dudwadkar** (Assistant Professor, Department of Electronics Engineering) and **Mrs. Netto Mertia** (Assistant Professor, Department of Humanities and Applied Sciences), further added to the academic weight of the event.



(L-R): **Dr. (Mrs.) Asawari Dudwadkar** (Assistant Professor, Department of Electronics and Computer Science Engineering) and **Mrs. Netto Mertia** (Assistant Professor, Department of Humanities and Applied Sciences)

The discussion that unfolded was truly captivating. Our esteemed guests, **Mrs. Pooja Chandravanshi** (Scientist/Engineer SD, Physical Research Laboratory (PRL) Ahmedabad) and **Mr. Jaya Krishana Meka** (Scientist/Engineer SD, Physical Research Laboratory (PRL) Ahmedabad) took the stage. They unraveled

the intricate relationship between ISRO (Indian Space Research Organisation) and PRL (Physical



(L-R): **Shashwat Tripathi** (TE-INFT), **Aditya Nama** (TE-EXT), **Mrs. Netto Mertia** (Assistant Professor, Department of Humanities and Applied Sciences), **Dr. Asawari Dudwadkar** (Assistant Professor, Department of Electronics Engineering), **Mr. Jaya Krishana Meka**, **Mrs. Pooja Chandravanshi**, **Bhakti Daga** (TE-EXT), **Rachna Rane** (TE-INFT) unveiling the annual magazine

Research Laboratory), shedding light on how these two institutions collaborate and play distinct, yet crucial roles in India's spacefaring endeavors.



(L-R): Sahil Kodwani (BE-INFT) interacted with Mrs. Pooja Chandravanshi (Scientist/Engineer SD, Physical Research Laboratory (PRL) Ahmedabad) and Mr. Jaya Krishana Meka (Scientist/Engineer SD, Physical Research Laboratory (PRL) Ahmedabad)

The audience, particularly those with starry-eyed dreams of a space career, were all

ears as the discussion delved into how ISRO, the mastermind behind space exploration and satellite launches, works hand-in-hand with PRL, renowned for its groundbreaking contributions to space and planetary sciences. Keeping the momentum going, **Sahil Kodwani** (BE-INFT), took the conversation to a new level with a thought-provoking podcast-style session with both guests.

The excitement continued as the highly anticipated unveiling of the annual magazine, **Cognizance '24**, occurred. Following this was a celebratory awards ceremony where faculty members and the chief guests presented awards to the winners of various events held throughout the year. This recognition of student achievements added a touch of pride to the overall atmosphere.

As the event neared its conclusion, the "Five Pillars" of ISTE-VESIT, the esteemed heads of the organization, delivered heartfelt farewell speeches

that left a lasting emotional impact on everyone present. The evening concluded with delicious food from Tibb's Frankie and Sweet Secrets, along with a delightful spread of treats.



(Top to Bottom, L-R): Hosts of the event - Vishwajeet Panaskar (SE-ECS) and Adheeti Dalal (TE-EXTC), along with Ritali Jadhav (SE-ECS) and Shreyas Akmanchi (SE-EXTC)

FEATURED

Degree Distribution Ceremony

The Degree Distribution Ceremony for the engineering and MCA students of Batch of 2023 at VESIT marked a significant milestone in the academic journey of the graduates. This momentous occasion celebrated

their dedication, perseverance, and outstanding achievements. Faculty, parents, and distinguished guests gathered to honor the students' hard work and success as they prepared to embark on new professional and academic ventures. The

event also served as a platform to recognize the unwavering support of the faculty and families, whose guidance played a crucial role in shaping the graduates' accomplishments and future paths with distinguished guests addressing the students.

CMPN 2023 Graduates Celebrate Success

~Harshita Sewani

The Degree Distribution Ceremony for the CMPN Batch of 2023, held on 30th March 2024 at the VES Auditorium in Chembur, Mumbai, marked a significant milestone in the graduates' lives. With around 180 students registered for the event, the celebration commenced with a Ceremonial Parade from the Vivekanand Statue in the VESIT Campus.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Computer Engineering

Chief Guest Mr. Sanjeev Chugh (Quality Kiosk), alongside Dr. (Mrs.) Nupur Giri (Head, Department of Computer Engineering), and Dr. (Mrs.) Gresha Bhatia (Deputy Head, Department of Computer Engineering), offered flowers at the statue. Dr. Nupur Giri then delivered the welcome address, honoring esteemed dignitaries, parents, students, and faculty members. The ceremony included the lamp's lighting, the Sarasvati Vandana's recitation, and the Mumbai University Song.

The dignitaries on the dais included Mr. Sanjeev Chugh, Dr. Gresha Bhatia, Dr. (Mrs.) Sharmila Sengupta (Associate Professor, Department of Computer Engineering), Dr. (Mrs.) Sujata Khedkar (Associate Professor, Department of Computer Engineering), Dr. Nupur Giri (Head, Department of Computer Engineering), Mr. Sanjeev Chugh (Quality Kiosk), Dr. (Mrs.) Gresha Bhatia (Deputy Head, Department of Computer Engineering), Dr. Sharmila Sengupta (Associate Professor, Computer Engineering), and Prof. A. G. Nagananda (Training and Placement Officer, VESIT).

Engineering), and Mr. A. Nagananda (Training and Placement Officer, VESIT). Mr. Chugh was felicitated with memento and floral bouquet by Dr. (Mrs.) Nupur Giri.



(L-R): Dignitaries present for the event Dr. M.D.Patil (Associate Professor, Department of Computer Engineering), Dr. Sujata Khedkar (Associate Professor, Department of Computer Engineering), Dr. Nupur Giri (Head, Department of Computer Engineering), Mr. Sanjeev Chugh (Quality Kiosk), Dr. (Mrs.) Gresha Bhatia (Deputy Head, Department of Computer Engineering), Dr. Sharmila Sengupta (Associate Professor, Computer Engineering), and Prof. A. G. Nagananda (Training and Placement Officer, VESIT).

The ceremony was expertly conducted by Dr. Dashrath Mane (Assistant Professor, Department of Computer Engineering), supported by Mrs. Abha Tewari (Assistant Professor, Department of Computer Engineering) and Mrs. Pallavi Saindane (Assistant Professor, Department of Computer Engineering). Dr. Nupur Giri praised the graduates for their hard work, dedication, and perseverance, emphasizing their mastery of technical and problem-solving skills, ethical responsibilities,

and potential to make significant contributions in various fields.



Chief Guest Mr. Sanjeev Chugh (Quality Kiosk) addressing the students

In his Convocation Address, Mr. Sanjeev Chugh inspired the graduates with his engaging and motivational speech, emphasizing the importance of being future-ready and acquiring a blend of technical and domain knowledge. He highlighted the growth of the Indian Software Market and the role of AI, not just as Artificial Intelligence but as All-Inclusive technology. Mr. Chugh encouraged the graduates to remain lifelong learners, value their education, and practice leadership.

The ceremony concluded with the graduates taking the "Engineers Oath," led by Dr. Dashrath Mane, followed by the award of degrees presented by the Chief Guest and other dignitaries. Graduates Siddhi Bhosle (BE-CMPN) and Sneha Kartik (BE-CMPN) expressed their gratitude on behalf of the students. Dr. Gresha Bhatia, delivered the Vote of Thanks, acknowledging the efforts of the student volunteers and the staff committee in coordinating the ceremony. The event, filled with optimism and pride, celebrated the graduates' achievements and set the stage for their future endeavors.

Graduation Triumph Of INFT

~Tanisha Nanwani

Vivekanand Education Society's Institute of Technology (VESIT) marked a significant milestone with the **convocation ceremony** of Class 2023, **Information Technology Department** held in VESIT Auditorium on 23 April 2024. This joyful event was attended by students, faculty, and families celebrating the achievements of the new graduates.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Information Technology

The ceremony was hosted by **Mrs. Anushree Prabhu** (Assistant Professor, Department of Information Technology) and **Mrs. Bincy Ivin** (Assistant Professor, Department of Information Technology). The ceremony began with the lighting of a ceremonial lamp, a traditional gesture symbolizing enlightenment and knowledge.

Chief Guest **Mrs. Garima Srivastava**, (Assistant Vice President, YES Bank), **Dr. (Mrs.) Shalu Chopra** (Head of Information Technology), **Dr Manoj Sabnis** (Deputy Head of Information

Technology), **Mrs. Shanta Sondur** (Associate Professor, Department of Information Technology), **Dr. Abhay Kshirsagar** (Associate Professor, Department of Information Technology), **Mr. A. Nagananda** (Training & Placement Officer), **Mrs. Jayashree Hajgude** (Exam Incharge, Department of Information Technology). Chief Guest Mrs. Garima Srivastava, delivered an inspiring speech, emphasizing the importance of dedication and ethical conduct in professional life. Faculty members also shared valuable insights and congratulated the students on their accomplishments.



(L-R): Dignitaries at the event - **Dr. Shanta Sondur** (Associate Professor, Department of Information Technology), **Dr. Shalu Chopra** (Head, Department of Information Technology), **Mrs. Garima Srivastava**, (Assistant Vice President, YES Bank), **Dr. Manoj Sabnis** (Deputy Head, Department of Information Technology), **Dr. Abhay Kshirsagar** (Associate Professor, Department of Information Technology) and **Mr. A. A. Nagananda** (Training and Placement Officer, VESIT)



Chief Guest of the event - **Mrs. Garima Srivastava**, (Assistant Vice President, YES Bank)

A major event highlight was the **oath-taking** ceremony, where graduates pledged to adhere to ethical engineering practices and contribute positively to society. This commitment reflects the values and principles instilled in them during their time at VESIT.

The distribution of degree certificates was a smooth process, with each student individually felicitated by the faculty members. This personal touch added a special significance to the event, making each graduate feel valued and appreciated for their hard work.

The ceremony concluded with encouraging words, reminding the graduates of the importance of maintaining integrity and striving for excellence in their future endeavors. The event was a proud moment for everyone involved and marked the beginning of a new chapter in the graduates' professional lives.

Yash Ka Samaaroh

~Arpita Yaligeti

In a ceremony brimming with pride and anticipation, the Department of **Electronics and Telecommunication** hosted its **Degree Distribution Ceremony** for the graduating class of 2023-24 on 23rd March. The Auditorium resonated with the joy of accomplishment as faculty, students, and families gathered to celebrate this occasion.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Electronics and Telecommunication Engineering

The ceremony commenced with a vibrant procession, led by our gracious host **Mrs. Arti Sawant** (Assistant Professor, Department of Electronics and Telecommunication Engineering), and the graduating class in their academic regalia. The air crackled with excitement as they marched proudly, a testament to their years of dedication. The procession culminated at the venue, where the dignitaries, including the esteemed Chief Guest, **Dr. (Mrs.) Gopika Vinod** (FNA, Head, Probabilistic Safety Solution, Reactor Safety Division, BARC), were welcomed with a traditional lamp-lighting ceremony.

Dr. Chandan Singh Rawat (Head, Department of Electronics and Telecommunication Engineering), delivered a heartfelt welcome address,

expressing his immense pride in the graduating class and their achievements. He then introduced the Chief Guest Dr. Gopika Vinod, who served as a source of inspiration for the students.



(L-R): Dignitaries at the event - **Dr. (Mrs.) Monali Chaudhari** (Assistant Professor, Department of Electronics and Telecommunication Engineering), **Mrs. Manisha Chattopadhyay** (Deputy Head, Department of Electronics and Telecommunications Engineering), **Dr. Gopika Vinod** (FNA, Head, Probabilistic Safety Solution, Reactor Safety Division, BARC), **Mr. Chandan Singh Rawat** (Head, Department of Electronics and Telecommunications Engineering), **Mr. A.A. Nagananda** (Training and Placement Officer, VESIT), **Dr. (Mrs.) Saylee Gharage** (Assistant Professor, Department of Electronics and Telecommunication Engineering)



Chief Guest **Dr. (Mrs.) Gopika Vinod** (FNA, Head, Probabilistic Safety Solution, Reactor Safety Division, BARC) addressing the students

The most awaited moment arrived with the Degree Distribution of Electronics and Telecommunication for the Batch of 2020-24. As each student's name was called, they stepped forward to receive their degree from the esteemed dignitaries. The ceremony concluded with a heartfelt vote of thanks delivered by **Mrs. Manisha Chattopadhyay** (Deputy Head, Department of Electronics and Telecommunications Engineering), expressing gratitude to the faculty, staff, and families who played a pivotal role in the graduates' success.

As the echoes of applause faded, a bittersweet feeling permeated the air. It marked the end of an enriching academic chapter and the beginning of a new adventure for the graduating class. The Department of Electronics and Telecommunication takes immense pride in its graduates and wishes them the very best in their future endeavors.

Dr. (Mrs.) Gopika Vinod captivated the audience with a thought-provoking Convocation Address. They shared valuable insights and practical advice, urging the graduates to embrace the challenges and opportunities that lie ahead. Their words resonated deeply, leaving the students

Success Soiree

In a significant celebration of the relentless efforts and commitment, the **Degree Distribution Ceremony** for the graduating batch of 2019-2023 of Electronics Engineering was organized on **30th March, 2024** at the prestigious college auditorium by the **Department of Electronics and Computer Science Engineering**. The event was a culmination of years of hard work and dedication by the students, and it marked a milestone in their academic journey.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Electronics Engineering

The ceremony was graced by the presence of an esteemed dignitary, **Dr. Amar Banerji**, (Former Head, The Technology Transfer and Collaboration Division, Bhabha Atomic Research Centre). He is an expert with academia and industry in the area of incubation, Project Consultancy, Course Design, Patent, Robotics, entrepreneurship Promotion as consultant, speaker and guide. Dr. Banerji's vast experience and expertise added prestige to the occasion, inspiring the graduates as they embarked on their professional careers.

The ceremony commenced with the lighting of the lamp, followed by Mumbai University Song and welcome remarks by **Dr. (Mrs.) Kavita Tewari** (Head, Department of Electronics and Computer Science Engineering) who expressed her pride in the accomplishments of the graduating class. Then, the introduction of the chief guest was given by **Dr. (Mrs.) Asawari Dudwadkar** (Assistant Professor, Department of Electronics and Computer Science Engineering).



(L-R): Dignitaries at the event - **Dr. (Mrs.) Asawari Dudwadkar** (Assistant Professor, Department of Electronics and Computer Science Engineering), **Dr. (Mrs.) Kavita Tewari** (Head, Department of Electronics and Computer Science Engineering), **Chief Guest - Dr. Amar Banerji** (Former Head, The Technology Transfer and Collaboration Division, BARC), **Dr. (Mrs.) Naveeta Kant** (Deputy Head, Department of Electronics and Computer Science Engineering), **Dr. (Mrs.) T. Rajani Mangala** (Professor, Department of Electronics and Computer Science Engineering), and **Dr. Abhay Kshirsagar** (Associate Professor, Department of Information Technology)



Chief Guest Dr. Amar Banerji (Former Head, Technology Transfer and Collaboration Division, BARC)

Dr. Amar Banerji delivered the keynote address, sharing insights from his illustrious career and offering valuable advice to the young engineers. His words resonated deeply with the audience, emphasizing the importance of innovation, integrity, and continuous learning in the field of electronics engineering. Each graduate was called upon the stage individually, where they were presented with their degrees by Dr. Amar Banerji and the faculty members.

Lastly, Oath was taken by all graduates followed by felicitation of the chief guest and at the end of the event, the vote of thanks was given by **Dr. (Mrs.) Naveeta Kant** (Deputy Head, Department of Electronics and Computer Science Engineering) who had extended sincere gratitude to all individuals and organizations who contributed to the success of the degree distribution ceremony, including the organizing committee, faculty members, administrative staff and student volunteers for their invaluable support, dedication and commitment and lastly, the event ended with the national anthem.

INST'23 Commencement Ceremony

~Vedika Date

The Degree Distribution Ceremony for the **Automation and Robotics Department** at VES Institute of Technology was held on **Saturday, 30th March 2024**, marking a significant milestone for the graduating students. The event, hosted at the Hashu Advani Memorial Complex in Mumbai, commenced with a registration period, followed by a solemn procession starting at the Vivekanand Statue.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Instrumentation Engineering

The ceremony officially commenced with the lighting of the lamp and a Ganesh Vandana, signifying auspicious beginnings. This was succeeded by the University Song and a warm Welcome Speech along with the Department address by the **Dr. (Mrs.) Sangeetha Prasanna** (Head, Department of Automation and Robotics) highlighting the achievements and challenges faced by the graduating Class.

Dr. Upendra Joshi (Vice President, Head, Control Systems and Instrumentation at Reliance Industries), graced the occasion as the esteemed Chief Guest. His keynote address provided invaluable insights drawn from his extensive 32+ years of experience in the Refinery, Petrochemicals, and Life Sciences industries. Dr. Joshi's presence underscored the industry relevance and the academic excellence of the graduating cohort.



(L-R): Dignitaries at the event - **Dr. (Mrs.) Kadambani Sharma** (Assistant Professor, Department of Automation and Robotics Engineering), **Dr. (Mrs.) Sangeeta Prasanna** (Head, Department of Automation and Robotics), **Dr. Upendra Joshi** (Vice President, Head, Control Systems and Instrumentation at Reliance Industries), **Mrs. Dipti Khimani** (Deputy Head, Department of Automation and Robotics) and **Mrs. Madhumati Khuspe** (Assistant Professor, Department of Automation and Robotics)

The heart of the ceremony unfolded with the Distribution of Degree Certificates, a moment



Chief Guest - **Dr. Upendra Joshi** (Vice President, Head, Control Systems and Instrumentation at Reliance Industries) addressing the students

The ceremony not only celebrated the academic achievements of the students but also reaffirmed VESIT's commitment to producing skilled professionals equipped to contribute meaningfully to their respective fields. The presence of Dr. Upendra Joshi, with his distinguished career and accolades, added a layer of inspiration and encouragement to the event, making it a memorable occasion for all attendees.

Overall, the Degree Distribution Ceremony 2024 was a resounding success, encapsulating the spirit of academic excellence, industry partnership, and future aspirations of the graduating class from the Automation and Robotics Department.

~Shruti Thakare

Graduation Gala of MCA Batch '24

~Harshita Sewani

On the 23rd of March, 2024, at VES Institute Of Technology, **Department of Master of Computer Applications** held a memorable **Degree Distribution Ceremony** for its Master of Computer Application graduates on behalf of the University of Mumbai, marking a significant milestone in their academic journey. The event, which spanned over 2 enriching hours, was graced by the presence of esteemed dignitaries and enthusiastic attendees.



Ceremonial Parade from Vivekanand Statue of Batch of 2023 of Department of Master of Computer Applications

The ceremony was honored by the chief guest, **Mr. Saurabh Desai** (Project Manager and Consultant at Tata Consultancy Services), who brought a wealth of industry experience and insights to the event. The dais was adorned with the presence of several notable dignitaries: Mr. Saurabh Desai (Project Manager and Consultant, Tata Consultancy Services), **Dr. Shivkumar Goel** (Head, Department of MCA), **Dr. (Mrs.) A. Nagananda** (Training and Placement Officer, VESIT).

Dhanamma Jagli (Deputy Head, Department of MCA), **Dr. (Mrs.) Mona Deshmukh** (Exam Incharge, Department of MCA), **Dr. (Mrs.) Saylee Gcharge** (Controller of Examination, VESIT), **Mr. A. Nagananda** (Placement Officer, VESIT). The event was skillfully hosted by **Mrs. Vaishali Gatty** (Assistant Professor, Department of MCA), who was the Master of Ceremonies (MOC).



(L-R): Dignitaries at the event - **Dr. (Mrs.) Mona Deshmukh** (Exam Incharge, Department of MCA), **Dr. (Mrs.) Saylee Gcharge** (Controller of Examination, VESIT), **Dr. Shivkumar Goel** (Head, Department of MCA), **Mr. Saurabh Desai** (Project Manager and Consultant at Tata Consultancy Services), **Dr. (Mrs.) Dhanamma Jagli** (Deputy Head, Department of MCA), and **Mr. A. Nagananda** (Training and Placement Officer, VESIT)

The day's proceedings began with the registration of students, followed by a dignified procession from the Swami Vivekanand Statue to the auditorium, setting a tone of reverence and anticipation. **Dr. Shivkumar Goel** (Head of Department, MCA), warmly welcomed

the gathering with an inspiring address that highlighted the significance of the occasion and the achievements of the graduates. The chief guest, **Mr. Saurabh Desai**, delivered a keynote speech that was both motivational and insightful. His valuable inputs on the professional world and career guidance were particularly enlightening for the students poised to embark on their professional journeys. The highlight of the event was the degree distribution ceremony, where graduates were called to the stage to receive their degrees, marking the culmination of their hard work and dedication. The pride and joy were palpable as each student stepped forward to receive their accolade.



Chief Guest - **Mr. Saurabh Desai** (Project Manager and Consultant, Tata Consultancy Services)

Dr. Dhanamma Jagli (Deputy Head, Department of MCA), concluded the ceremony with a heartfelt vote of thanks, expressing gratitude to all who contributed to the resounding success of the event. The ceremony saw a commendable turnout, with 48 students and 10 proud parents in attendance, all sharing in the celebratory spirit of the day.

VESITians Today, Professional Tomorrow

~Preetika Khilnaney

On 10th May 2024, the **Training and Placement Cell** organized a job fair at Vivekanand Education Society's Institute of Technology. The event aimed at bridging the gap between job seekers and potential employers by providing opportunities for career exploration, offering career development resources, and assisting employers in candidate identification. It was orchestrated by **Mr. A. Nagananda** (Training and Placement Officer, VESIT), **Dr. Dashrath Mane** (Deputy Training and Placement Officer, VESIT), **Mrs. Vidya Pujari** (Assistant Professor, Department Of Information Technology) and **Mrs. Indu Dokare** (Assistant Professor, Department Of Computer Engineering).

The event commenced with the registration of participating students and subsequently transitioned into a series of company presentations. Representatives from various industries took the stage to put forth key aspects of their organizations such as company culture, future career prospects, and recruitment processes. By providing them with invaluable insights, these presentations helped students gain a deeper understanding of their potential employers and the roles they offer. Later, the focus shifted towards interview sessions, where students had the opportunity to interact one-on-one with recruiters and discuss their qualifications, aspirations, and suitability for available positions. These interviews served as a crucial platform for students to make lasting impressions and potentially secure internships or full-time employment.



(L-R): **Mr. Gaurav Tawde** (Assistant Professor, Department Of Electronics And Telecommunication Engineering), **Mr. Prasad Godse** (Assistant Professor, Department Of Automation & Robotics), **Mr. Ajinkya Valanjoo** (Assistant Professor, Department of Artificial Intelligence and Data science), **Mr. Sanjay Mirchandani** (Assistant Professor, Department Of Computer Engineering), **Mr. Abhishek Chaudhari** (Assistant Professor, Department Of Information Technology), **Dr. Dashrath Mane** (Deputy Training & Placement Officer, VESIT), **Mr. Nagananda** (Training And Placement Officer, VESIT), **Mrs. Indu Dokare** (Assistant Professor, Department Of Computer Engineering), **Mrs. Indira Bhattacharya** (Assistant Professor, Department of Master of Computer Applications), **Mrs. Amrita Jhaveri** (Assistant Professor, Department Of Electronics And Computer Science) **Mrs. Sarika Kuhikar** (Assistant Professor, Department of Electronics and Computer Science)

In addition to formal presentations and interviews, the Job Fair also encouraged informal networking. Students casually interacted with recruiters and industry professionals, exchanging contact details and building connections that could help in future career opportunities. These interactions offered students a chance to discuss their career goals, learn more about company cultures, and gain advice on industry trends. Such connections proved valuable, opening doors to internships, mentorships, and potential job offers.

Thus overall, the Job Fair emerged as a highly effective platform that seamlessly aligned the objectives of career exploration, professional networking and recruitment assistance. With the enthusiastic participation of **11 companies** and a vast multitude of students from various branches, it established itself as a benchmark event that garnered widespread appreciation and acknowledgement.



Students at the talk by the Company Delegates at the VESIT Job Fair

POSH- A Step To Empower and Protect

~Vedika Date

The POSH Awareness Programme organised by the VESIT Women Development Cell and Internal Committee in association with VESIT-IQAC and VESIT-IIC was held on Friday, 7th June 2024. The event took place at the VESIT Auditorium, aiming to educate attendees about the Prevention of Sexual Harassment (POSH) Act at the workplace.

The programme began with a welcome address by Dr. (Mrs.) Kavita Tewari (Chairperson, Women Development Cell), who emphasised the importance of understanding and implementing the POSH Act to ensure a safe and respectful environment. Dr. (Mrs.) Shanta Sondur (Chairperson, Internal Committee), then discussed the committee's role in addressing complaints related to sexual harassment and the necessity of awareness to prevent such incidents.

The keynote speaker, Mrs. Swayamjyoti Char (Master of Arts (M.A.) and Certified POSH Trainer from Swayam Consultancy, Mumbai), delivered an insightful presentation. She covered many definitions and forms of sexual harassment and legal provisions and guidelines under the POSH Act. She also gave an overview on Procedures for filing a complaint and the role of the Internal Committee and preventive measures and best practices for creating a harassment-free workplace.

Ms. Char's presentation included real-life case studies and interactive sessions, which engaged the audience and clarified their doubts. Her talk was both informative and empowering, providing the attendees with valuable knowledge and practical tools to address and prevent sexual harassment.



**Speaker of the event - Mrs. Swayamjyoti Char
(Master of Arts (M.A.) and Certified POSH Trainer, Swayam Consultancy, MUMBAI)**



Dr. (Mrs.) J. M. Nair (Principal, VESIT) expressed gratitude to the speaker

The event concluded with a vote of thanks by Dr. (Mrs.) J. M. Nair (Principal, VESIT), who expressed gratitude to the speaker, organisers, and participants for making the event a success. The ceremony ended with the National Anthem.

The POSH Awareness Programme was a significant initiative towards promoting awareness about the Prevention of Sexual Harassment (POSH) Act. The event successfully educated the participants about their rights and the procedures to address sexual harassment, contributing to a safer and more inclusive workplace culture. The session received positive feedback, reflecting the effectiveness of the programme in achieving its objectives.



(L-R): Dr. (Mrs.) Kavita Tewari (Chairperson, Women Development Cell) and Dr. (Mrs.) Shanta Sondur (Chairperson, Internal Committee)

Yoga Day Knowledge Challenge

~Shruti Thakare

International Yoga Day is celebrated worldwide annually on 21st June following its adoption by the UN in 2014. As Yoga exercises have shown significant benefits for physical and mental well-being, it was considered important by the UN to globally promote the wellness practice which originated in ancient India.

In association with the Institutions Innovation Council (IIC), VESIT Library

organized a National quiz competition on 21st June 2024. The quiz was coordinated by Dr. Meenakshi Tyagi (Library Incharge, VESIT).

More than 100 participants participated in the same. The quiz was free of cost for all the staff members of the college, students and professionals working in the domain. Participants with a score of 40% and above were awarded with E-certificates.



Coordinator of the event - Dr. Meenakshi Tyagi (Library Incharge, VESIT)



Nelson Mandela

I learned that courage was not the absence of fear, but the triumph over it. The brave man is not he who does not feel afraid, but he who conquers that fear.

VESIT DIARIES

Mayuresh Sarpotdar, an alumnus of VESIT and currently serving as the Executive Director at Morgan Stanley, shared valuable insights into his career journey. From practical exposure and mentor influence to continuous learning, Mayuresh highlighted the importance of technical basics for aspiring engineers.

What motivated you to choose your alma mater, and how did your journey from being a student there lead you to your current role at Morgan Stanley?

I took admission into VESIT as a Second year direct diploma student. VJTI and VESIT were my 2 top choices and I was very Happy to have gotten into VESIT Comps. Diploma curriculum allowed us to have significant practical exposure which I continued to build upon in VESIT. How I made it to Morgan Stanley, I think there were many aspects including being Hands on, Coding Skills, Knowledge of Operating systems and Databases and Computer Architecture (Theory part as well) and ofcourse Luck.

Reflecting on your college days, could you share a memorable moment or experience from your time there?

I would like to share 2 quotes I had heard from Nagananda Sir that have remained with me and have made considerable impact,

1. When going gets tough, Think of an Aeroplane , It takes off Against the Wind , not With it.
2. Never tell anyone you are Honest, You are Supposed To Be.

After graduating, what steps did you take to carve out your career path, eventually leading to your current position at Morgan Stanley?

I was lucky enough to get new roles every few years which continued to add up to my skills. Some of the advice that has helped me and I continue to build upon it is to drive your own career and to ask for opportunities when you think you can make a difference.

In your role at Morgan Stanley, what's the key to delivering successful solutions in your domain, and have you ever encountered challenges that made you wish you could simply say, "It's magic"?

The good or bad part of Computers is that there is never any Magic. Everything is explainable and

repeatable and hence it's important to dive deep and get to the root cause. Key to delivering a solution is to listen to everyone but do what you think is right and fail fast. Have a healthy conversation with people on approaches and why A vs B vs C approach is better. Such discussions are helpful to everyone.

Can you recount a pivotal moment or an interesting anecdote from your early career that significantly influenced your professional path, especially in the context of your role at Morgan Stanley?

I was fortunate enough to work with some of the experts in the Industry in Databases and Java. Back then my team had only myself and my manager who was a super Java Expert on the floor. Knowledge of How a system is Architected and how to develop large scale, Distributed , Fault tolerant and Resilient system was something I learnt on the Job and that experience which I got in initial years still continues to help as basics never change.

Could you elaborate on your role at Morgan Stanley? What are the day-to-day responsibilities and challenges you face in your position?

Presently I am lead for a specific team working on Corporate Action Processing. My main responsibility is to help my team succeed by giving them whatever required. Challenges tend to be solving mystery about certain system behaviour in certain conditions and how to change it to desired one without causing unintended impact.

Is it necessary to have a fool-proof plan for your career from the beginning, or did you find flexibility and adaptability more crucial in navigating your professional journey?

Always good to have a plan so you know at a high level where you are going. However being Flexible and Adaptable is important to sustain and succeed. There is always an element of Luck which one cant underestimate and control.

Given your experience, what technical skills and soft skills would you recommend for budding engineers preparing for the corporate world, especially in finance and technology?

Please be very Thorough with Basics , Understand how data structures work, which to use when,

their complexities, Computer architecture and Operating systems. What's covered in the syllabus



**Executive Director at Morgan Stanley,
Mr. Mayuresh Sarpotdar (Batch of 2006,
Department of Computer Engineering)**

is more than enough. Very important to do your labs seriously and by yourself :) . Multi Threading is a given thing in Industry and its critical to know how to write thread safe yet performance efficient code right at the college level itself and it's not at all difficult.

Outside of your professional life, what aspects of your personal life or hobbies do you prioritize in your free time?

I am a big proponent of Work Life Balance. Work is part of my life, it's not the entire Life. I travel extensively with Trips to somewhere nearly every weekend. Apart from that I am quite a foodie and like to play Racquet sports.

As we conclude, considering your experiences, what legacy or advice would you like to impart to future generations, particularly those at your alma mater, and any parting words filled with insight and perhaps a touch of humor?

Genuine Connections you will make in College will last a lifetime. Appreciating the power of Compounding, 8th Wonder of the World will start its rewards after 15-20 years but it's important not to disturb it until then.

~Suhaneet Khandalkar

VESIT Connect Team

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(Chief Editor)

Mrs. Priya R. L.
(Design Head)

Gaurang Desai
(Student Chief Editor)

Janhavi Patil
(Student Design Head) *

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Saniya Kadam

Shruti Thakare

Tanisha Nanwani

Tejasvini Bachhav

Vedika Date

**Arise! Awake! And do not stop until
your goal is achieved**



 @vesitedu

 vesit.connect@ves.ac.in



**Hashu Advani Memorial Complex, Collector's Colony,
Chembur, Mumbai, Maharashtra - 400074**