HW #3b

Instructions:

1. The deadline will be the end of this week, i.e., 11:59pm, Sept. 25, 2016.
2. We will have HW #3c next class.
3. Pls don’t submit until you complete all HWs, that is HW #3a, 3b, 3c.
4. At that moment, pls combine all HWs into one file, then submit it to D2L.

**[Problem 1]** Construct a 16-to-1 line multiplexer with two 8-to-1 multiplexers and one 2-to-1 line multiplexer. Use block diagrams for the three multiplexers.

**[Problem 2]** Using decoder to implement function f(A, B, C)=A⊕B⊕C

**[Problem 3]** Using multiplexer to implement function

F(A, B, C, D)=∑(3,5, 8, 10,13, 15)

**[Problem 4]** Using 8-to-1 MUX to implement 64-to-1 MUX.