

Half Subtractor

1) Definition

A half subtractor computes $A - B$ for single bits A (minuend) and B (subtrahend).
Outputs: D (Difference), Borrow (B_o).

2) Truth table

A	B	D	Borrow
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0

3) Boolean equations

Difference: $D = A \oplus B$

Borrow: $B_o = B \text{ AND } (\text{NOT } A) \Rightarrow B_o = B \& A'$

Alternate: $D = A B' + A' B$; $B_o = B A'$

4) Gate-level circuit (ASCII diagram)

```
A ----+-----+
      |           |
      |   XOR     | --> D (Difference)
      |           |
B ----+-----(A xor B)-----+
```

```
A ----+
      |-- NOT --+
      |
B -----+--- AND ---+--> Borrow (Bo)
```

5) Block diagram

Inputs: $A, B \rightarrow$ [Half Subtractor] \rightarrow Outputs: D, Borrow

6) Examples

$A=1, B=0 \rightarrow D=1, \text{Borrow}=0$

$A=0, B=1 \rightarrow D=1, \text{Borrow}=1$

$A=1, B=1 \rightarrow D=0, \text{Borrow}=0$