

Topic

Date

University of Delhi - Open Book Examination (Semester Examination)

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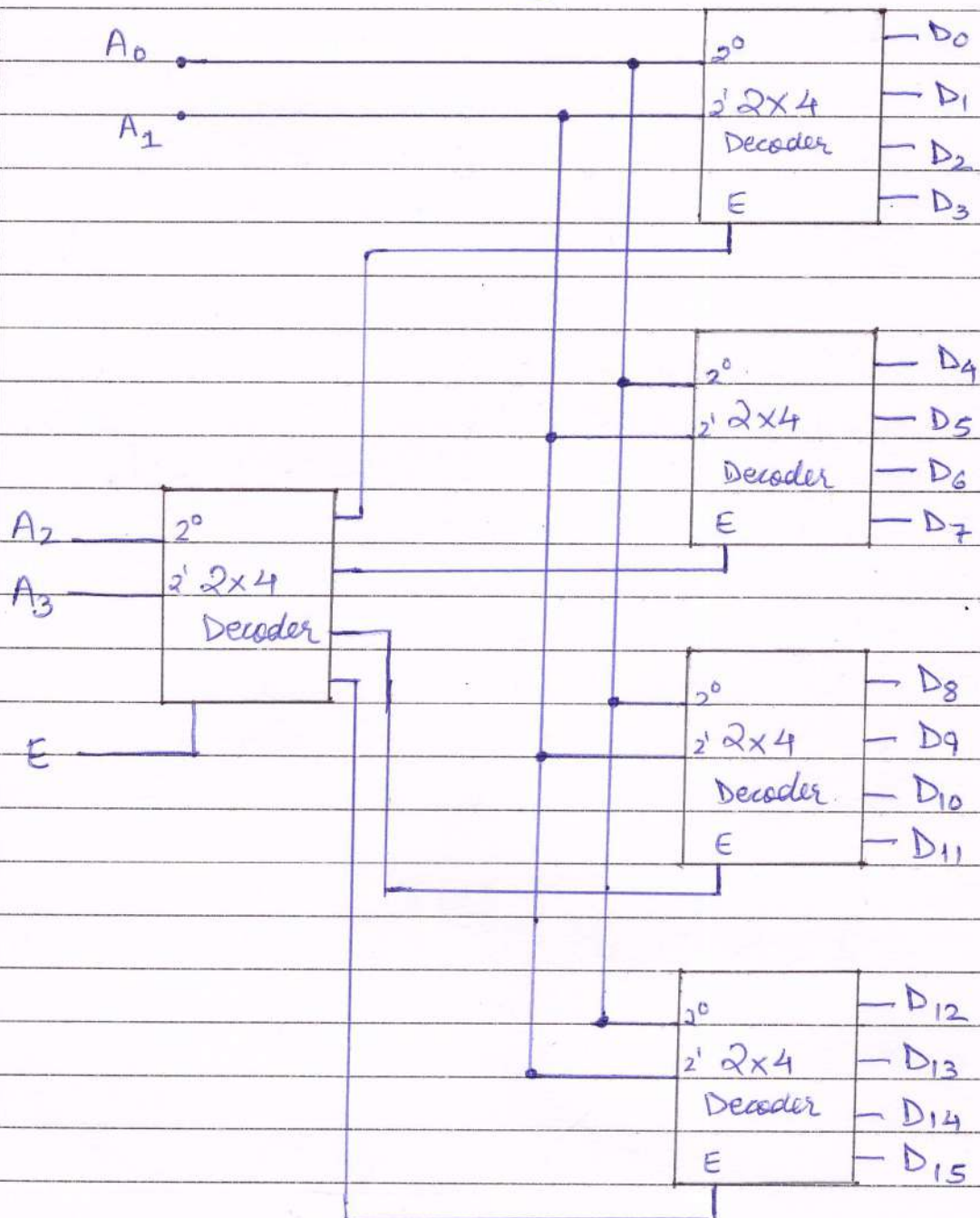
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Date

QUESTION : 5Ans

a) 4x16 decoder using five 2x4 decoder



Topic

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INPUTS						OUTPUTS
E	A ₃	A ₂	A ₁	A ₀		
0	X	X	X	X		0
1	0	0	0	0		D ₀ = 1
1	0	0	0	1		D ₁ = 1
1	0	0	1	0		D ₂ = 1
1	0	0	1	1		D ₃ = 1
1	0	1	0	0		D ₄ = 1
1	0	1	0	1		D ₅ = 1
1	0	1	1	0		D ₆ = 1
1	0	1	1	1		D ₇ = 1
1	1	0	0	0		D ₈ = 1
1	1	0	0	1		D ₉ = 1
1	1	0	1	0		D ₁₀ = 1
1	1	0	1	1		D ₁₁ = 1
1	1	1	0	0		D ₁₂ = 1
1	1	1	0	1		D ₁₃ = 1
1	1	1	1	0		D ₁₄ = 1
1	1	1	1	1		D ₁₅ = 1

b) $128K \times 16 \Rightarrow 2^7 \times 2^{10} \times 2^4 \Rightarrow 2^{21} \text{ bits}$
 $1 \text{ byte} = 8 \text{ bits} = 2^3 \text{ bits}$
 $\Rightarrow \frac{2^{21}}{2^3} = 2^{18} \text{ bytes}$

$$128K \times 16 \text{ Memory} = 2^{18} \text{ bytes}$$

$$\Rightarrow 128K \times 16$$

$$\Rightarrow 128K = 2^{17}$$

Address Lines

17

Data Lines

16

PAGE No. - 2

Topic

Date

$$\text{No. of chips} = \frac{4096 \times 16}{256 \times 8} = \frac{2^{12} \times 2^4}{2^8 \times 2^3}$$

$$[\text{Memory capacity} = 4096 \times 16] = 2^5 = 32 \text{ chips}$$

- c)
- (i) Status Command
 - (ii) Control Command (Also use status to check if reached at correct address)
 - ~~(iii)~~
 - (iii) Status Command
 - (iv) Output Command