GUIDELINES of B.Sc. (H) COMPUTER SCIENCE
(Under Choice Based Credit System Scheme)
Semester I
BHCS02
COMPUTER SYSTEM ARCHITECTURE

S. No.	Chapter Number and Name	Section Numbers	No. of Lectures*	Chapter wise Weightage* (Marks)
1	Ch 1: Digital Logic Circuits	1.1, 1.2, 1.3, 1.4, 1.5, 1.6 , 1.7 (up to pg. 28)	10	12
2	Ch 2: Digital Components	2.2, 2.3, 2.4, 2.7	6	8
3	Ch 3: Data Representation	3.1, 3.2, 3.3, 3.4	9	10
4	Ch 5: Basic Computer Organization and Design	5.1, 5.2, 5.3, 5.4 (up to pg. 137), 5.5, 5.6, 5.7, 5.8 5.9 (up to pg. 159)	14	15
5	Ch 8: Central Processing Unit	8.1, 8.2, 8.3 (up to pg. 247), 8.4, 8.5, 8.8 (only characteristics, i.e., pg. 282 – 284)	10	14
6	Ch 9: Pipeline and Vector Processing	9.1, 9.2	3	6
7	Ch 11: Input Output Organization	11.1 (up to pg. 383), 11.2 (up to pg. 389, excluding example of I/O Interface), 11.4, 11.6	8	10

Reference:

Computer System Architecture: Morris M. Mano (PHI, 3rd Edition)#

The Page numbers may vary in Pearson Edition

*The number of lectures and chapter wise weightage may be treated as indicative only.