

# **BCSC 0804 - LAB RECORD**

**Submitted by: Priyanshu Vishwakarma**

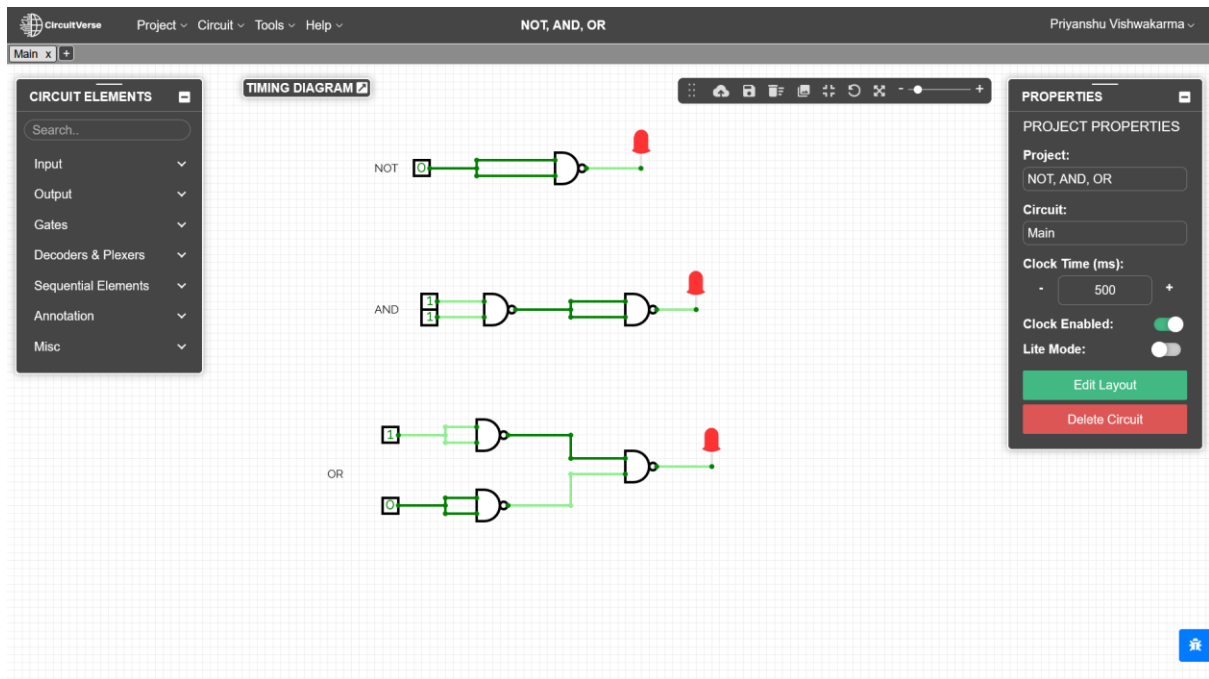
**Section: B**

**Roll No.: 14**

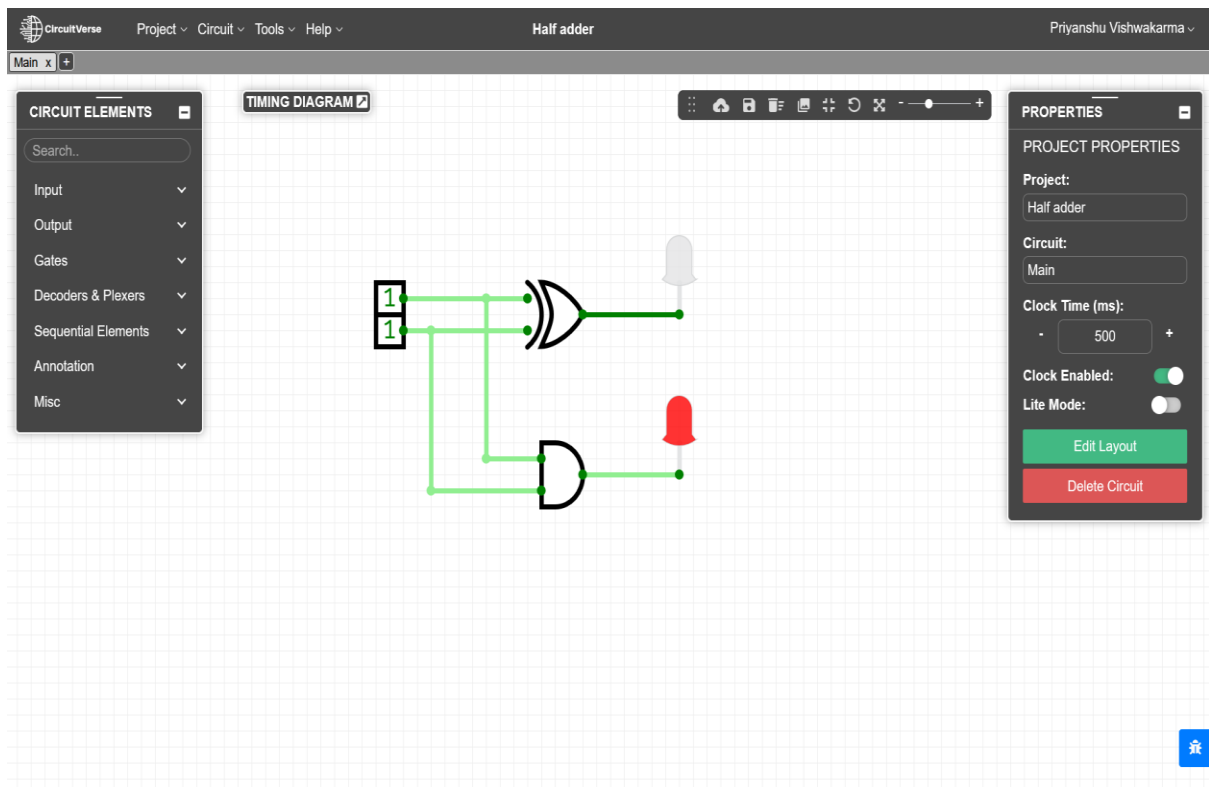
**University Rollno.: 191500611**

**Submitted to: Mrs. Mona Kumari**

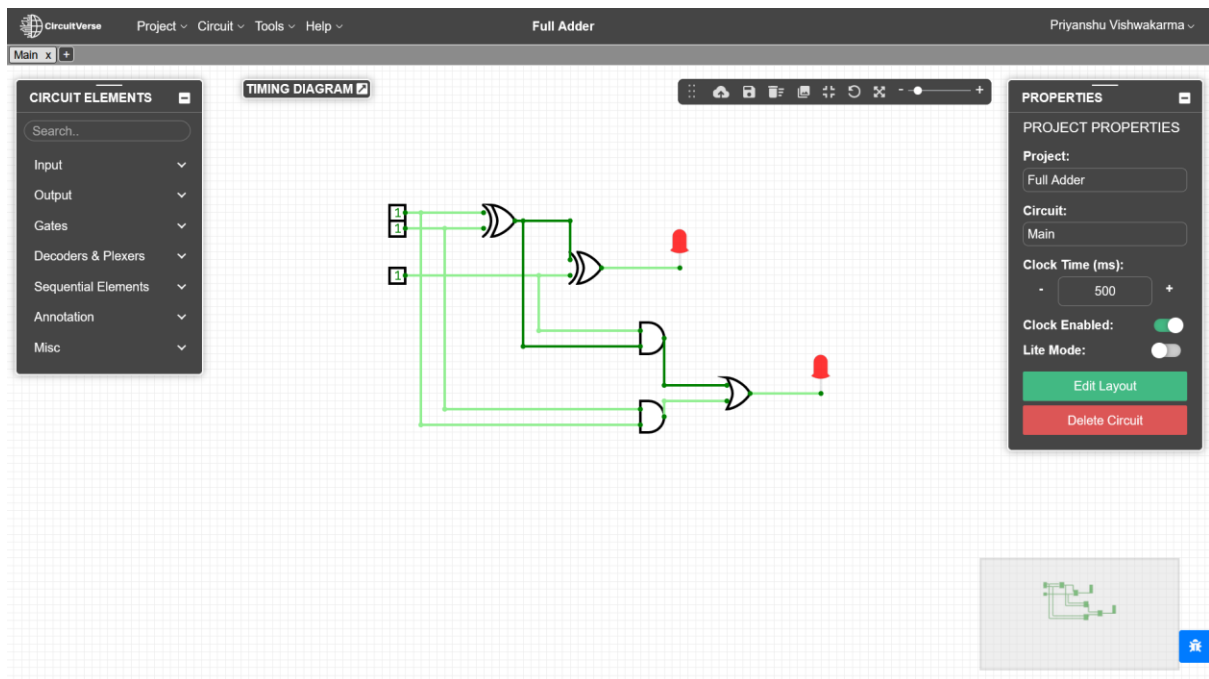
## 1. NOT, AND, OR GATE USING NAND GATE



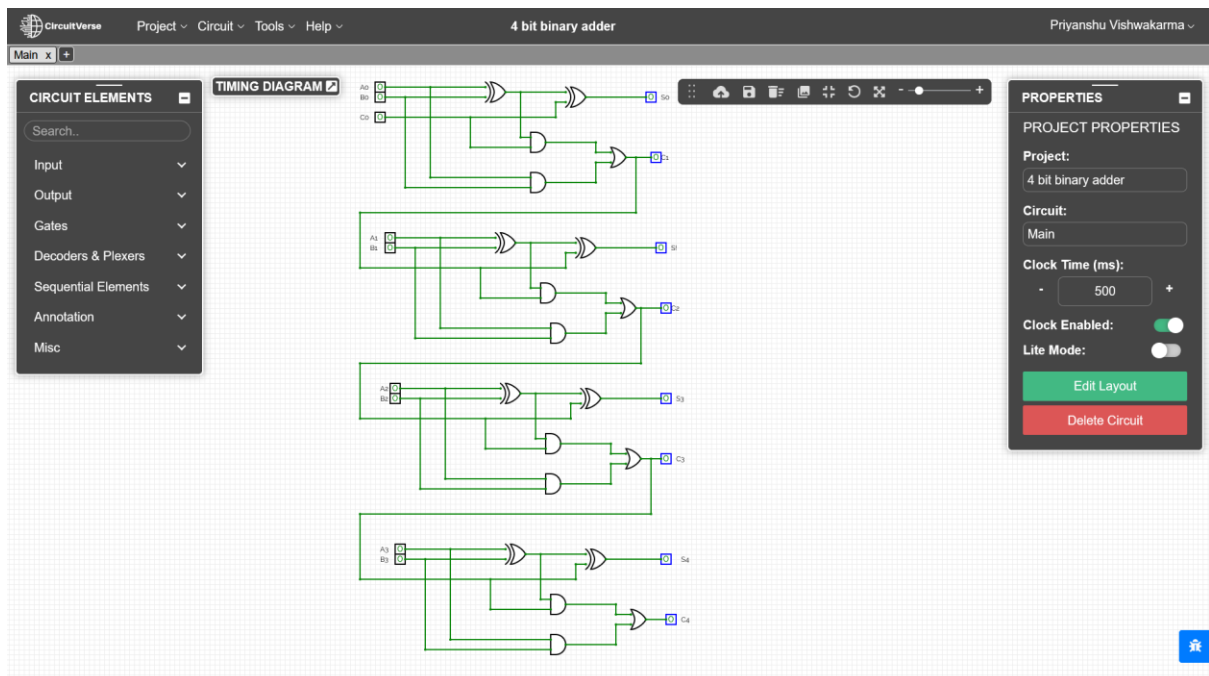
## 2. HALF ADDER



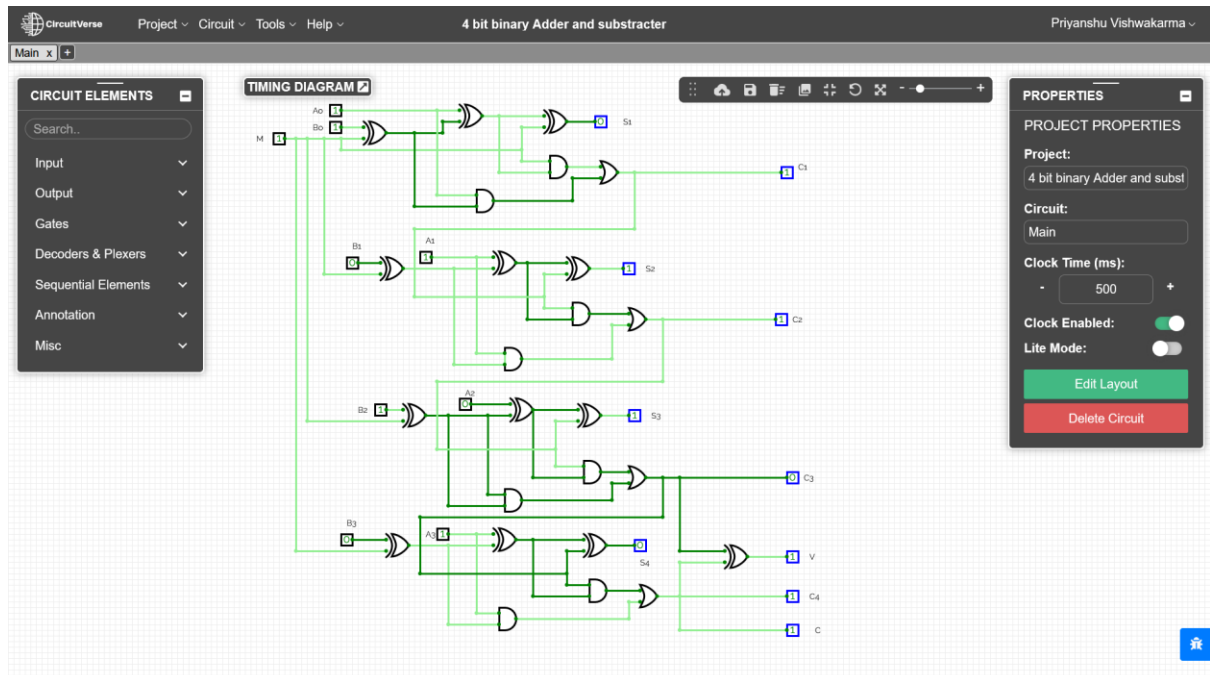
### 3. FULL ADDER



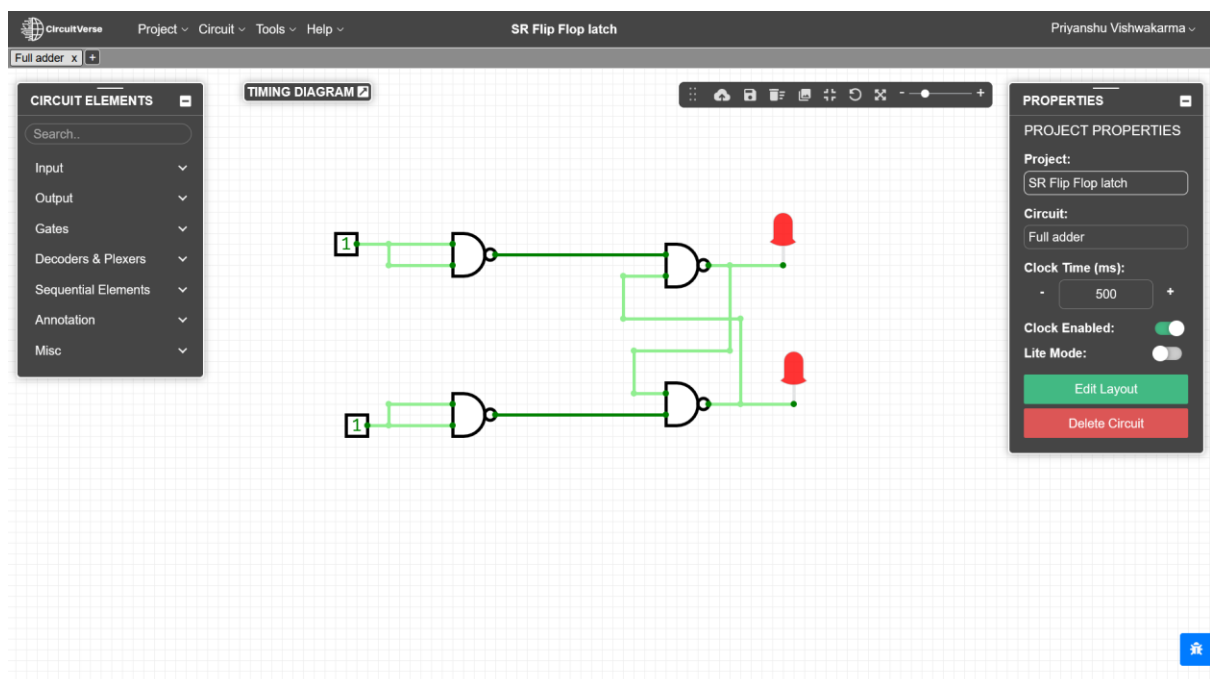
### 4. 4 bit Binary ADDER



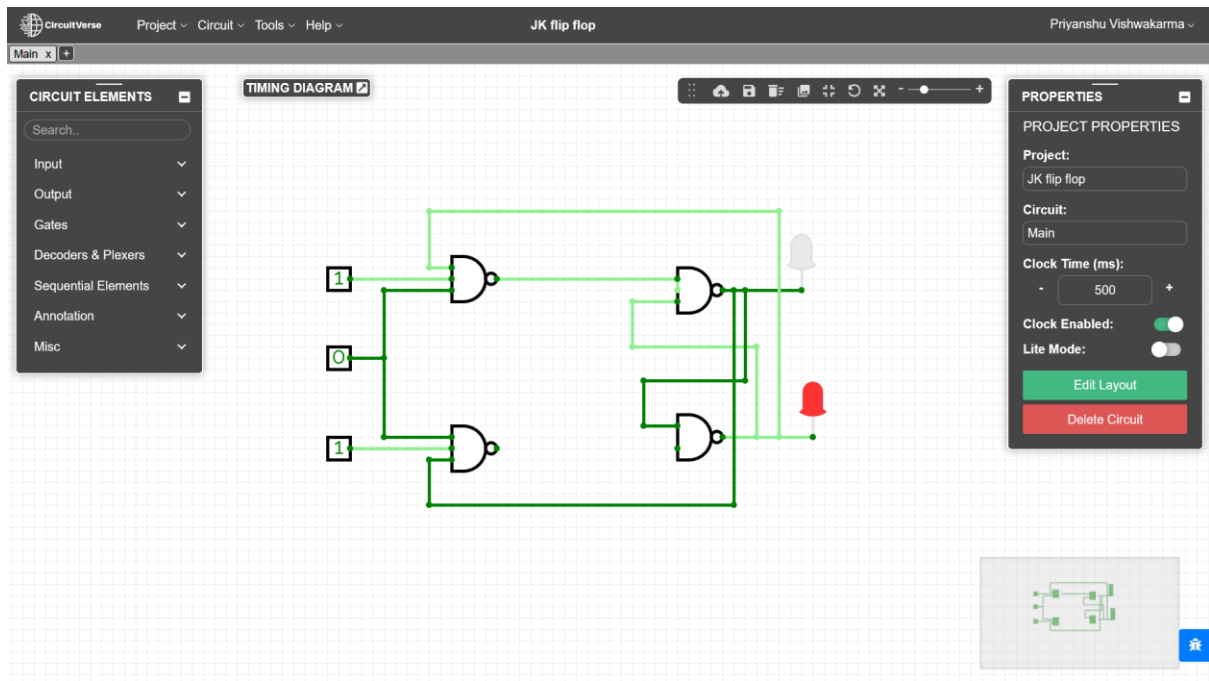
## 5. 4 bit Binary ADDER & SUBTRACTER



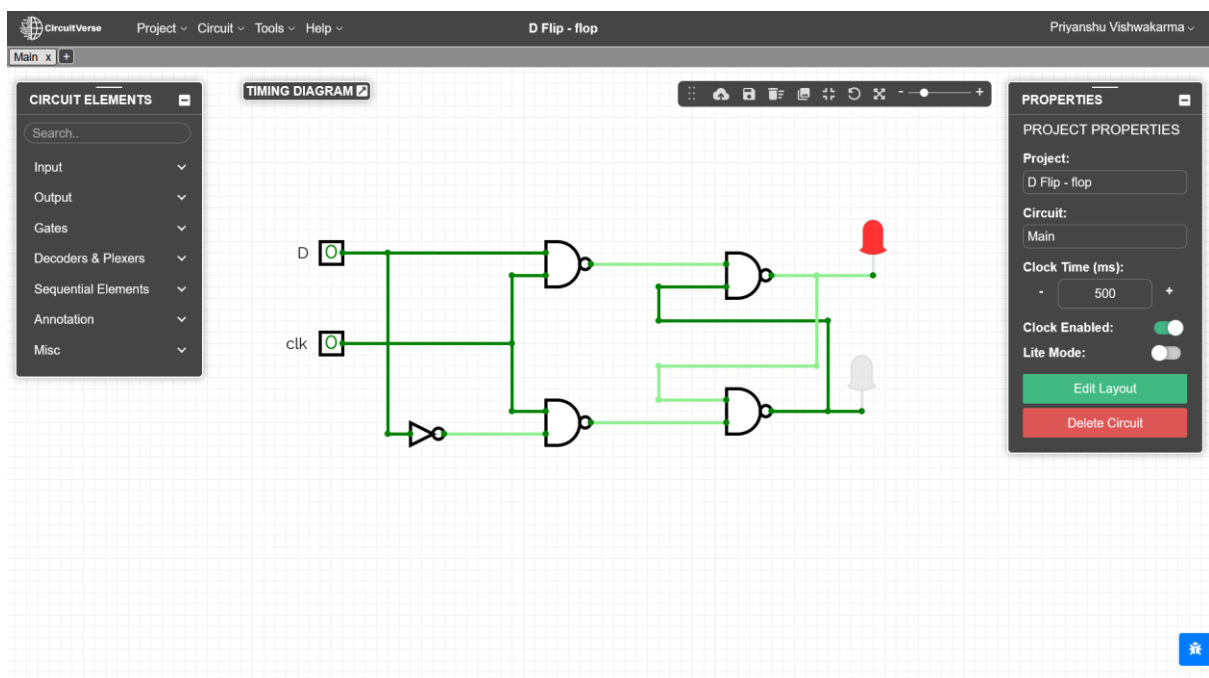
## 6. SR FLIP-FLOP LATCH



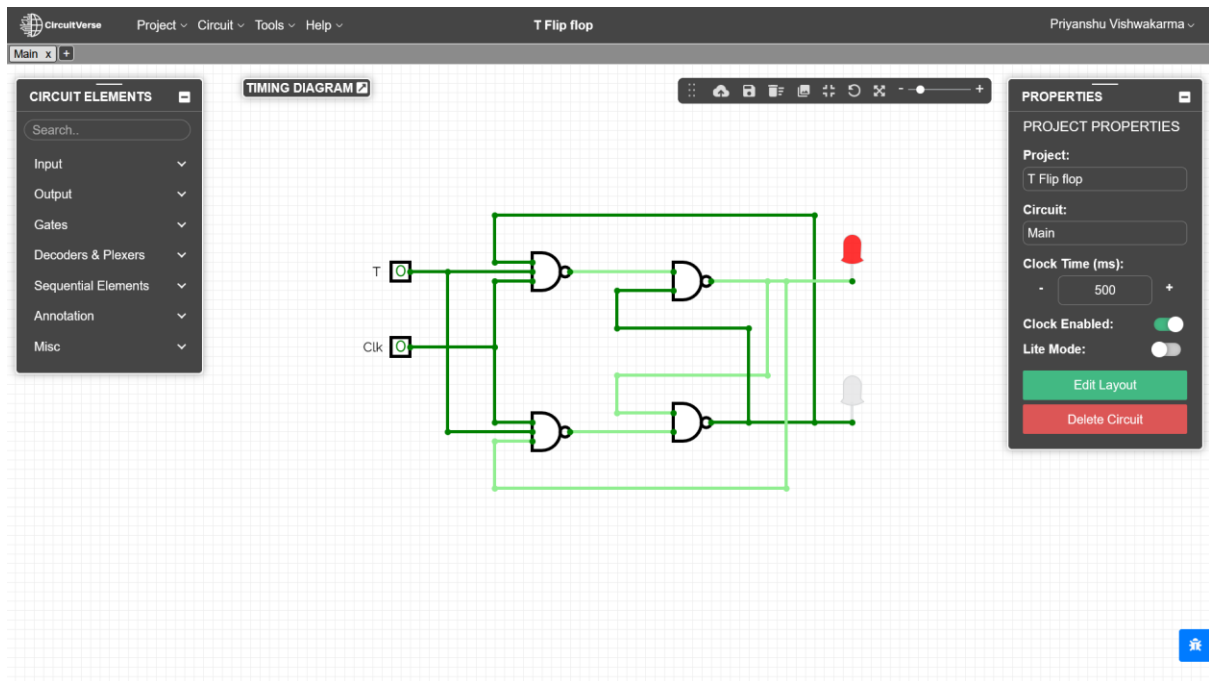
## 7. JK FLIP-FLOP



## 8. D FLIP-FLOP



## 9. T FLIP-FLOP



## 10. COUNTER

