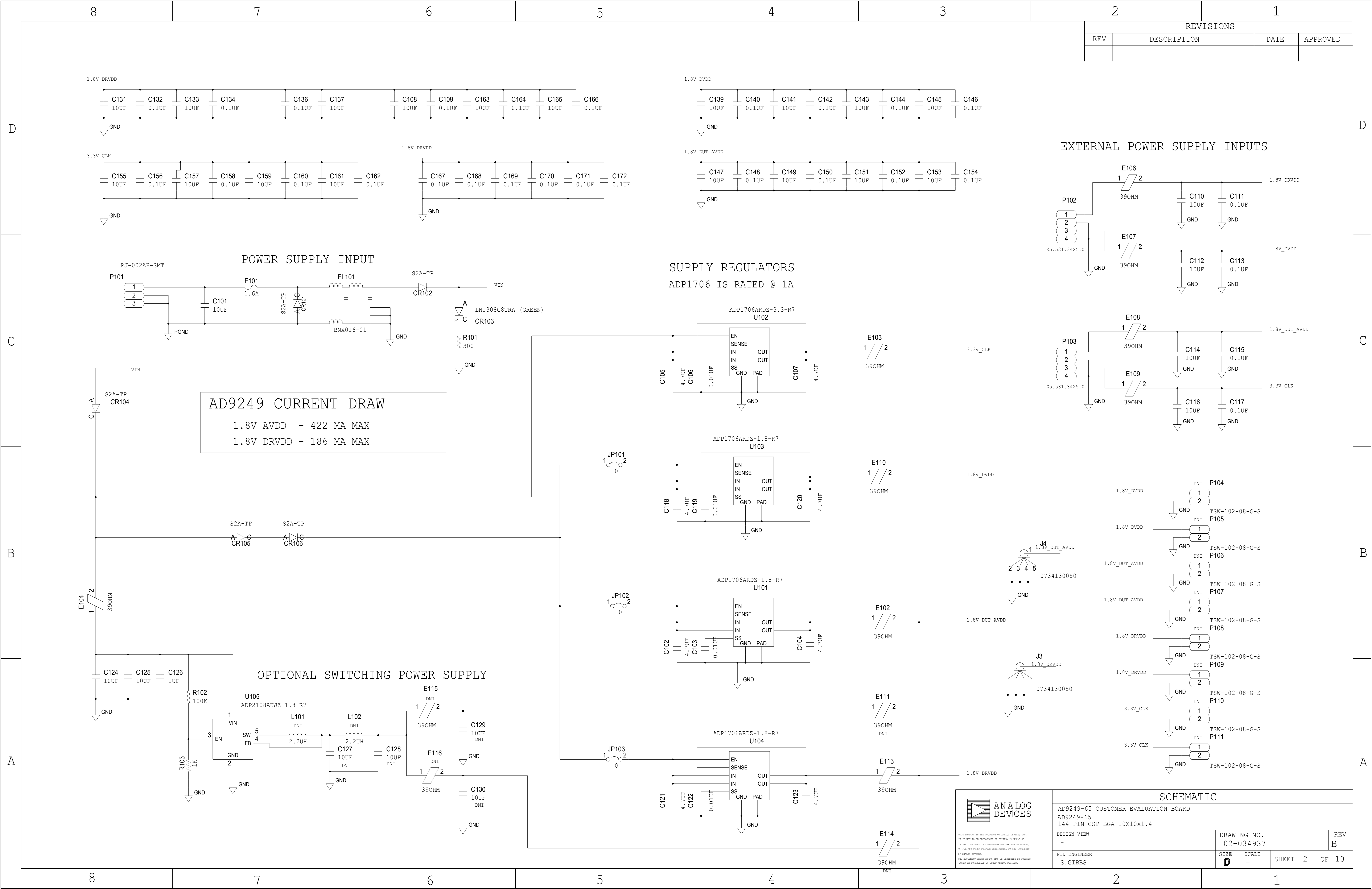
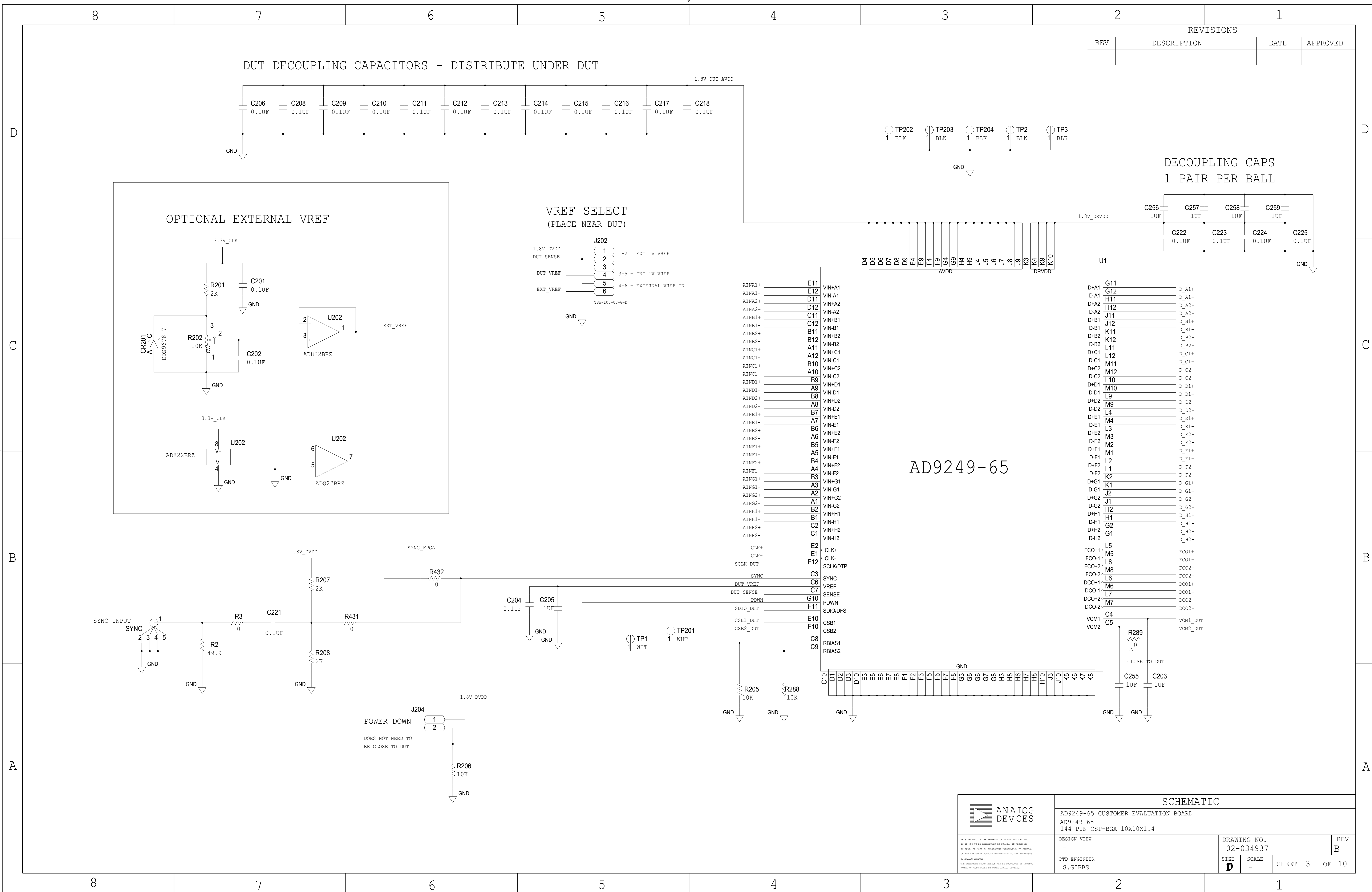
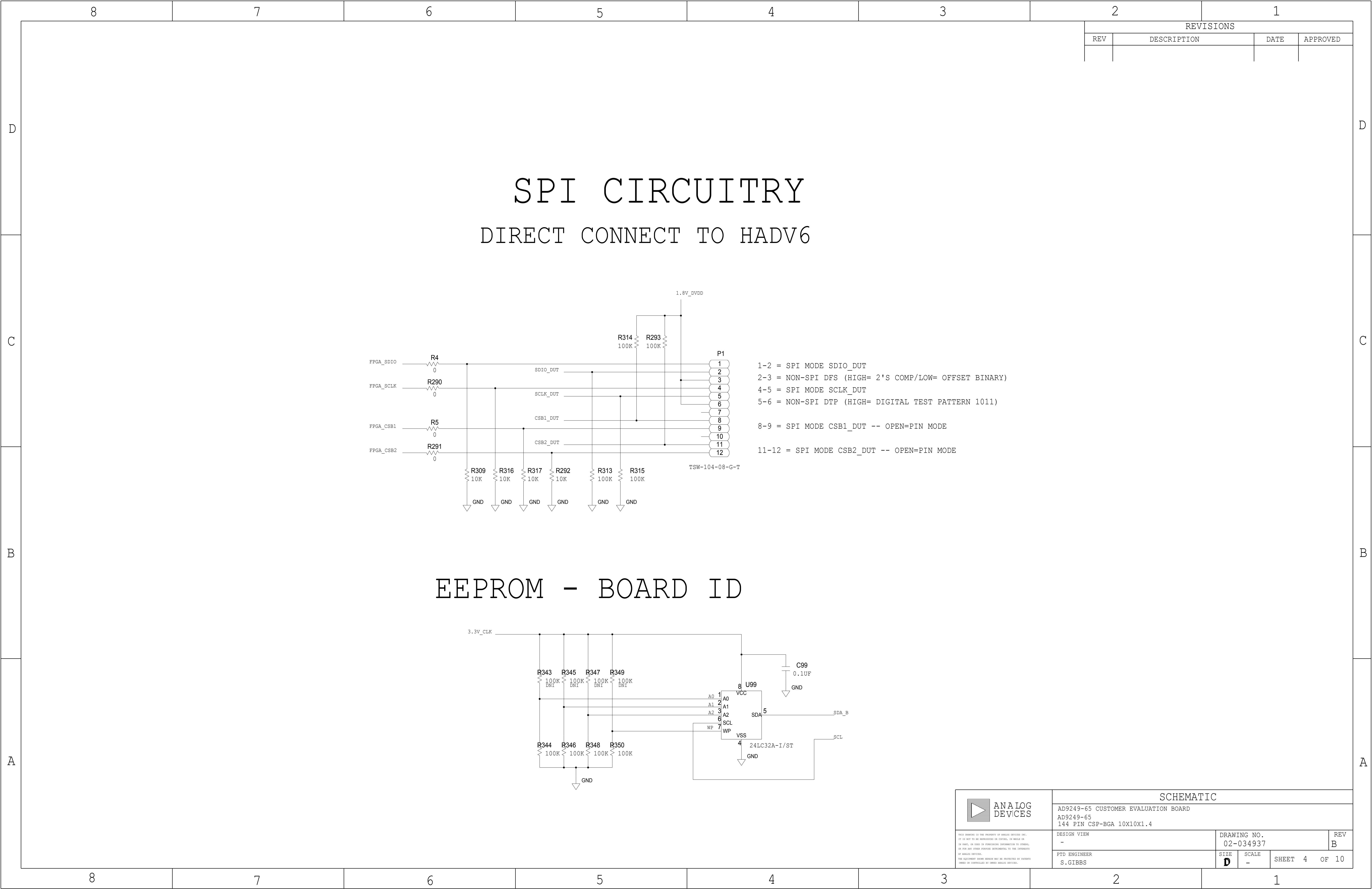


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1-2 = SPI MODE SDIO_DUT

2-3 = NON-SPI DFS (HIGH= 2'S COMP/LOW= OFFSET BINARY)

4-5 = SPI MODE SCLK_DUT

5-6 = NON-SPI DTP (HIGH= DIGITAL TEST PATTERN 1011)

8-9 = SPI MODE CSB1_DUT -- OPEN=PIN MODE

11-12 = SPI MODE CSB2_DUT -- OPEN=PIN MODE

EEPROM - BOARD ID

3.3V_CLK

R343

100K

DNI

R345

100K

DNI

R347

100K

DNI

R349

100K

DNI

R344

100K

GND

R346

100K

GND

R348

100K

GND

R350

100K

GND

A0

1

A1

2

A2

3

SCL

6

WP

7

VCC

8

VSS

4

U99

24LC32A-I/ST

C99

0.1UF

GND

SDA_B

5

SCL

ANALOG

DEVICES

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SCHEMATIC

AD9249-65 CUSTOMER EVALUATION BOARD

AD9249-65

144 PIN CSP-BGA 10X10X1.4

DESIGN VIEW

-

DRAWING NO.

02-034937

REV

B

PTD ENGINEER

S.GIBBS

SIZE

D

SCALE

-

SHEET

4

OF

10

