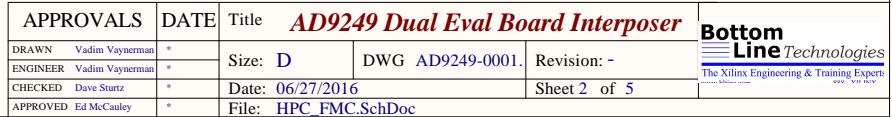
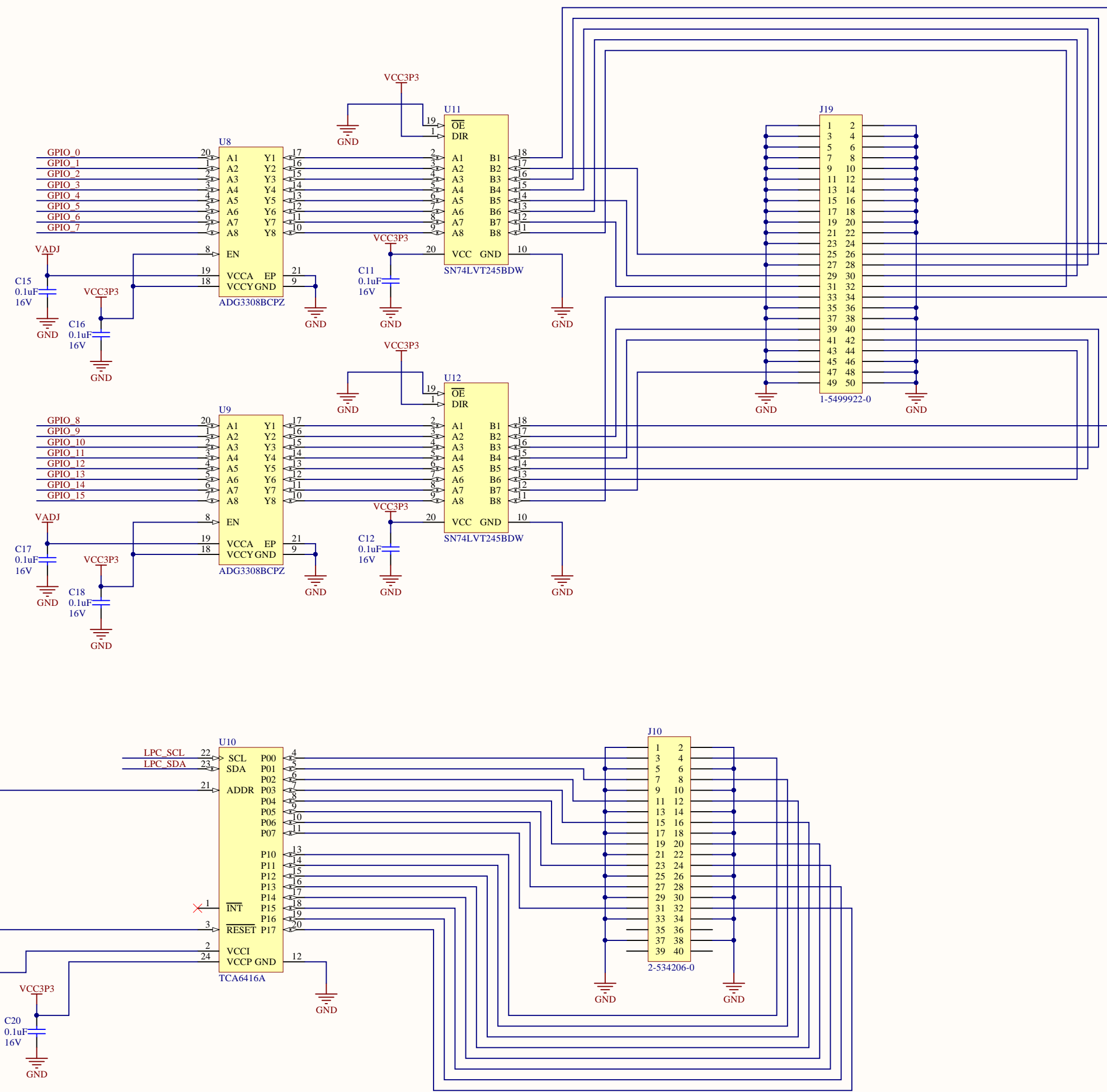
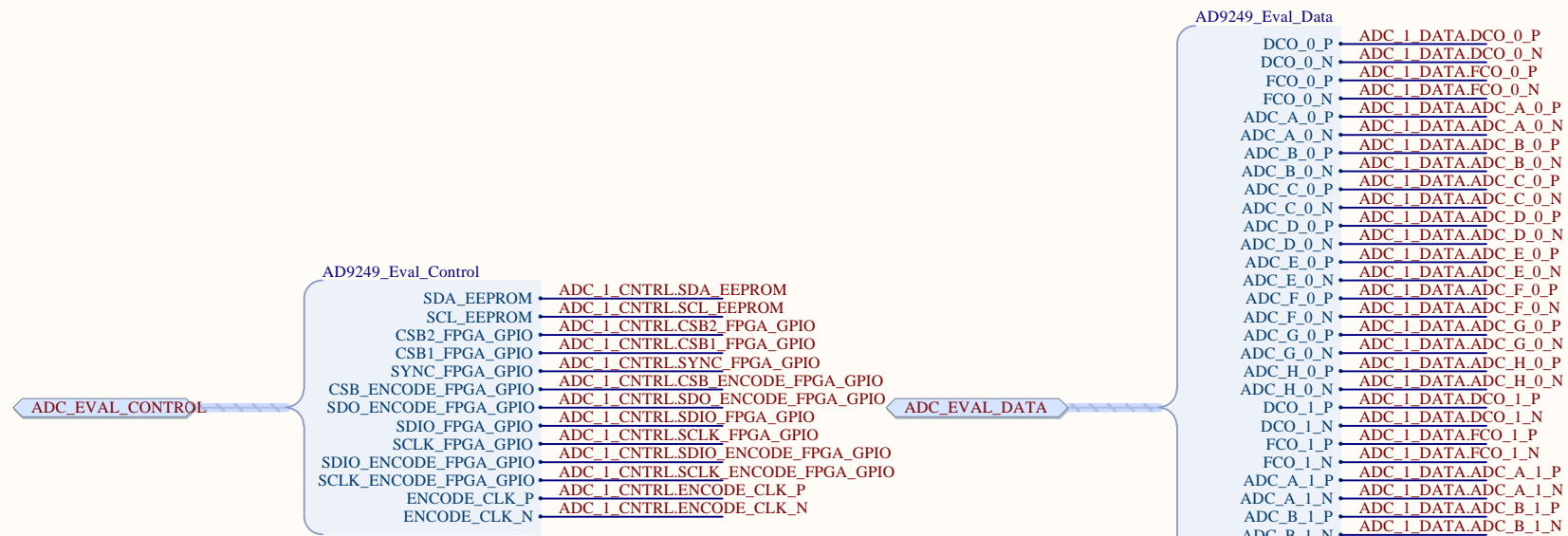
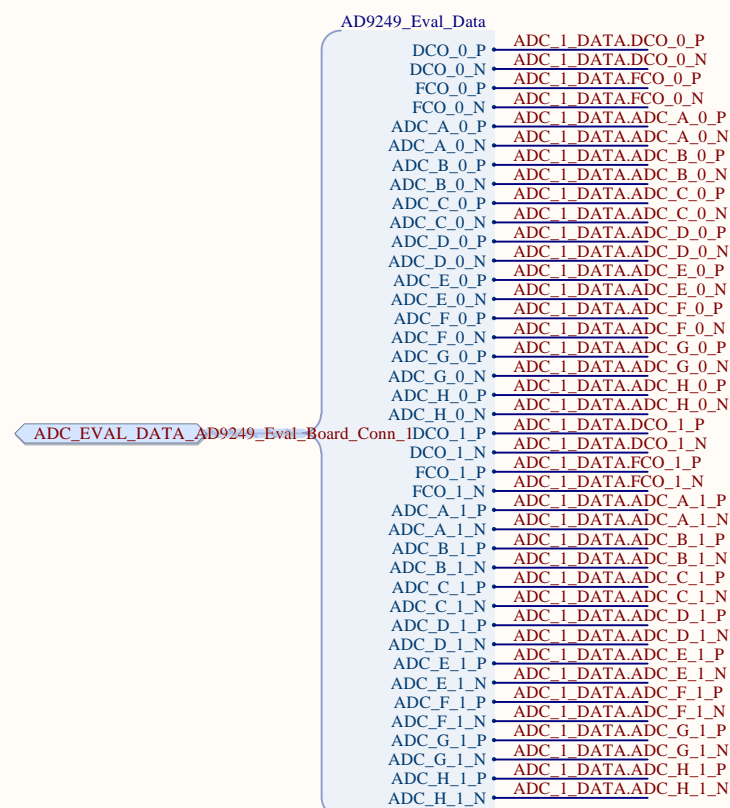
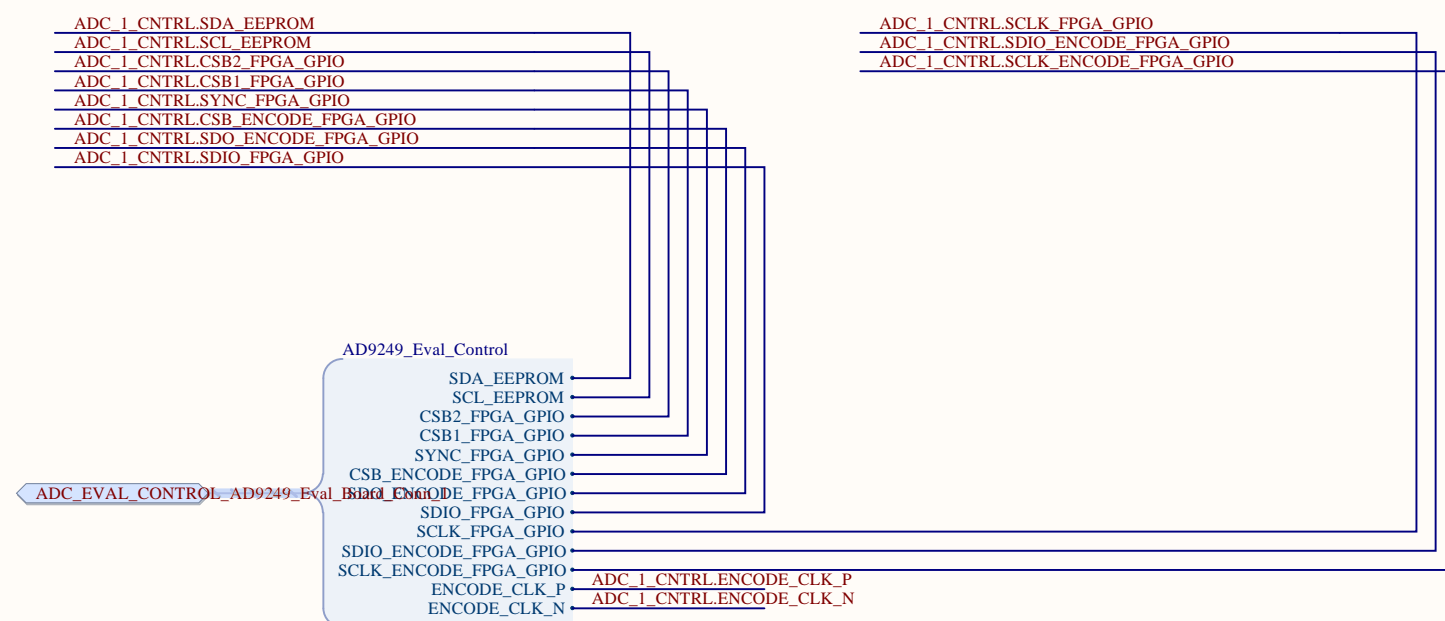
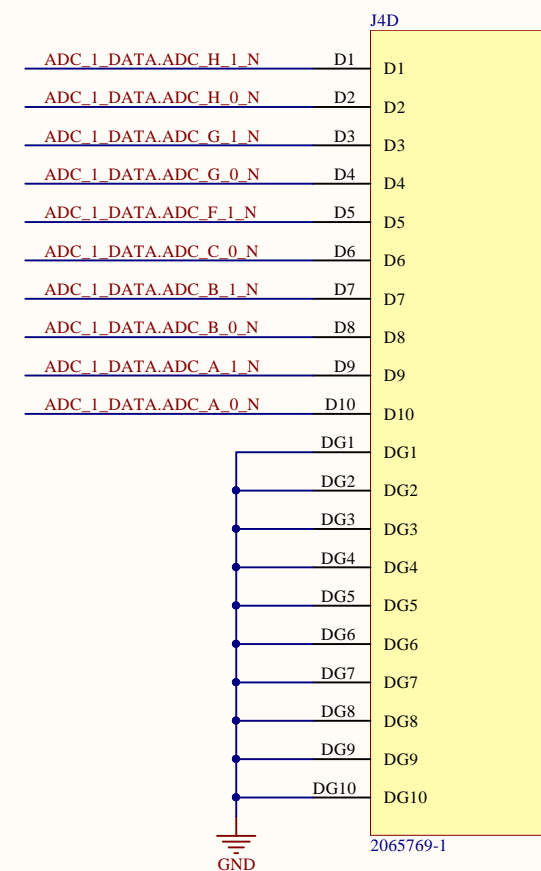
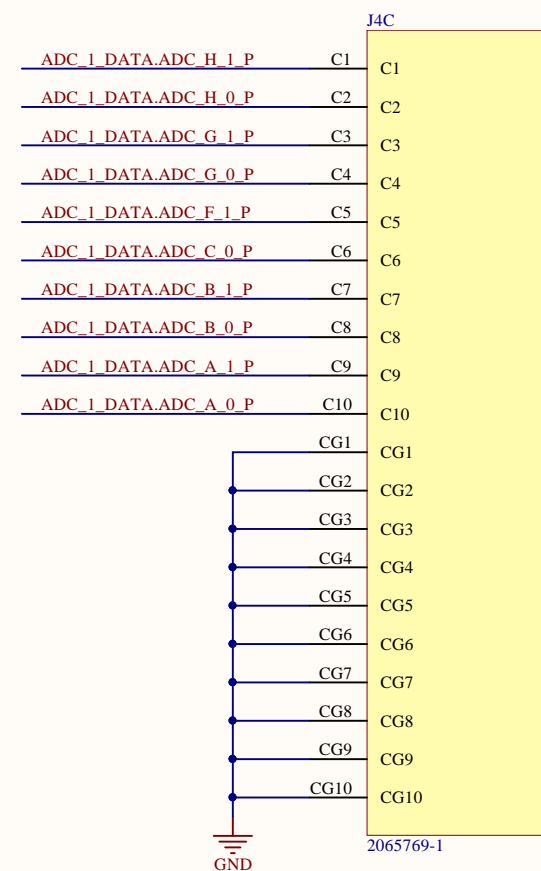
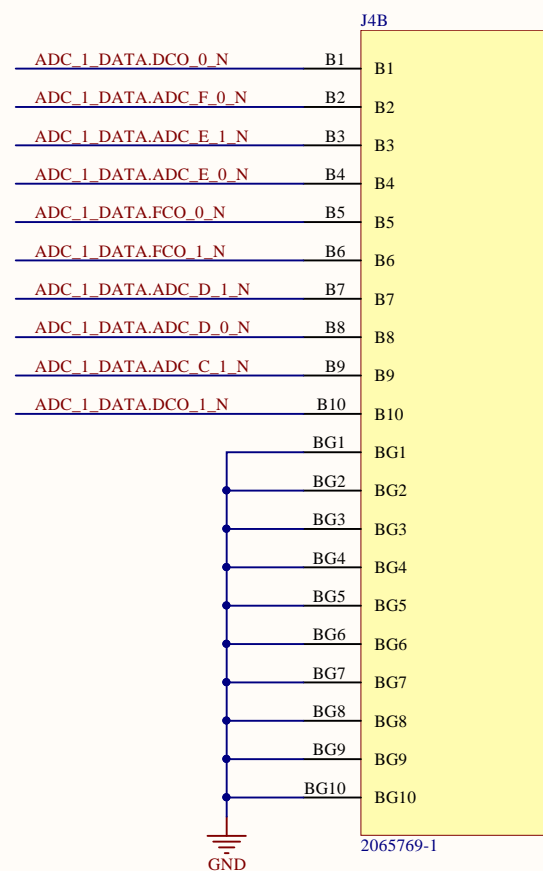
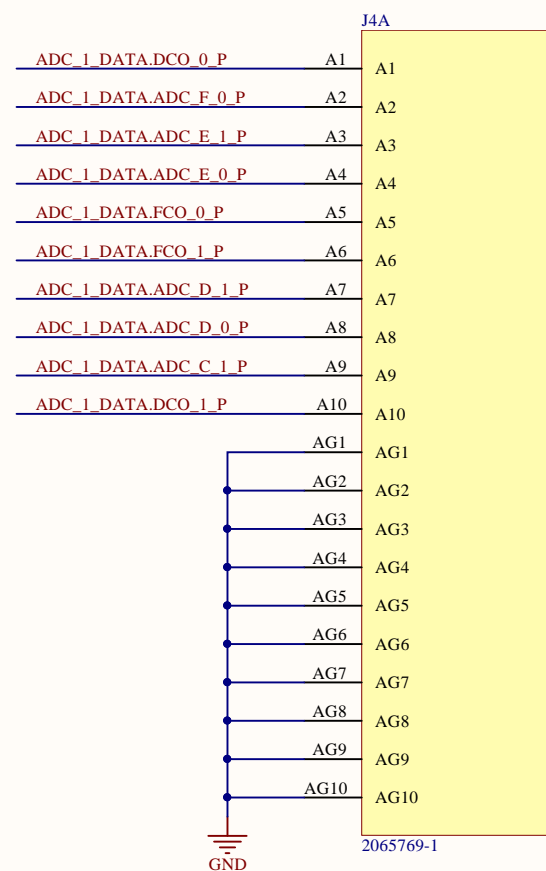
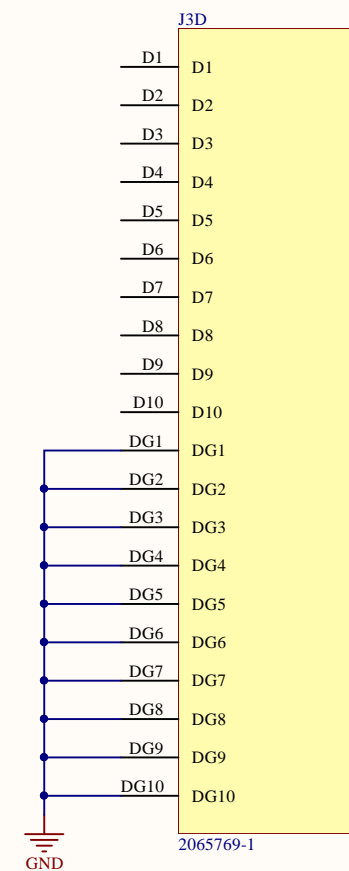
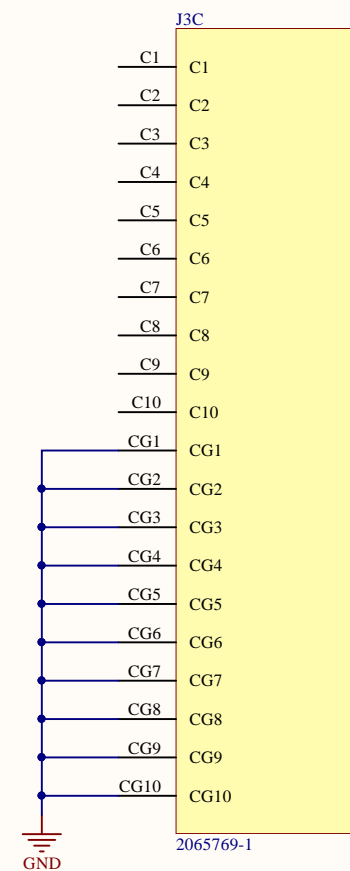
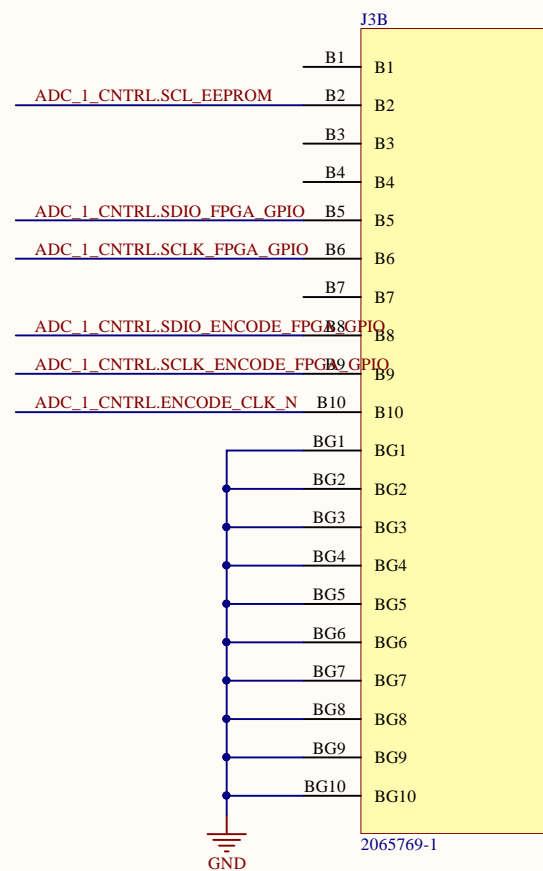
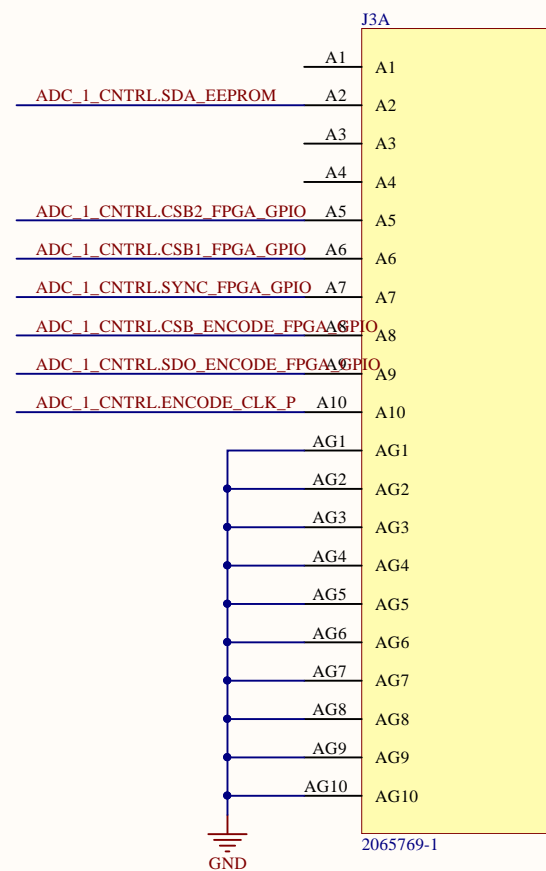



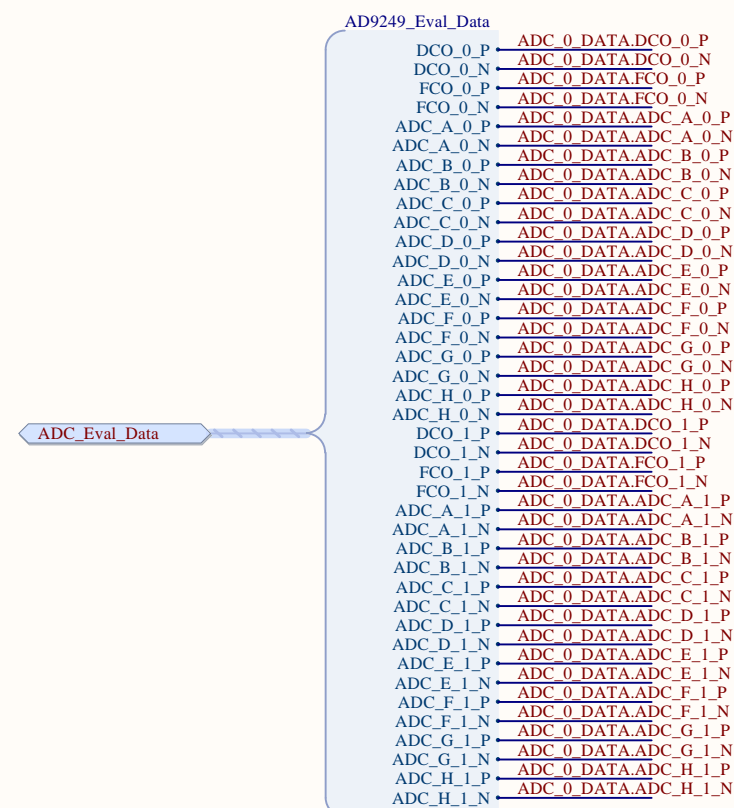
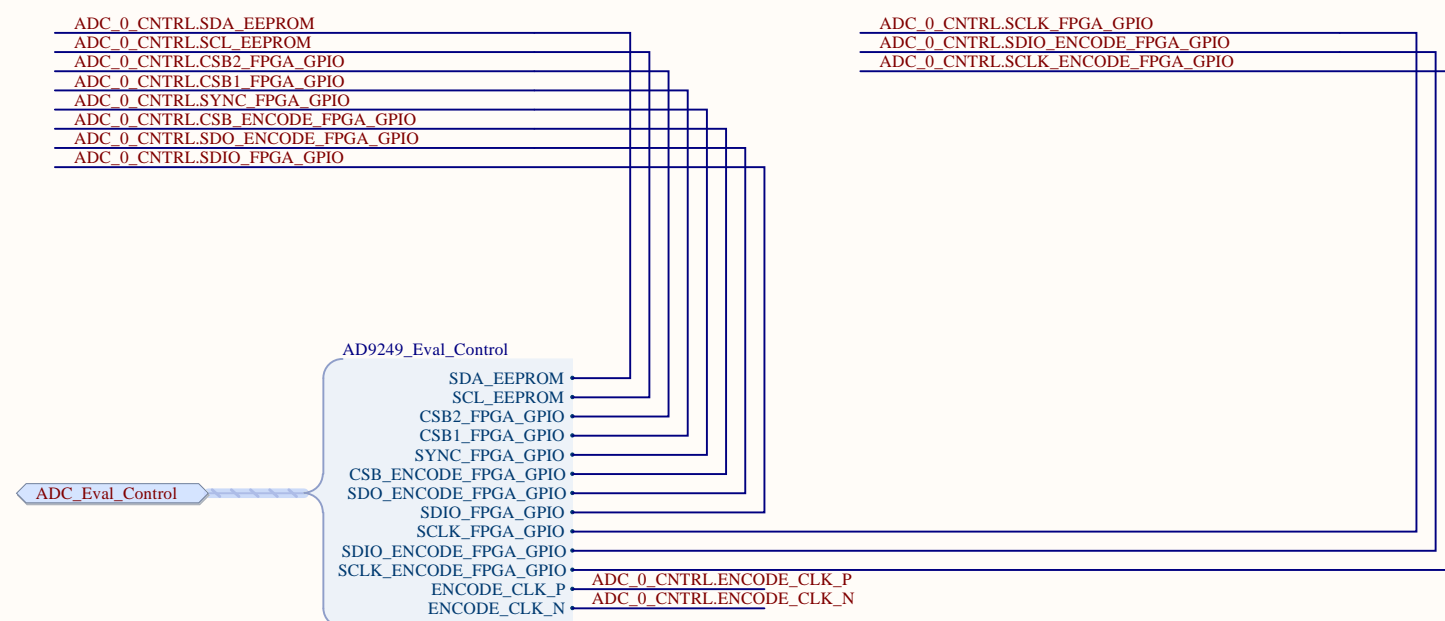
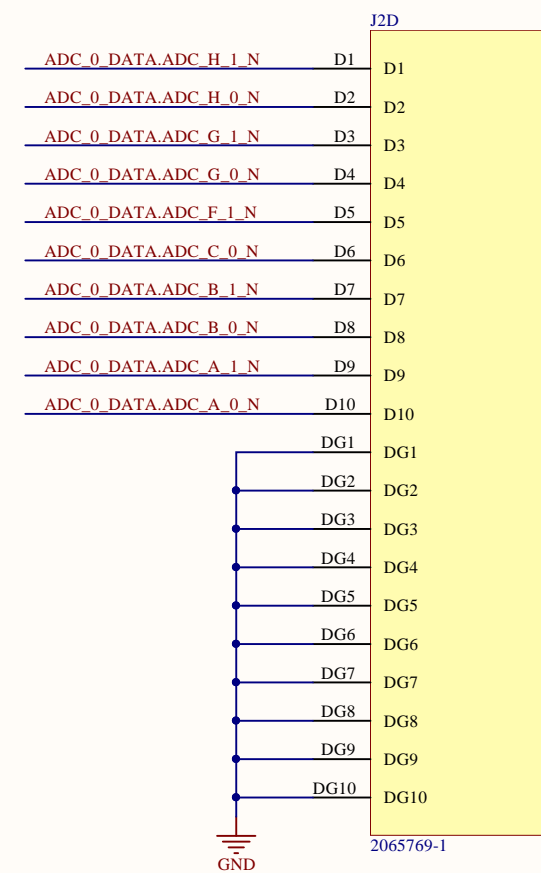
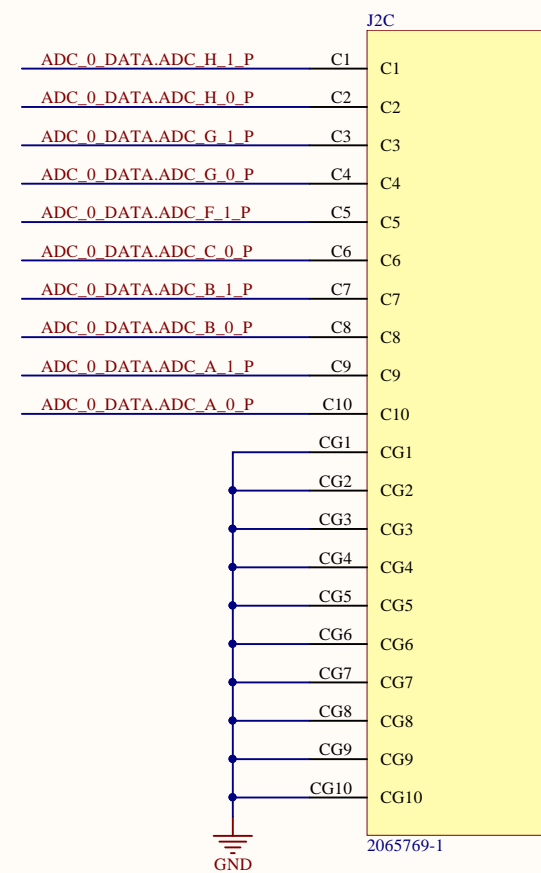
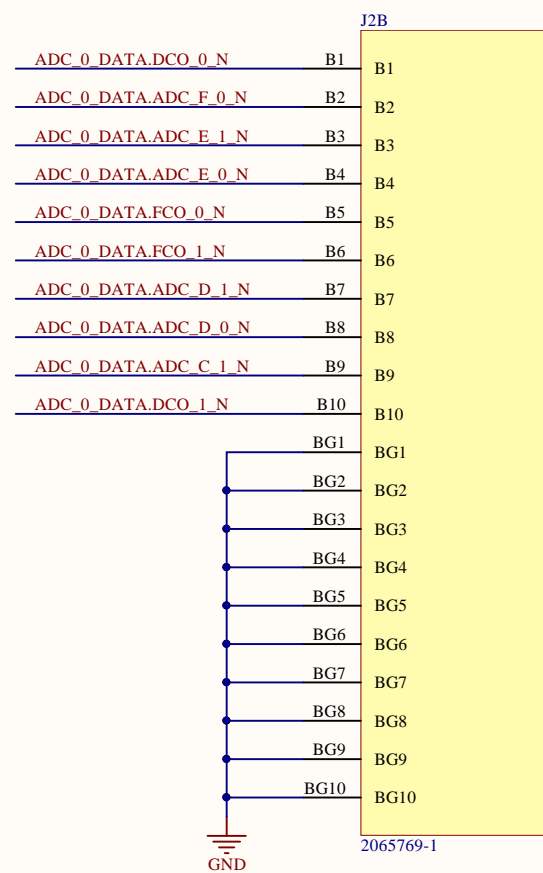
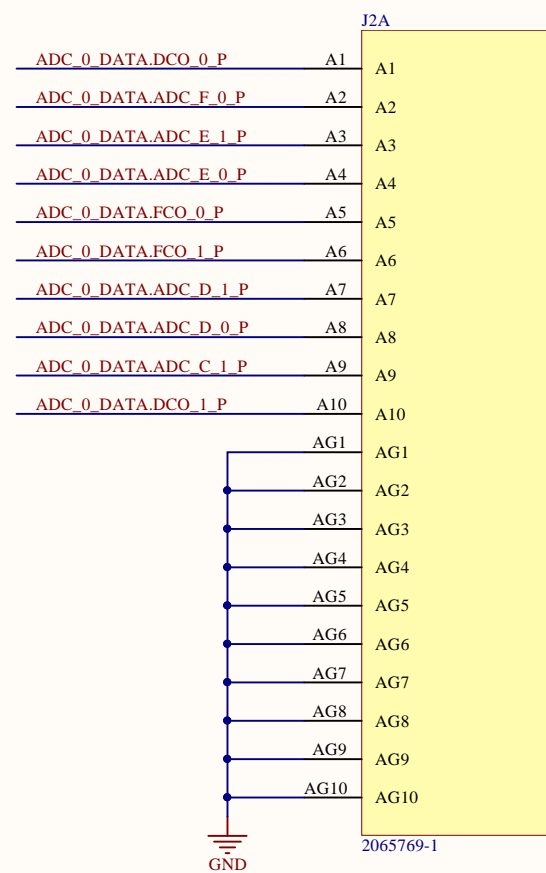
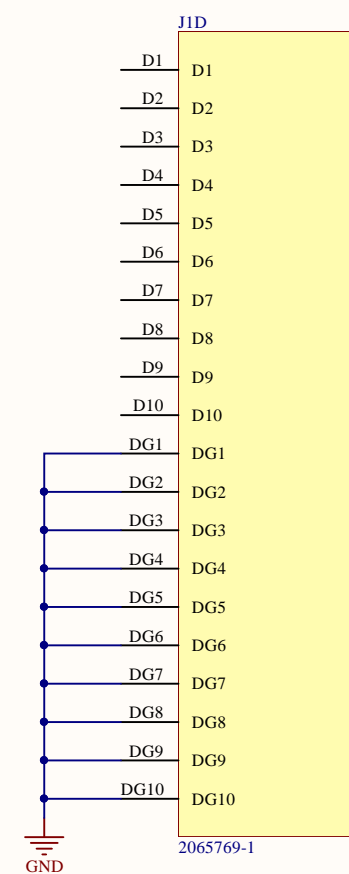
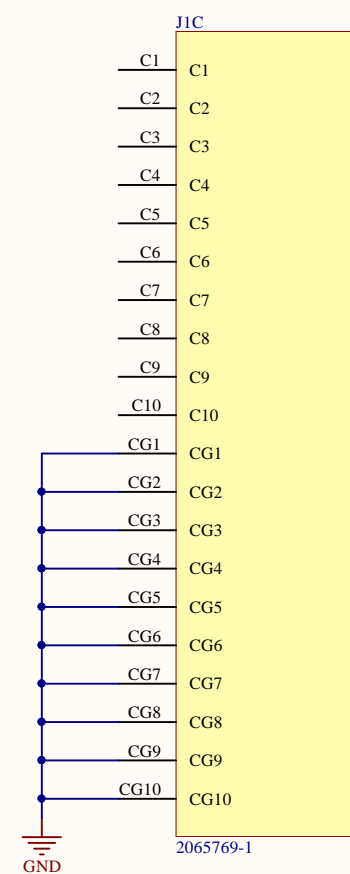
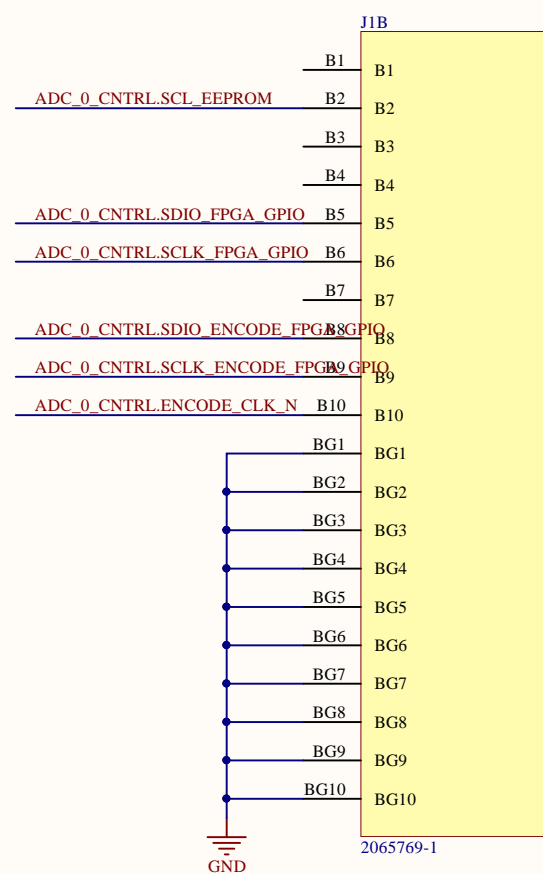
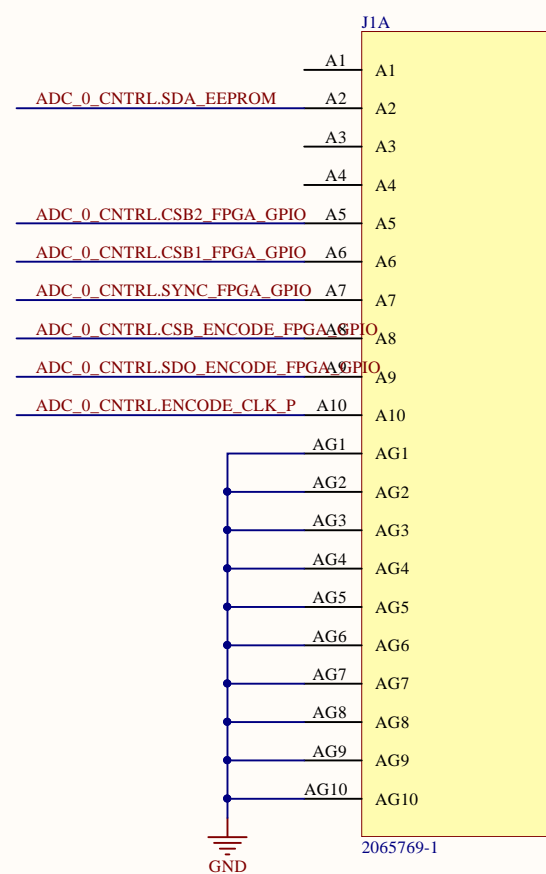
APPROVALS		DATE	Title AD9249 Dual Eval Board Interposer			<div>Bottom LineTechnologies</div> <div>The Xilinx Engineering & Training Experts www.bllt.com 888-711-1611</div>
DRAWN	Vadim Vaynerman	*	Size: B	DWG AD9249-0001	Revision: -	
ENGINEER	Vadim Vaynerman	*				
CHECKED	Dave Sturtz	*	Date: 06/27/2016	Sheet 1 of 5		
APPROVED	Ed McCauley	*	File: Top.SchDoc			








APPROVALS	DATE	Title AD9249 Dual Eval Board Interposer		 The Mixin Engineering & Training Experts www.bottomline-tech.com 08-330-85V-1
DRAWN	Vadim Vagnerman	*		
ENGINEER	Vadim Vagnerman	*	Size: C DWG AD9249-0001.2 Revision: -	
CHECKED	Dave Carsten	*	Date: 06/27/2016 Sheet 4 of 5	
APPROVED	Ed M. Gault	*	File: HAD_Connectors_SchDoc	



APPROVALS	DATE	Title AD9249 Dual Eval Board Interposer			 www.btm.com The Milling Engineering & Training Experts 400-530-3831
DRAWN	Vadim Vagnerman	*	Size: C	DWG AD9249-0001.1	
ENGINEER	Vadim Vagnerman	*		Revision: -	
CHECKED	Date Sturtz	*	File: 06/27/2016	Sheet 5 of 5	
APPROVED	Ed McCauley	*	File: HAD_Connectors_SchDoc		