## ARCHITECTURAL ENHANCEMENTS TO

## XSM

#### A THESIS

Submitted by

#### GOVIND R B110041CS

In partial fulfilment for the award of the degree of

# $\begin{array}{c} {\rm BACHELOR~OF~TECHNOLOGY} \\ {\rm IN} \\ {\rm COMPUTER~SCIENCE~AND~ENGINEERING} \end{array}$

Under the guidance of DR. K MURALIKRISHNAN



DEPARTMENT OF COMPUTER ENGINEERING NATIONAL INSTITUTE OF TECHNOLOGY CALICUT NIT CAMPUS PO, CALICUT KERALA, INDIA 673601

May 9, 2015

## ACKNOWLEDGEMENTS

Your acknowledgements

 $\mathbf{R}$ 

#### **DECLARATION**

"I hereby declare that this submission is my own work and that, to the best of my knowledge and belief, it contains no material previously published or written by another person nor material which has been accepted for the award of any other degree or diploma of the university or other institute of higher learning, except where due acknowledgment has been made in the text".

Place:	Signature :
Date:	Name:
	$\operatorname{Reg.No}$ :

#### **CERTIFICATE**

This is to certify that the thesis entitled: "ARCHITECTURAL ENHANCE-MENTS TO XSM" submitted by Sri/Smt/Ms GOVIND R B110041CS to National Institute of Technology Calicut towards partial fulfillment of the requirements for the award of Degree of Bachelor of Technology in Computer Science Engineering is a bonafide record of the work carried out by him/her under my/our supervision and guidance.

Signed by	y Thesis	Supervison	$\cdot(s)$	with	name(s)	) and	date
-----------	----------	------------	------------	------	---------	-------	------

Place: Date:

Signature of Head of the Department

Office Seal

## Contents

Chapter	
1 Introduction	1
2 Topic	2
Bibliography	Ę
Appendix	
A Appendix	4

### Abstract

Abstract here. Abstract should not exceed one page.  $\,$  if @ Tables

Table

Figures

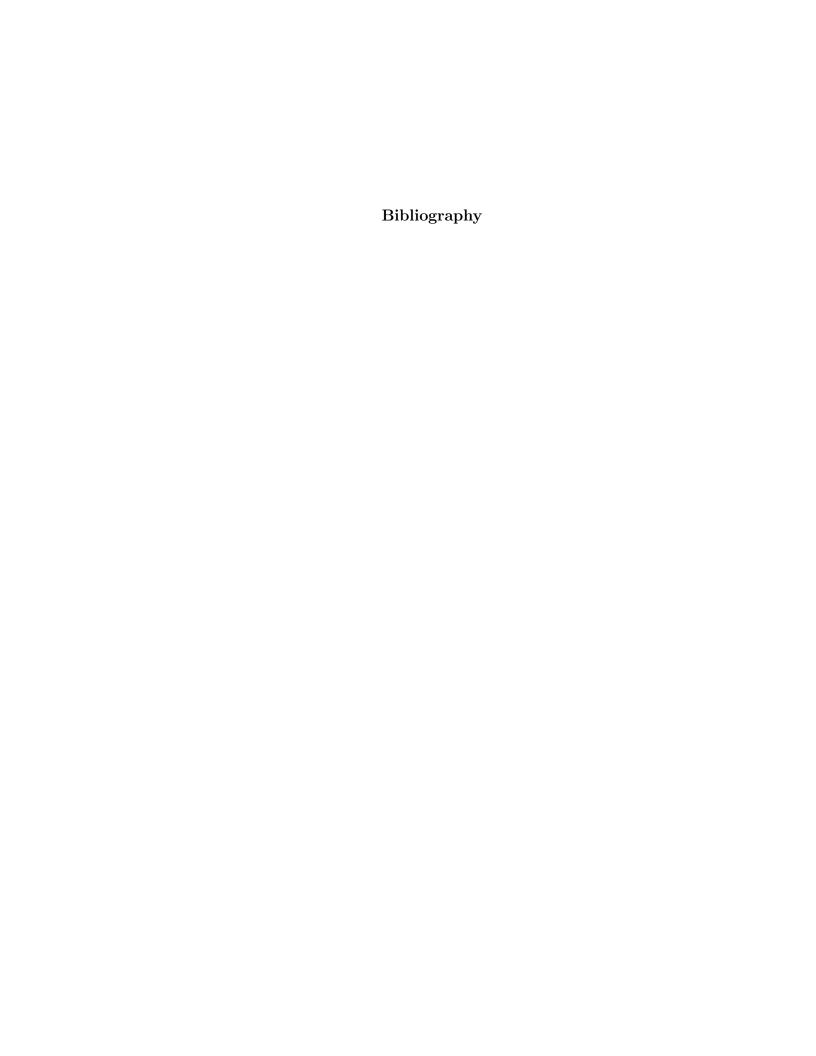
Figure

## Chapter 1

Introduction

## Chapter 2

Topic



## Appendix A

Architecture Binary Interface