(i) Printed Pages: 2 Roll No. mannament (ii) Questions: 9 Sub. Code: 0 9 2 0 Exam. Code: 0 0 2 8  Exam. Code: 0 0 2 8  Exam. Code: 0 0 2 8				
COMPUTER ORGANIZATION				
Paper-BCA-16-202				
Time Allowed: Three Hours] [Maximum Marks: 65				
Note:— (1) Attempt one question from each Unit and the compulsory question (Unit-V).				
(2) All questions carry equal marks divided in UNIT—I				
1. (a) Give the design and logic diagram of converting JK flip flop				
(b) Explain half adder and full adder with their logic diagram.  5				
2. (a) Discuss addition and subtraction for sign magnitude numbers.				
(b) Explain Booth's algorithm with suitable example.  UNIT—II				
<ul> <li>(a) Define Interrupt. What is an interrupt cycle? Also explain priority interrupt.</li> <li>(b) What are micro-operations? Design 4-bit arithmetic circuit.</li> </ul>				
(b) What are micro-operations? Design 6  4. Give the instruction set of 8086/8088 microprocessor. 13				

## UNIT-III

5.	(a)		rcuit	
		diagram.		
	(b)		nput/	
		output.		
6.	(a)			
		differences between machine and assembly language.	5	
	(b)	Compare one pass and two pass assembler.	8	
		UNIT—IV		
7.	Dis	cuss the various physical components of a computer ar	nd its	
		pection and diagnostics on PC.	13	
8.	Define the term "Computer Virus". Explain its various types and its			
		ventive and protective measures.	13	
		UNIT—V		
9.	(a)	What is hit ratio?	1	
	(b)	Discuss the role of Execution Unit (EU).	2	
	(c)	Define the term "parity".	2	
	(d)	What is DMA?	2	
	(e)	What is Cache memory?	2	
	(f)	Define RTL.	2	
	(g)	What is an Expansion Card?	2	