(i)	Pr	inted Pages : 2	ges : 2 Roll No					
(ii)	O	estions : 9	Sub. Co	de:	0	9	2	0
(-)			Exam. Co					8
		Bachelor of Comp	outer Application	ns 2 <sup>nd</sup> S	eme	ster		
			(2042)					
		COMPUT	ER ORGANIZA	TION				
		Pap	er—BCA-16-202		-			
Tin	ne All	owed : Three Hou	urs] [	Maxin	num	Mai	rks:	65
Not	te :-	- (1) Attempt O	ne question from	each U	nit.			
		(2) Question N	lo. 9 is compulsor	ry.				
		(3) All question	ns carry equal ma	rks unl	ess s	pecif	ied.	
			UNIT—I					
1.	72.	Give the design and logic diagram of converting JK flip flop to D flip flop.						
2.	Des	ign a 5*32 decoder	with four 3*8 dec	oders a	nd a	2*40	leco	2.2
								13
			UNIT—II					
3.	(a)	<ul><li>What are addressing modes? Discuss various addressing modes.</li></ul>						
	(b)	What is RTL? Di	scuss different log	gic mic	ro-o	perat	ions	. 5
4.	(a)	Write a note on va	arious instruction	codes.				·6
	(b)	Explain instruction cycle in detail.					7	

## UNIT-III

5.	(a)	What is virtual memory? Discuss its working.	5		
	(b)	Elaborate various mapping techniques of Cache memo	ory.		
			8		
6.	(a)	Compare one pass and two pass assembler.	5		
	(b)	Write assembly language program to perform basic i	nput		
á,		output.	8		
		UNIT—IV			
7.	Explain various physical components of a computer along with				
	inspection and diagnostics on PC.				
8.	Describe the working of various internal and external cards of P				
			13		
		(Compulsory)			
9.	(a)	Discuss fixed point and floating point representation.	3		
	(b)	What is the function of Bus Interface Unit?	3		
	(c)	What is cycle stealing in DMA?	3		
	(d)	Discuss various types of RAM and ROM.	2		
	(e)	What is an Expansion card?	2		