Microprocess Lab – Experiment 8

Addition


```
0000|  CLR C
0001|  MOV A, #06
0003|  ADD A, #02
0005|  MOV R2, A
0006|  SJMP 6
```

Time: 130us – Instructions: 129

Division


```
0000|  CLR C
0001|  MOV A, #04
0003|  MOV B, #02
0006|  DIV AB
0007|  MOV R3, A
0008|  SJMP 6
```
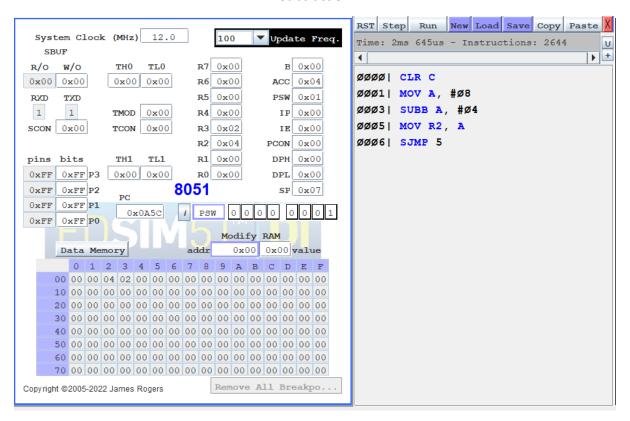
Time: 113us – Instructions: 108

Subtraction

System Clock (MHz) 12.0 — 100 — Update Freq.

```
RST Step Run New Load Save Copy Paste X
Time: 2ms 645us - Instructions: 2644

0000|  CLR C
0001|  MOV A, #08
0003|  SUBB A, #04
0005|  MOV R2, A
0006|  SJMP 5
```

Multiplication

System Clock (MHz) 12.0 — 100 — Update Freq.

```
RST Step Run New Load Save Copy Paste X
Time: 1ms 5us - Instructions: 1000

0000|  CLR C
0001|  MOV A, #04
0003|  MOV B, #02
0006|  MUL AB
0007|  MOV R2, A
0008|  SJMP 8
```