

SRM Institute of Science and Technology College of Engineering and Technology

Mode of Exam **OFFLINE**

SET B

DEPARTMENT OF ECE

SRM Nagar, Kattankulathur – 603203, Chengalpattu District, Tamilnadu

Academic Year: 2021-2022 (EVEN)

Test: CLAT- 2
Course Code & Title: 18ECC201J – Analog Electronic Circuits
Pear & Sem: II / IV
Date: 24-05-2022
Duration: 2 periods
Max. Marks: 50

Course Articulation Matrix:

	18ECC201J - Analog Electronic Circuits	Program Outcomes (POs)														
			Graduate Attributes					PSO								
COs	Course Outcomes (COs)	1	2	3	4	5	6	7	8	9	10	11	12	1	2	3
CO-1 :	Analyze bipolar amplifier circuits and their frequency response.	1	2	3	- 1	- 1	- 1	- 1	1	1	1	- 1	1	- 1	-	-
CO-2 :	Develop MOSFET amplifier circuits and their frequency response.	1	2	3	-	-	- 1	-	-	1	1	- 1	1	- 1	-	-
CO-3 :	Compile various negative feedback amplifier and oscillator circuits.	1	-	3	-	-	-	-	-	-	-	-	-	1	-	-
CO-4 :	Demonstrate the different classes of power amplifiers according to their performance characteristics.	1	2	3	-	- 1	- 1	- 1	1	1	-	- 1	1	1	-	-
CO-5	Construct the basic circuit building blocks that are used in the design of IC amplifiers, namely current mirrors and sources.	1	2	3	-	-	- 1	-	1	-	-	1	1	1	-	-
	Organize analog electronic circuits using discrete components to measure various analog circuits' performance.	-	-	3	-	-	-	-	-	2	-	-	-	3	1	-

	Part - A						
	$(10 \times 1 = 10 \text{ Marks})$						
Instructions: Answer ALL the Questions							
Q. No	Question	Marks	BL	CO	PO		
1.	at negative peak	1	1	2	1		
2.	It blocks the noise & for ac signal it acts a short circuit resulting in grounding source terminal.	1	2	2	1		
3.	common gate	1	2	2	1		
4.	45MHz	1	2	2	2		
5.	mid-frequency	1	2	2	1		
6.	Shunt-Series	1	2	3	2		
7.	Series-Series	1	2	3	1		
8.	Shunt-Shunt	1	2	3	1		
9.	one frequency	1	2	3	1		
10	capacitors	1	2	3	1		
	Part – B						
	$(4 \times 10 = 40 \text{ Marks})$						
	SECTION B1		•	•	•		
	Instructions: Answer ANY 2 Questi	ons					

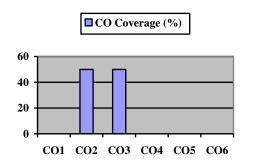
11	Vosa = 12 x 450 x 10 = 8.70 Y - (mark) Too = kn (Vesa - VTN)2 - (1mark)	10	3	2	2
	= 4x103 (8.7-2.5)				
	= 153 ma				
	9m = 2kn (Visa - YTN)				
	= 2×4 (8.70-2.5)				
	= 49.6 mn/v -(2mark)				
	910 = (2 Feq) = ((e.01) (153))				
	= 0.65 kil = 650 r _ (Imark)				
	Av = 9m (Rs11973) KillR2				
	1+gm(RS11970) RS+(R,11R7)				
	RillR2 = 123K				
	$A_{V} = \frac{49.6(575.22)}{1+49.6(575.22)} \frac{123\times10^{3}}{5k+123\times10^{3}}$				
	= 0.96 _(5mark)				
12		10	3	2	2
	RSi Ca, R, E FROVO PV: RS E Equivalent would	10	J	2	-
	RSIMUL STATISTO TO THE ROLL OF				
	Av = No/N;				
	$V_0 = -g_m V_{gs} \times (g_{10} R_0)$ $V_{gs} = V_1 \times \frac{R_1 R_2}{R_{Si} + (R_1 R_2)}$ (5 mart)				
	Subs vgs in Vo Vo = -gm (970 RD) Vix Rill Rz Psi+(Rill Rz)				
	AV = Vo = -9m (STONRD) (R,11R2) RSi+(R,11R2)				

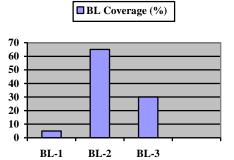
	Enjert resistance (Ri) Ri = Ri II R2 Low frequency input resistance looking into the gate of Mosfet is & (mark) output resistance (Ro) Ro is found by setting Vi =0 when Vi =0 => Vgs=0 gardys =0 ! Ro = RD 1170] (2 mark)				
13	RD & CZ VO SRL VI	5	2	2	2

b. Ri = 80 kmll 30km = 21.8(km (2 morale) Ro = Ro 1170 90 = (\lambda I \lambda g] IDA = kn (Vhs - VTN) = 0.5 (2.72-1.5) = 0.613 mA 90 = (\lambda IDA) = 163km (3 mark)	5	3	2	3
Ro = RD 11970 = 5.78 k 2 SECTION B2				
Instructions: Answer ANY 2 Questi	ons 8	3	3	2
Current Series feedback amplified Sold To the series feedback amplified Sold To the series feedback amplified Lind To the series feedback amplified Agt = To N; To = Ag VE = VE = To Ag Vi = VE + VI b = Ro + By To = (I+ Ag 124) Io Agt = 30 + By To = (I+ Ag 124) Io Agt = 10 N; Right = Vi				

	T			1	1
	b. 1. The total phase shift around the closed loop should be 0° (00) 360° (1 mane) [AB = 360/0° 2. The product of open loop gain of the amplifier and the feedback notwook should be mady to AB =1 (1 masse)	2	2	3	2
15	Series - Shoul topology is Series - Shoul topology is Signal is Vbe, feed back signal is equal to outpet Voltage ie βν = 1 Transfer gain (Δνγ) Aνγ = Vo Vi = Vπ + (νπ + gπ νπ) Rε - νπ (1+ γπ νπ) Rε	8	3	3	3

b. b) Hartley oscillator - uses tapped inductance colpits oscillator - uses tapped capacitance (i) In Hartley oscillator, the feedback for the cutive device is laken from a voltage diviser made of two inductors in series awas the capacitor whereas in colpits oscillator, the voltage divider is made of two apartors in series across the inductor	2	2	3	1
16 a.	4	3	3	3
b. (b) we're bridge oscillator (c) (c) (c) (d) (d) (d) (e) (d) (e) (e) (e	6	2	3	2





Evaluation Sheet

Name of the Student:

Register No.:

Part- A (10 x 1= 10 Marks)							
Q. No	CO	PO	Maximum	Marks	Total		
			Marks	Obtained			
1	CO2	1	1				
2	CO2	1	1				
3	CO2	1	1				
4	CO2	2	1				
5	CO2	1	1				
6	CO3	2	1				
7	CO3	1	1				
8	CO3	1	1				
9	CO3	1	1				
10	CO3	1	1				
		Part- B (4 x 10= 40 M	arks)			
11	CO2	3	10				
12	CO2	2	10				
13.a	CO2	2	5				
13.b	CO2	3	5				
14.a	CO3	2	8				
14.b	CO3	2	2				
15.a	CO3	3	8				
15.b	CO3	1	2				
16.a	CO3	3	4				
16.b	CO3	2	6				

Consolidated Marks:

CO	Maximum	Marks
	Marks	Obtained
2		
3		
Total		

PO	Maximum Marks	Marks Obtained
1		
2		
3		
Total		

Signature of Course Teacher