ORDER OF THE EXPERIMENTS TO BE FOLLOWED

EXP NO	NAME OF THE EXPERIMENT	Hardware/ Software
1	Design and Analysis of BJT Common Emitter Amplifier Configuration	Hardware
3	Design and Analysis of Multistage Amplifier Configurations	Hardware
2	Pspice Simulation Of MOSFET Amplifier Configurations	Software
7	Design and Analysis of Current Series Feedback Amplifier	Hardware
4	RC Phase Shift Oscillator	Hardware
8	Design and Analysis Of LC Oscillator	Hardware
5	Pspice Simulation of Class C Power Amplifier	Software
9	Pspice Simulation of Two Transistor Current Source Using BJT & FET	Software
6	Design and Analysis of Basic BJT Differential Pairs	Hardware
10	Pspice Simulation of Common Emitter Amplifier With Active Load	Software
11	Pspice Simulation of MOSFET Common Source Amplifier With Active Load	Software
12	Simulation Experiments Using Pspice- Differential Amplifier With Active Load	Software