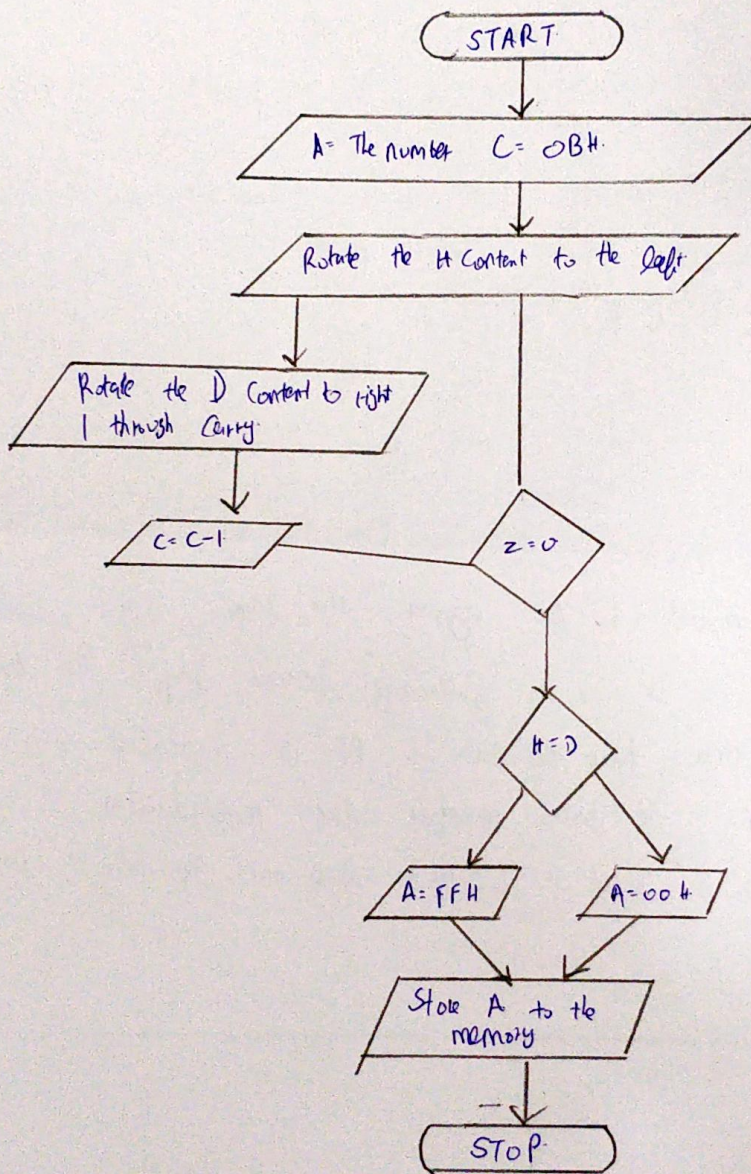


I Create:



Answer: 5

- i) Test [BX][DI], CX \Rightarrow Based index mode.
The effective address is the name of base register and index register.
- ii) JMP 1000H: 4000H \Rightarrow Intersegment direct mode.
(The addressing mode is generated by CS, IP and provide branching from one segment to another.)
- iii) AND, AX, 000FH \Rightarrow Immediate mode. (as immediate data is provided)
- iv) OUT 03H, AL \Rightarrow Direct I/O addressing mode.
- v) MUL BX \Rightarrow Register Mode. (Default operand of MUL = AX).

ANALYSIS

Consider sample data 1234H.

So 1234H will be moved to AX register, the same 1234H will be copied (or) transferred into BX in to following second step. So, AX and BX both contain 1234H. Now the data in BP is multiplied with default AX's data or in AX the entire number after multiplication. Now the data in AX is stored in [1200H] address and the data in BX is stored in [1202H].