18EES101J - CT-3 (02/08/2021; 09:00 am - 10:30 am)

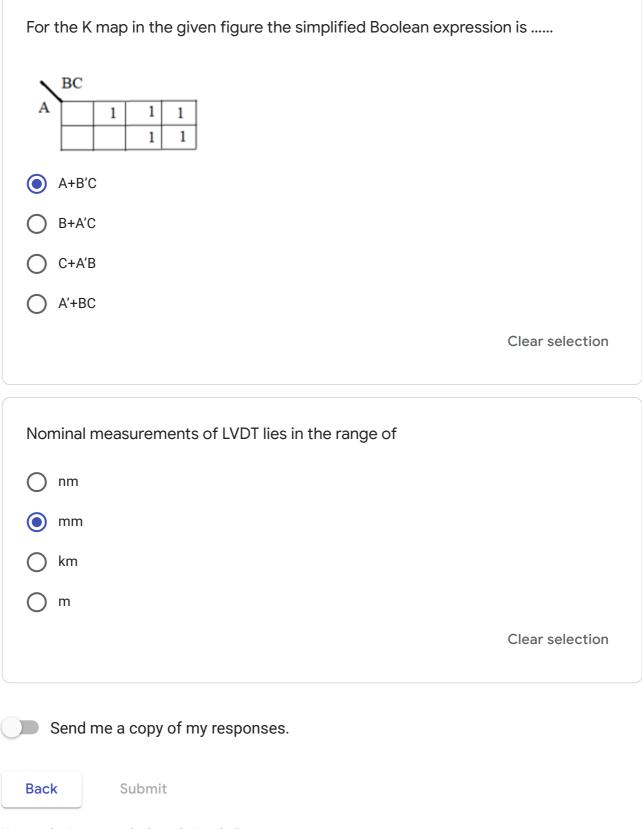
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| Part-B | |
|---|-----------------|
| 10 X 2 = 20 MARKS | |
| The binary code of (21.125) 10 | |
| 10101.001 | |
| 0 10100.001 | |
| 0 10101.010 | |
| 0 10100.111 | |
| | Clear selection |
| | |
| The representation of octal number (532.2)8 in decimal is | |
| | |
| (346.25)10 | |
| (346.25)10(532.864)10 | |
| | |
| (532.864)10 | |
| (532.864)10 (340.67)10 | Clear selection |

| If VP is denoted as the primary voltage, Vs1 and Vs2 are denoted by the secondary voltages of LVDT, then which of the following holds true | |
|--|--------|
| VP, Vs1 and Vs2 are average values | |
| VP, Vs1 and Vs2 are peak values | |
| VP, Vs1 and Vs2 are constant values | |
| VP, Vs1 and Vs2 are RMS values | |
| Clear sele | ection |
| For a given transducer, closeness of a measured value to true value is defin | ed as |
| Precision | |
| efficiency | |
| accuracy | |
| reliability | |
| Clear sele | ection |
| Derive the Boolean expression for the logic circuit shown below | |
| | |
| ○ C.(A+B).D.E | |
| C.(A+B). D'. E | |
| A.B+C+D+E | |
| A.B.C.D.E | |
| | |

| A thermistor has | | |
|--|-----------------|--|
| opositive temperature coefficient | | |
| negative temperature coefficient | | |
| zero temperature coefficient | | |
| variable temperature coefficient | | |
| | Clear selection | |
| | | |
| Which of the following is not a requirement of a transducer | | |
| Ruggedness and no residual 8 | | |
| C Linearity and repeatability | | |
| Very low signal to noise ratio and presence of hysteresis | | |
| Robustness and reliability | | |
| | Clear selection | |
| | | |
| For the minterm designation Y = \sum m (1, 3, 5, 7) the complete expression is | | |
| Y= A'B'C+AB'C | | |
| Y=A'B'C+A'BC+ AB'C+ABC | | |
| Y=A'B'C'+A'B'C+A'BC+AB'C | | |
| Y=A'B'C'+ABC+A'B'C+AB'C | | |
| | Clear selection | |
| | | |



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