Microprocessor, Microcontroller and Interface Techniques Lab
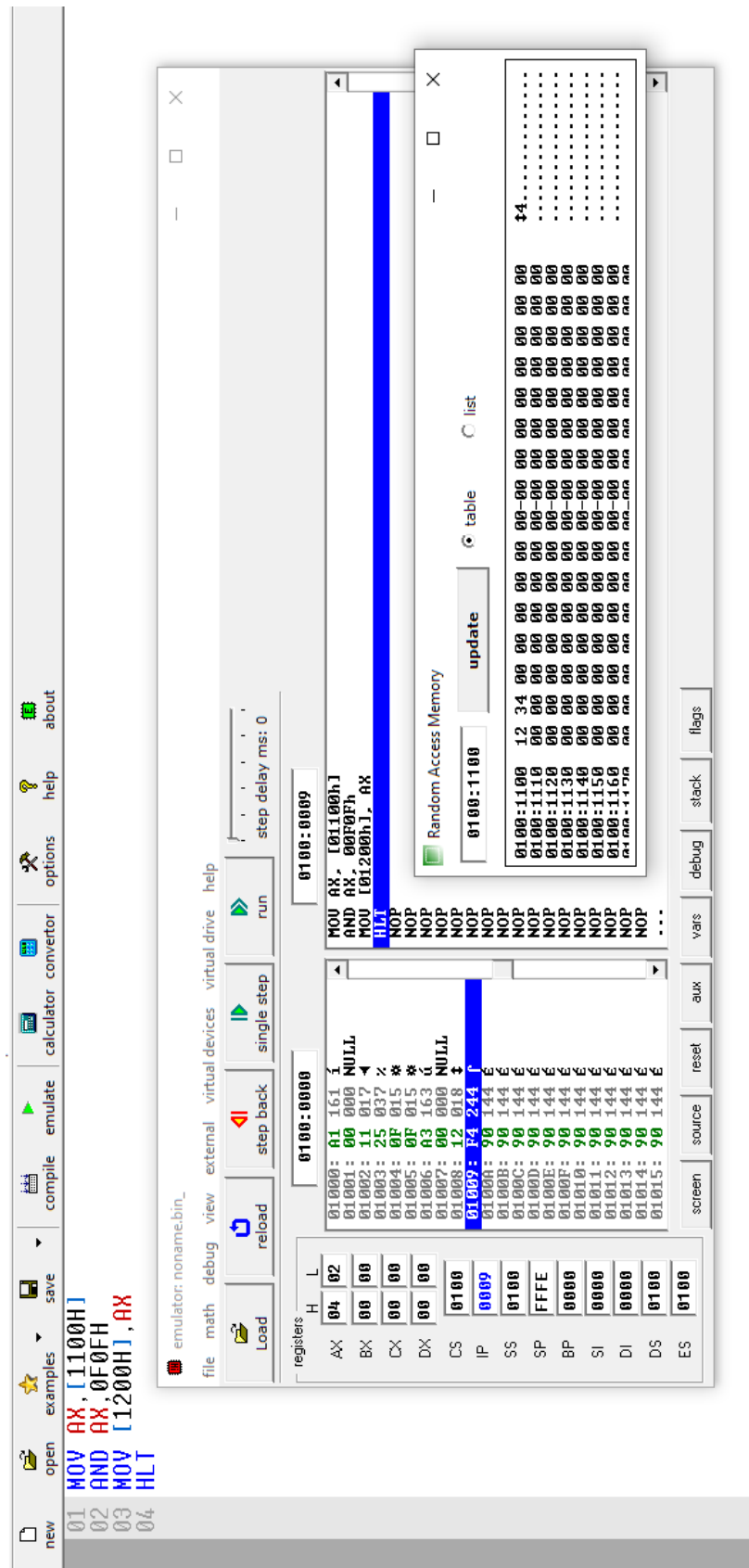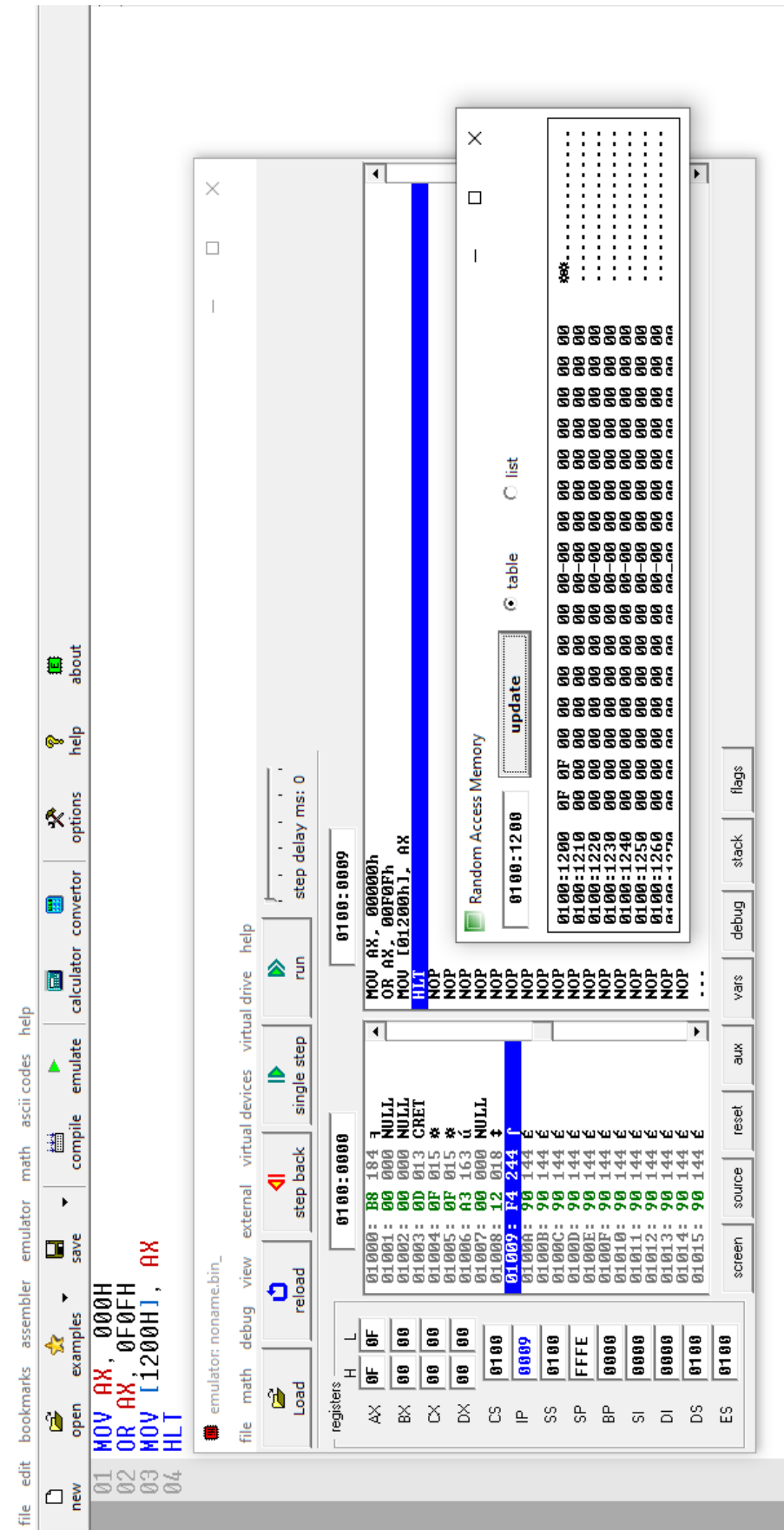
AND Operation Output
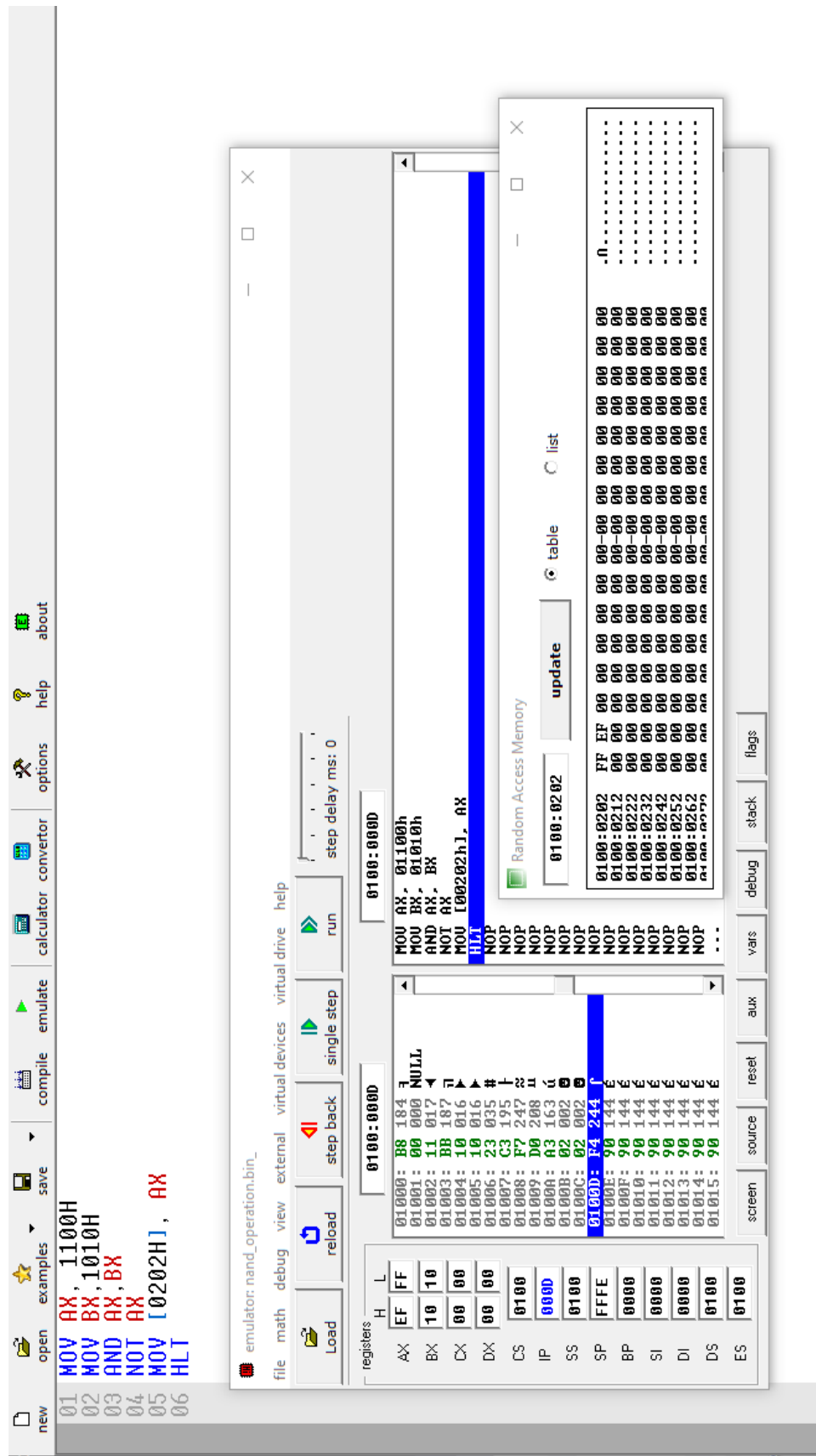
OR Operation Output

NAND Operation

```
01  MOV AX, 1100H
02  MOV BX, 1010H
03  AND AX, BX
04  NOT AX
05  MOV [0202H], AX
06  HLT
```

emulator: nand_operation.bin_

file math debug view external virtual devices virtual drive help

Load   reload   step back   single step   run   step delay ms: 0

registers   H L
AX EF FF
BX 10 10
CX 00 00
DX 00 00
CS 0100
IP 000D
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100

0100:000D

01000: B8 184 ╕
01001: 00 000 NULL
01002: 11 017 ▼
01003: BB 187 ╖
01004: 10 016 ▲
01005: 10 016 ▲
01006: 23 035 #
01007: C3 195 ┴
01008: F7 247 ≈
01009: D0 208 ╨
0100A: A3 163 ú
0100B: 02 002 ☻
0100C: 02 002 ☻
0100D: F4 244 ⌠
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É

0100:000D

MOV AX, 01100h
MOV BX, 01010h
AND AX, BX
NOT AX
MOV [00202h], AX
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP

screen source reset aux vars debug stack flags

Random Access Memory

0100:0202   update   ○ table   ○ list

0100:0202 FF EF 00 00 00 00 00 00 00
0100:0212 00 00 00 00 00 00 00 00 00
0100:0222 00 00 00 00 00 00 00 00 00
0100:0232 00 00 00 00 00 00 00 00 00
0100:0242 00 00 00 00 00 00 00 00 00
0100:0252 00 00 00 00 00 00 00 00 00
0100:0262 00 00 00 00 00 00 00 00 00

NOR Operation

```
new   open  examples  save  |  compile  emulate  |  calculator  convertor  |  options  help  about

01  MOV AX,1100H
02  MOV BX,1010H
03  OR AX,BX
04  NOT AX
05  MOV [0202H], AX
06  HLT
```