

Test: CLAT- 1

Course Code & Title: 18ECC201J – Analog Electronic Circuits

Year & Sem: II / IV

Date: 07-04-2022

Duration: 60 minutes

Max. Marks: 25

Course Articulation Matrix:

18ECC201J - Analog Electronic Circuits		Program Outcomes (POs)																
		Graduate Attributes												PSO				
COs	Course Outcomes (COs)	1	2	3	4	5	6	7	8	9	10	11	12	1	2	3		
CO-1	Analyze bipolar amplifier circuits and their frequency response.	1	2	3	-	-	-	-	-	-	-	-	-	-	-	-		
CO-2	Develop MOSFET amplifier circuits and their frequency response.	1	2	3	-	-	-	-	-	-	-	-	-	-	-	-		
CO-3	Compile various negative feedback amplifier and oscillator circuits.	1	-	3	-	-	-	-	-	-	-	-	-	-	-	-		
CO-4	Demonstrate the different classes of power amplifiers according to their performance characteristics.	1	2	3	-	-	-	-	-	-	-	-	-	-	-	-		
CO-5	Construct the basic circuit building blocks that are used in the design of IC amplifiers, namely current mirrors and sources.	1	2	3	-	-	-	-	-	-	-	-	-	-	-	-		
CO-6	Organize analog electronic circuits using discrete components to measure various analog circuits' performance.	-	-	3	-	-	-	-	-	2	-	-	-	3	1	-		

Part - A

(5 x 1 = 5 Marks)

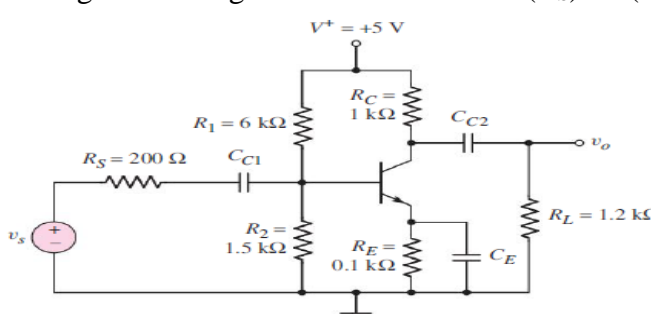
Instructions: Answer any 5

Q. No	Question	Marks	BL	CO	PO	PI Code
1	b. that I_C flows into transistor while I_E flows out it	1	1	1	1	
2	d. 0.002 mA	1	3	1	2	
3	d. Cascode	1	1	1	1	
4	a. Saturation point and Cutoff point	1	2	1	1	
5	c. Stray	1	3	1	2	

Part - B

(2 x 10 = 20 Marks)

Instructions: Answer any TWO

6.	For the circuit given below with transistor parameters $I_{CEQ} = 2.79$ mA, $\beta = 180$ and $r_0 = \infty$, a. Determine the Q point values. (4) b. Find the small signal parameters and voltage gain including the source resistance (R_S). (6)	5	3	1	3	
		5	2	1	2	

b. Q point values (I_{Ceq}, V_{Ceq})

Given $I_{Ceq} = 2.79 \text{ mA}$, $\beta = 180$ & $r_o = \infty$

① $V_{Ceq} = V_{CC} - I_C (R_C + R_E)$
 $= 5 - 3.0646 = 1.934 \text{ V}$
 $\therefore Q \text{ point} = (2.79 \text{ mA}, 1.934 \text{ V})$

② small signal parameters.

$g_m = \frac{I_{Ceq}}{V_T} = \frac{2.79}{0.026} = 107.3 \text{ mA/V}$

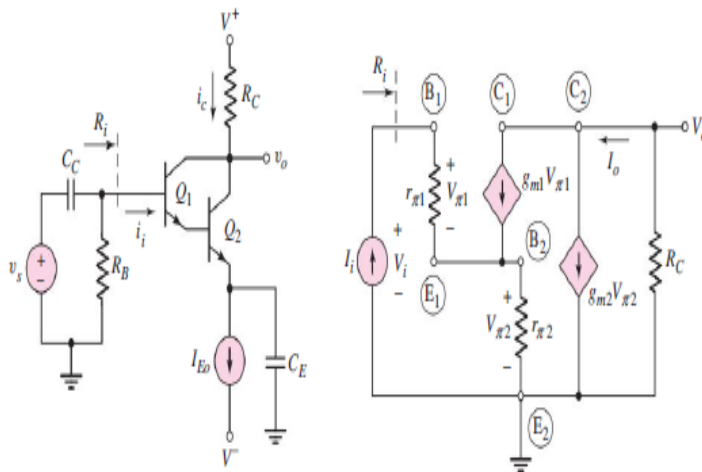
$r_{\pi} = \frac{\beta V_T}{I_{Ceq}} = \frac{180 \times 0.026}{2.79} = 1.67 \text{ k}\Omega$

$R_i = R_1 \parallel R_2 \parallel R_{ib}$
 $\hookrightarrow R_{ib} = r_{\pi} + (\beta + 1) R_E$
 $= 1.67 + 18.1 = 19.77 \text{ k}\Omega$
 $\Rightarrow \frac{1.2 \times 19.77}{1.2 + 19.77} = 1.131 \text{ k}\Omega$

$\Rightarrow A_v = -\frac{\beta R_C}{r_{\pi} + (\beta + 1) R_E} \cdot \frac{R_i}{R_i + R_i} = -7.89$

$\therefore A_v = -7.89$

7.a Draw the Darlington amplifier and derive the expression for the current gain and input resistance.



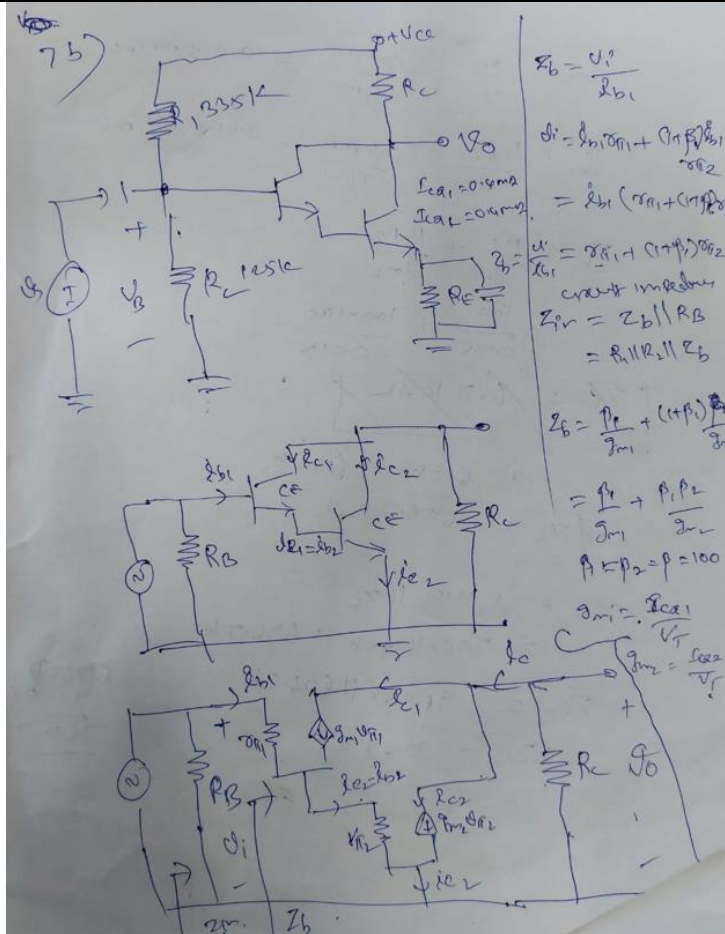
5

2

1

3

	<p style="text-align: center;">INPUT RESISTANCE</p> $R_i = \frac{V_i}{I_i}$ $V_i = V_{\pi_1} + V_{\pi_2}$ $V_{\pi_1} = I_i r_{\pi_1}$ $V_{\pi_2} = (I_i + g_m V_{\pi_1}) r_{\pi_2}$ $= (I_i + g_m I_i r_{\pi_1}) r_{\pi_2}$ $= I_i (1 + \beta) r_{\pi_2}$ $\therefore V_{\pi_2} = I_i (1 + \beta) r_{\pi_2}$ <p>Sub V_{π_1} & V_{π_2} in V_i</p> $\therefore V_i = I_i r_{\pi_1} + I_i (1 + \beta) r_{\pi_2}$ $= I_i (r_{\pi_1} + (1 + \beta) r_{\pi_2})$ $R_i = \frac{V_i}{I_i} = r_{\pi_1} + (1 + \beta) r_{\pi_2}$ $r_{\pi_1} = \frac{\beta_1}{g_{m1}} = \frac{\beta_1 V_T}{I_{CQ1}} \quad \text{Let } I_{CQ1} = \frac{I_{CQ2}}{\beta_2}$ $r_T = \frac{\beta_1 V_T}{I_{CQ2}} \cdot \beta_2$ $r_T = \beta_1 r_{\pi_2}$ $R_i = \beta_1 r_{\pi_2} + (1 + \beta_1) r_{\pi_2}$ $R_i \approx 2\beta_1 r_{\pi_2}$	5	3	1	3	
7.b.	Determine the value of input resistance for the given circuit. Assume $\beta = 100$; $I_{C1} = I_{C2} = 4 \text{ mA}$.					



$$g_{m1} = \frac{I_{CQ1}}{V_T} = \frac{0.4mA}{26mV} = 0.015mA/mV$$

$$g_{m2} = \frac{I_{CQ2}}{V_T} = \frac{0.4mA}{26mV} = 0.015mA/mV$$

$$Z_b = \frac{\beta_1}{g_{m1}} + \frac{\beta_1 \beta_2}{g_{m2}}$$

$$= \frac{100}{0.015} + \frac{100 \times 100}{0.015}$$

$$\boxed{Z_b = 6.7k\Omega}$$

$$= 6.7k + 670k$$

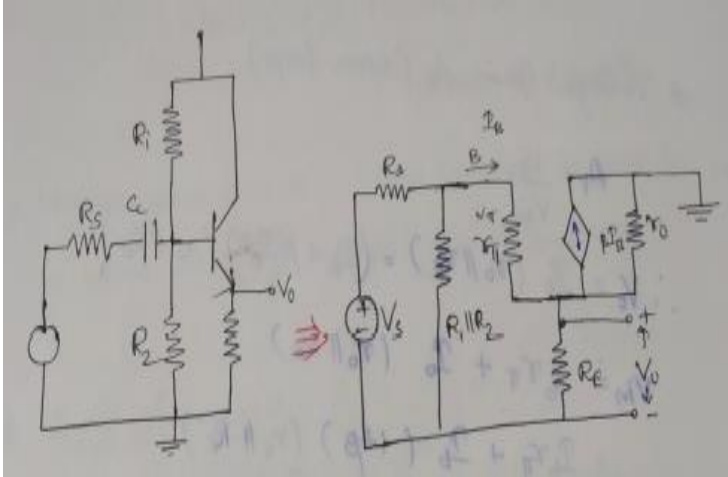
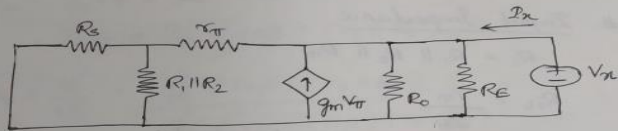
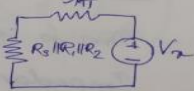
$$\boxed{Z_b = 676.7k\Omega}$$

$$Z_{in} = R_1 \parallel R_2 \parallel Z_b$$

$$= 335k \parallel 125k \parallel 676.7k$$

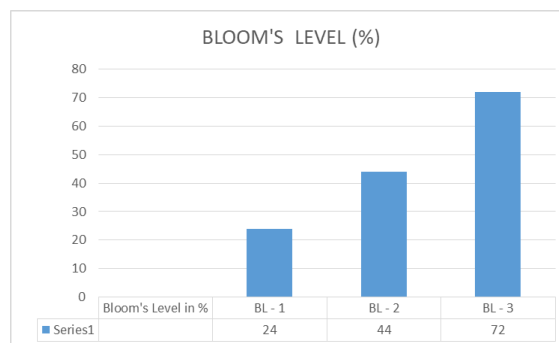
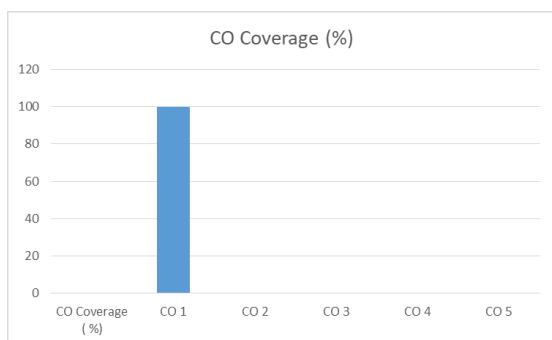
$$Z_{in} = 91.03k \parallel 676.7k$$

$$\boxed{Z_{in} = 80.23k}$$

8.a.	<p>Why common collector configuration is otherwise called as Emitter Follower?</p> <p>Common-collector transistor amplifiers are so-called because the input and output voltage points share the collector lead of the transistor in common with each other, not considering any power supplies. The common-collector amplifier is also known as an emitter-follower. (2)</p>	2	2	1	2
8.b.	<p>Derive the output resistance for a common collector configuration with necessary diagram</p>  <p># Output Impedance (R_o)</p> <p>The independent voltage source is set to zero ($V_s = 0$). A test voltage V_x is applied to the o/p terminals and the resulting test current is I_x.</p>  $\beta I_b = g_m V_{\pi}$ $R_o = \frac{V_x}{I_x}$ <p>Applying KCL at o/p</p> $I_x + g_m V_{\pi} = \frac{V_x}{R_E} + \frac{V_x}{r_o} + \frac{V_x}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)} \quad \text{--- (1)}$ $V_{\pi} = \frac{-V_x \times r_{\pi}}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)}$  $I_x = \frac{V_x g_m r_{\pi}}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)} + \frac{V_x}{R_E} + \frac{V_x}{r_o} + \frac{V_x}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)}$	8	3	1	3

	$I_n = V_n \left[\frac{1}{R_E} + \frac{1}{r_o} + \frac{(1 + g_m r_{\pi})}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)} \right]$ $I_n = V_n \left[\frac{1}{R_E} + \frac{1}{r_o} + \frac{1 + \beta}{r_{\pi} + (R_s \parallel R_1 \parallel R_2)} \right]$ $R_o = \frac{V_n}{I_n} = R_E \parallel r_o \parallel \frac{r_{\pi} + (R_s \parallel R_1 \parallel R_2)}{1 + \beta}$ <p># Emitter follower is sometimes referred to as an impedance transformer, since the i/p impedance is large and o/p impedance is small</p> <p># This makes it an ideal voltage source, so the o/p is not loaded when used to drive another load</p> <p># This is used as o/p stage in multistage amplifier</p> <p>(8)</p>					
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Course Outcome (CO) and Bloom's level (BL) Coverage in Questions



Approved by the Course Coordinator