# **Faculty Profile**

#### Dr. Manoranjitham R

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Department of Computer Science and Engineering
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# Academic Background

Degree	University	Year
Ph.D	Anna University	2020
M.E	Pune University	2014
B.E	Anna University	2011

### **Courses Taught**

- Software Engineering
- Number Theory and Cryptography
- Object Oriented Programming Concepts
- Signals and Systems
- Data Compression

### Research Interests

- Image Processing
- Computer Vision
- Al in Software Engineering
- Artificial Intelligence

#### Most recent Publications

 Manoranjitham R, Deepa P, "Efficient invariant interest point detector using Bilateral-Harris Corner Detector for object recognition application", Multimedia Tools and Application, Springer, June 2017, vol.77, pp. 9365-9378. DOI: <a href="https://doi.org/10.1007/s11042-017-4982-5">https://doi.org/10.1007/s11042-017-4982-5</a> [SCI indexed, IF: 2.57]

- Manoranjitham R, Deepa P, "A Robust Weighted Guided Harris Corner Feature Detector for Invariant Object Recognition Application", International Journal of Pattern Recognition and Artificial Intelligence (IJPRAI) (Submitted)
- Manoranjitham R, Deepa P, "Novel Interest Point Detector using Bilateral-Harris Corner method", International Conference on Advanced Computing and Communication Systems – ICACCS 2017 held at Sri Eshwar College of Engineering, Coimbatore, Tamil Nadu, India from 6th to 7th January 2017.
- Manoranjitham R, Deepa P, "Robust invariant feature detector algorithm with Trilateral and Harris corner point detector", First International Conference on Advances in Electrical and Computer Technologies 2019(ICAECT - 2019), Hotel Aloft, Coimbatore, Tamilnadu, India, during 26-27, April 2019.
- Manoranjitham R and B N Jagdale, presented the paper on "Designing an automation framework to improve the performance of SIP" in Cyber Times International Journal of Technology and Management (CTIJTM 2014), April 2014. ISSN No.: 2278-7518, vol.7, pp.1
- Manoranjitham R, B N Jagdale, "Designing an automation framework to improve the performance of SIP based test suites in PBX feature testing", in 1st IEEE Global Conference on Wireless Computing and Networking (GCWCN 2014), Sinhgad Institute of Technology, Lonavala, India, December 22-24, 2014.

## **Projects Guided**

- Predicting Bug Fix Time Using Recurrent Networks
- Prediction of Number of Software Faults Based on the Dynamic Selection of Learning Techniques
- Similarity Calculation of Executables for Software Plagiarism Detection
- Cartooning an Image Using Bilateral Filter

# Memberships in Professional societies

NA

#### Significant achievements:

- Gate Qualified in 2013
- Awarded for demonstrating competence in the completion of Fundamentals of Deep Learning For Multiple Data Types from NVIDIA Deep Learning Institute
- Contributed Reviewer of papers for 8th International Conference on Frontiers of Intelligent Computing: Theory and Applications (FICTA 2020) January 4 - 5, 2020 organized by National Institute of Technology Karnataka, India

•	Contributed Reviewer of papers for IBSSC-2021 organized by IEEE Bombay section and ABV-Gwalior, India. (18-20 November 2021)		