Cody Bullock

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EDUCATION

B.S. Computer Engineering

Virginia Tech, Blacksburg, VA

Expected Graduation: December 2025

GPA: 3.59

Study Abroad: Engineering Practices in South Korea and Japan

Relevant Courses: Embedded Systems, AC Circuit Analysis, Digital Design I, Physical Electronics, Integrated Design Project, Signals and Systems, Data Structures and Algorithms

Awards: Dean's List (2022-2024)

EXPERIENCE

Hardware/Software Designer - VT MICS Lab | Blacksburg, VA

09/2024-present

- Working with the VT Agriculture Department to develop a wireless sensor for a cow's rumen
- Designing PCB schematic for data acquisition and long-range transmitting device
- Give weekly presentations on updates and progress

IT / Engineering Team - WUVT Radio Station | Blacksburg, VA

01/2023-present

- Brainstormed ideas for improving music logging and storage systems
- Responsible for general upkeep of station's servers, sound systems, website, and all backend processes

VASTS - NASA Virginia Aerospace And Technology Scholars

2021-2022

- 17 week long online asynchronous program consisting of a math assignment, reading quiz, and report every two weeks, all relating to space travel and NASA mission design
- Final report was a structured and detailed layout of a mission to Mars planned out 50 years into the future

PROJECTS

Home Audio System

- Designed and built a home audio system capable of altering the gain of certain frequency bands
 of an audio signal using potentiometers. This system was created using a Graphic Equalizer
 circuit and a Class-D Amplifier
- LTspice was used for preliminary design and simulations and tested using an oscilloscope, wave generator, and network analyzer

LEADERSHIP

Mozaiko Living Learning Community Student Mentor

08/2023-05/2024

- Monitor student academic progress, communicating with each student frequently to assist students experiencing difficulties.
- Offer stability and positive reinforcement in students' lives to build strong rapport and trust with other students

SKILLS

Programming Languages (C, C++), Verilog, MATLAB, Intel Quartus (FPGA), ModelSim (RTL), Google and Microsoft Suites, Laboratory Equipment, LTspice