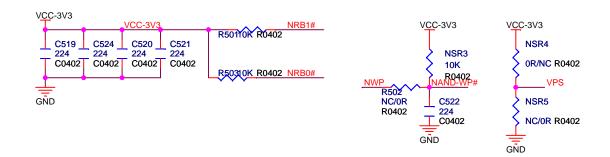


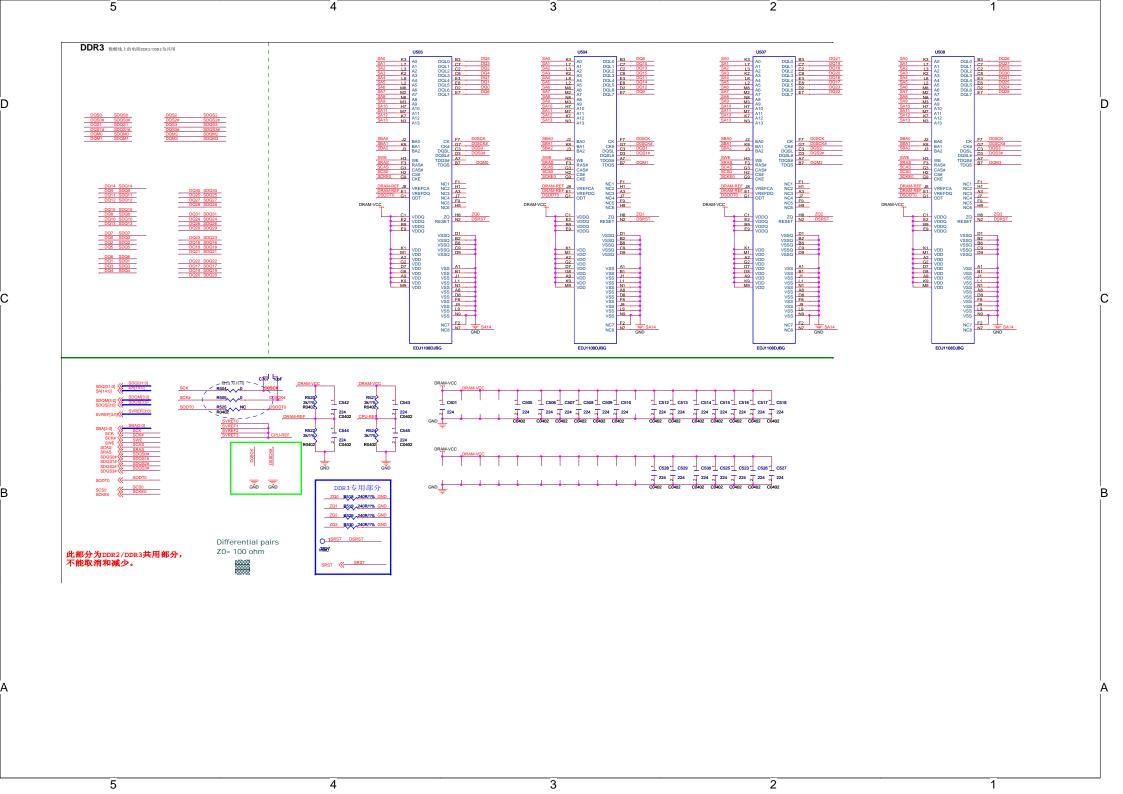
NAND Flash

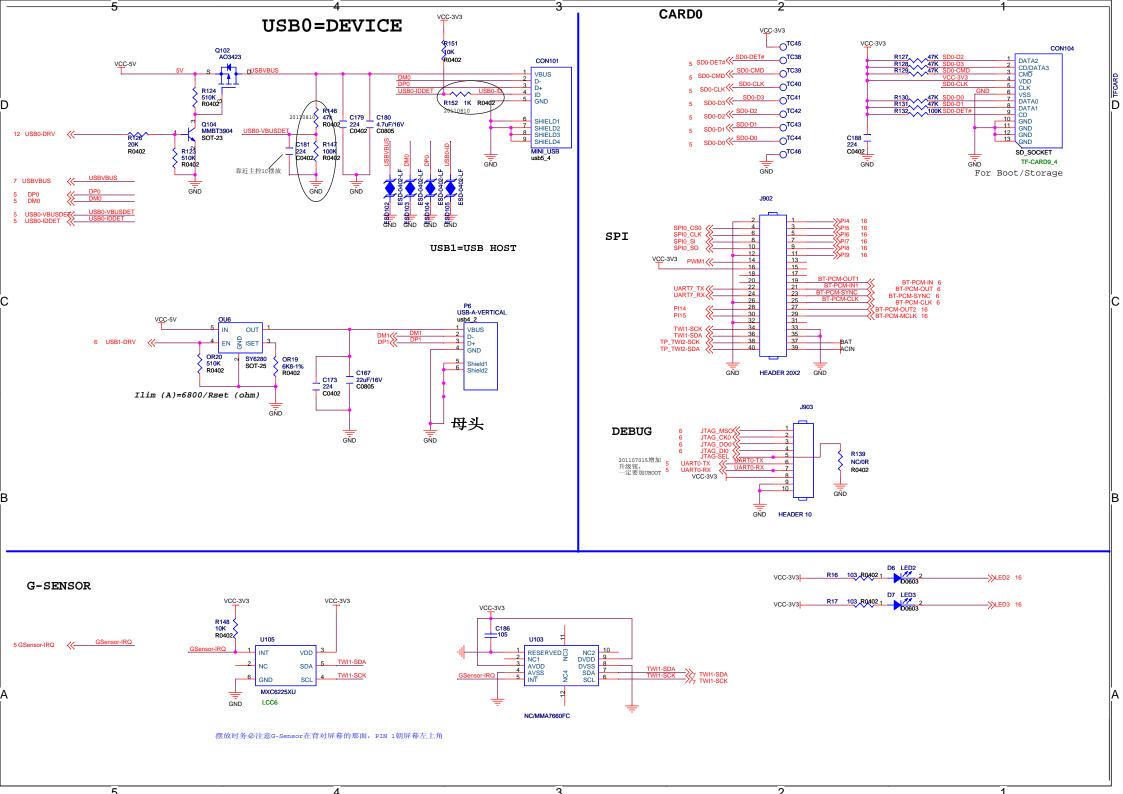
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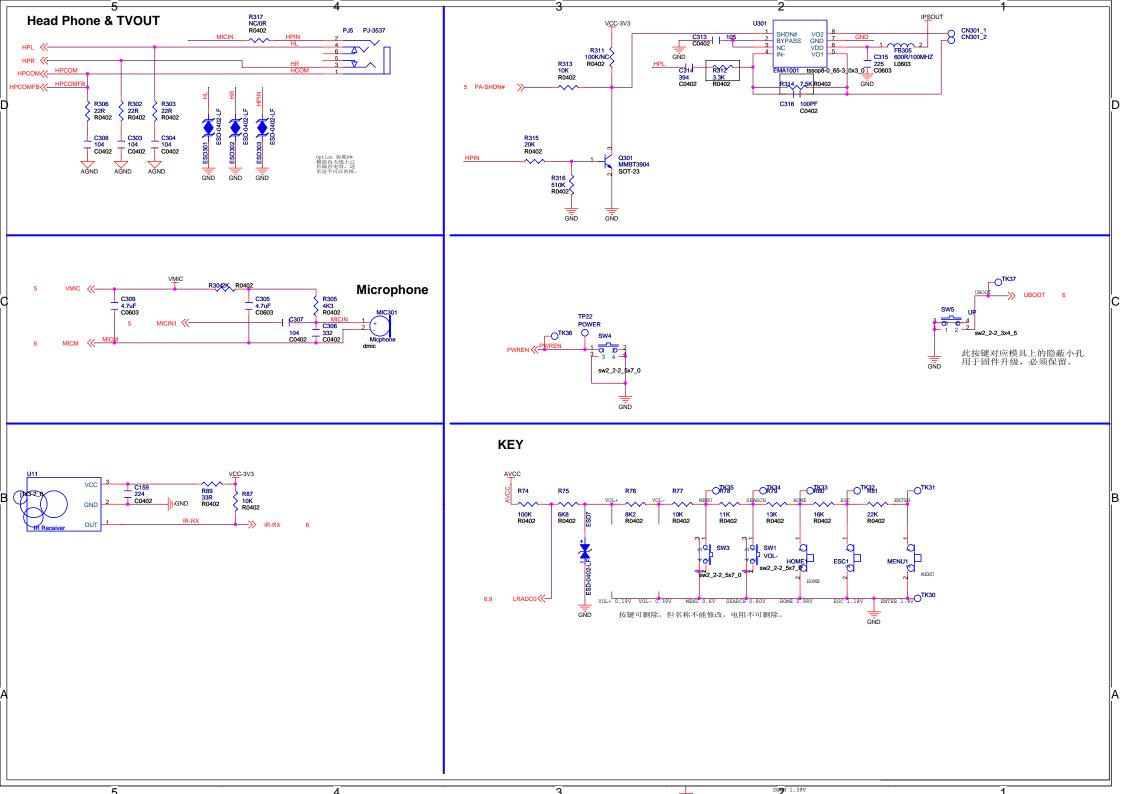
U505 NC22 48 GND GND RES# NC21 47 X 2 NC2 NC20 46 3 NC3 NC19 45 4 NWE# RB4# NALE ND7 5 44 NALE RB3# 107 NCE1# NCE0# NRD# NRB1# NSR2 NC/OR R0402 ND6 ND6 6 43 NCLE 106 RB2# NRB1# NRD# NCE2# 42 7 NCE1# RB1# 105 NSR1 NC/0R R0402 NRD# ND4 ND4 41 8 NCE0# 104 RE# NRB0# NCE0# NC18 40 × 9 NRE# CE1# NRB0# NCE1# 10 39 NCE3# NRB0# CE2# NC17 VPS VCC-3V3 GND NDQS VPS VCC-3V3 GND NDQS NRB1# 11 NRB1# NC4 VSS3 VCC-3V3 GND 12 37 VCC1 VCC2 GND NCE2# 13 36 ND[7:0] VSS2 VSS1 14 35 CE3# NC16 NC15 34 NCE3# VCC-3V3 15 CE4# NWP NCE2# NCE3# NCLE NALE NC15 NC14 33 32 NCLE 16 NWP CLE ND3 ND2 NALE 17 NCE2# ALE 103 ND2 NWE# NWE# 18 31 NCE3# WE# 102 NAND-WP# ND1 NAND-WP# ND1 19 30 **NDQS** WP# 101 100 29 20 ND0 ND0 NC5 21 NC6 NC13 NC12 NC11 NC11 22 NC7 GND VCC-3V3 23 NC8 NC10 25 GND GND NC9 NAND FLASH X8 TSOP48 **TSOP-48 Nand**

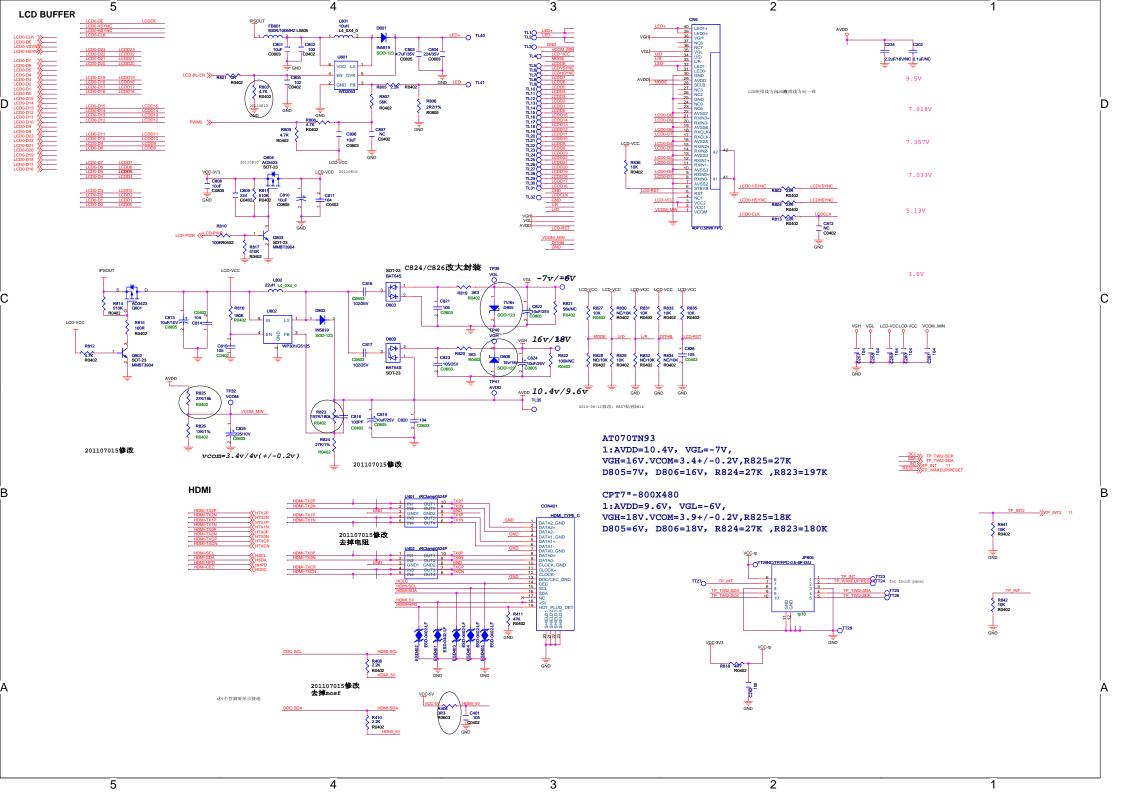
- (1) 接1片单片选Nand 时,NSR2、NSR1断开
- (2) 接1片双片选Nand 时,连接NSR2,断开NSR1
- (3) 接1片四片选Nand 时,连接NSR1,断开NSR2
- (4) 接2片单片选或接2片双片选Nand时,连接NSR1,断开NSR2
- (5) 接Intel、Toshiba、Samsung 2xnm TSOP Nand时, NSR4连接, NSR5断开; 其它的NSR4断开, NSR5连接

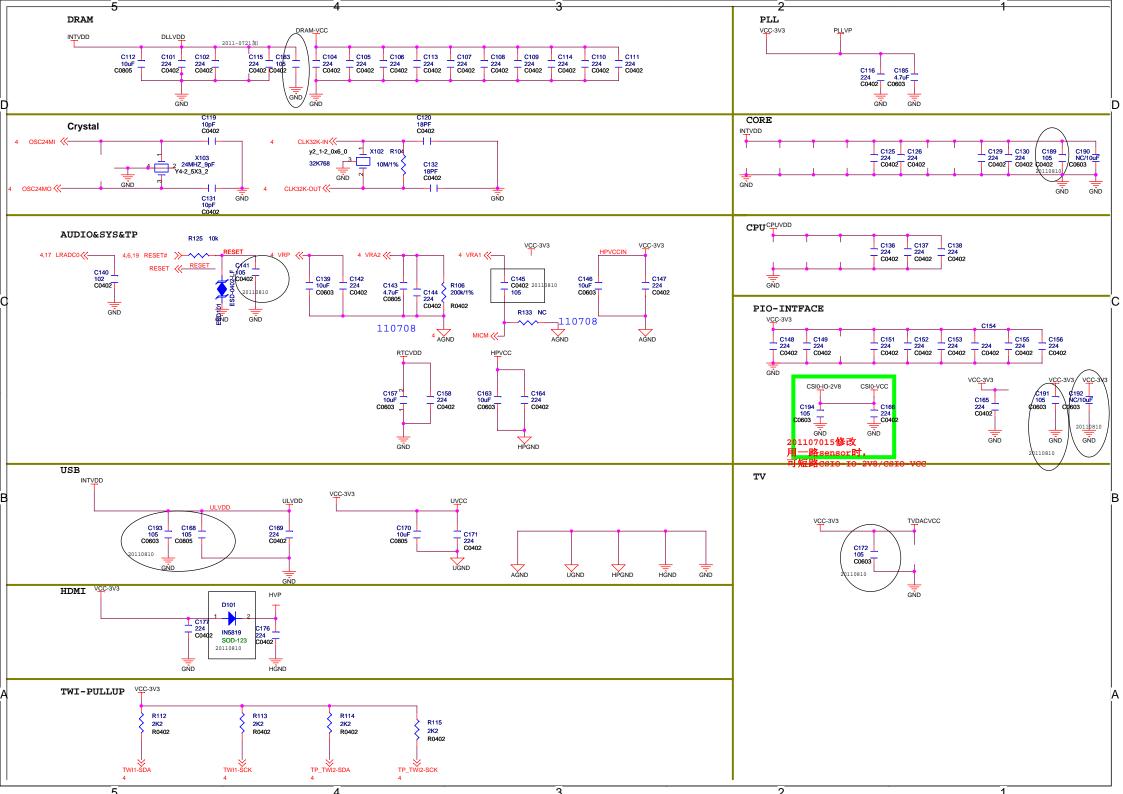


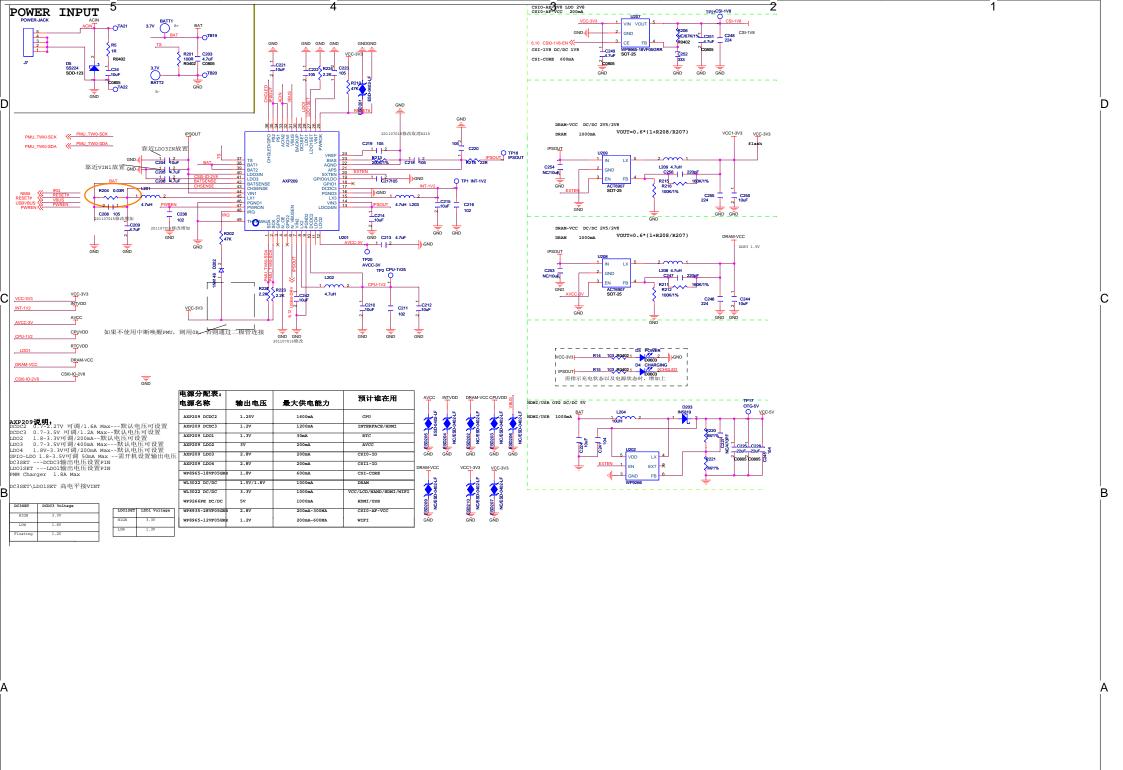




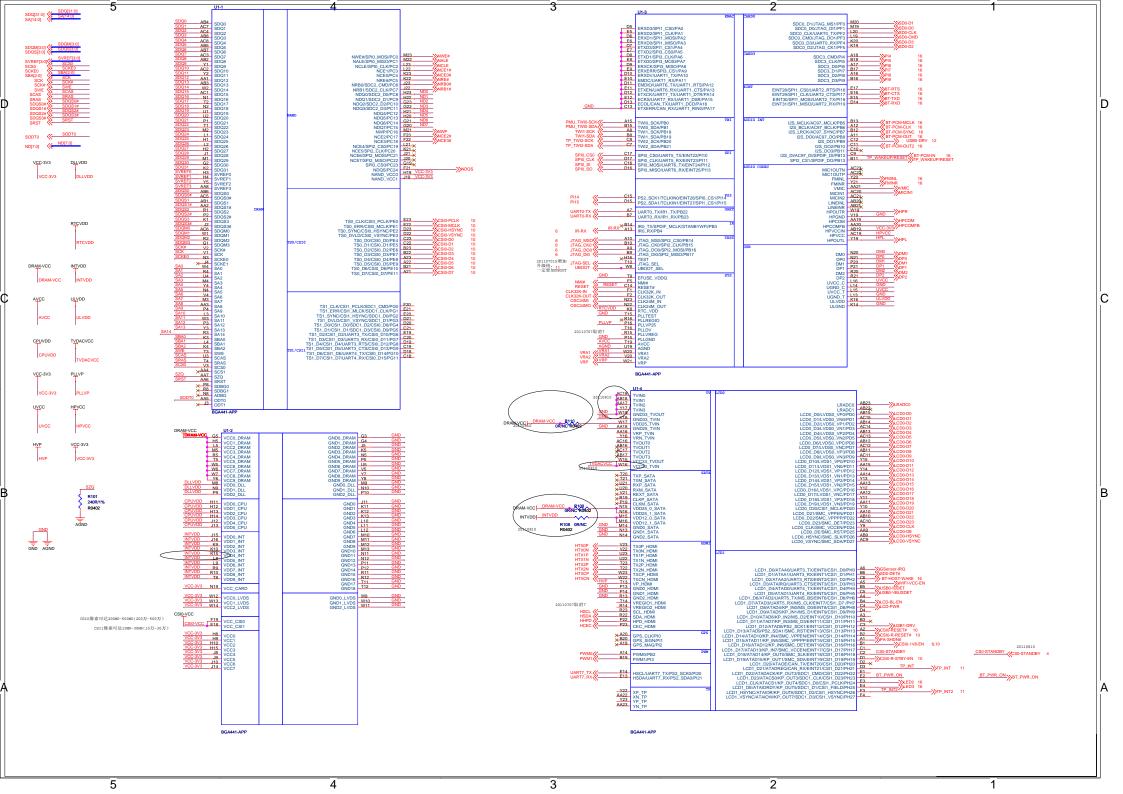








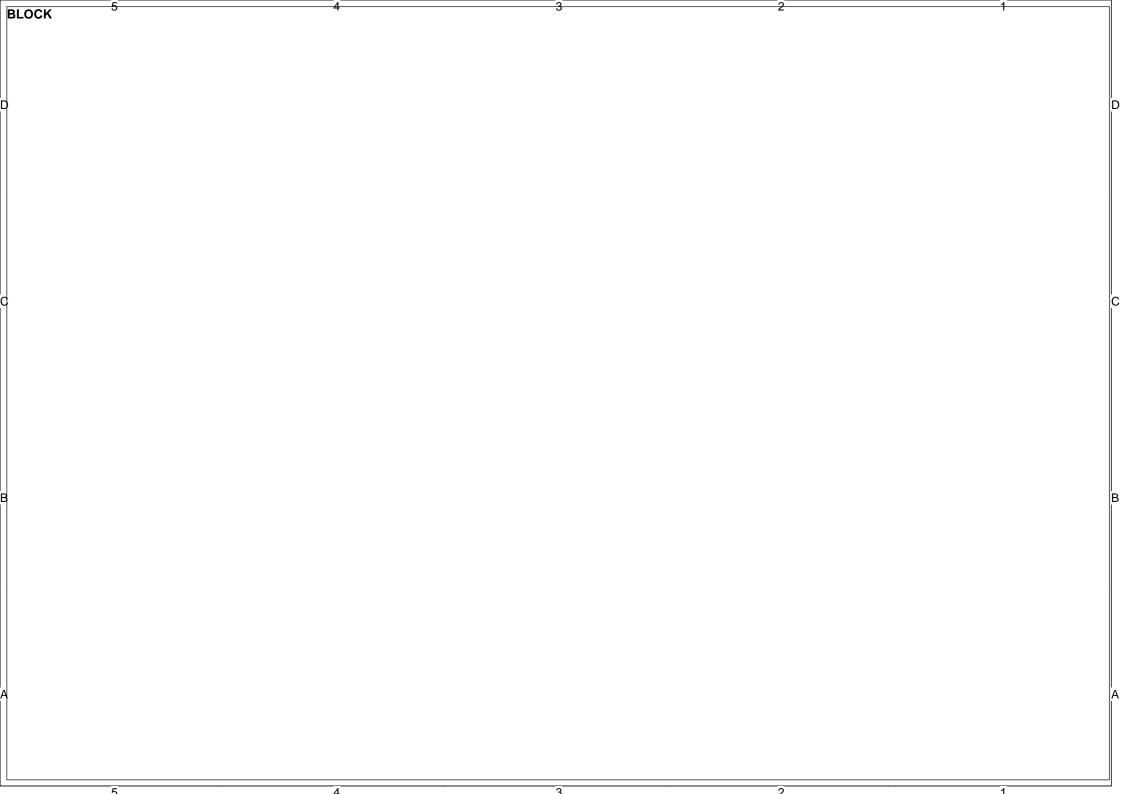
 $\overline{\mathbf{5}}$ 4 $\overline{\mathbf{3}}$ 2



PIO ASSIGNMENT

Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function	Pin Group	Pin Name	Define	Function		Pin Group	Pin Name	Define	Function
	PA0	ERXD3				PC0	NWE#				PD18	LCD0_D18			PH0	EINT0	GS-INT1			PI15	GPIO_OUT	GPS-RX-EN
l	PA1	ERXD2				PC1	NALE	İ			PD19	LCD0_D19			PH1	GPIO_IN	SD0-DET#			PI16	UART2_RTS	BT-UART-RTS
	PA2	ERXD1	1			PC2	NCLE	1			PD20	LCD0_D20			PH2	GPIO_IN	SD1-DET#			PI17	UART2_CTS	BT-UART-CTS
l	PA3	ERXD0				PC3	NCE1				PD21	LCD0_D21			PH3	GPIO_OUT	USB2-DRV		PI(22)	PI18	UART2_TX	BT-UART-TX
l	PA4	ETXD3				PC4	NCE0			PD(28)	PD22	LCD0_D22	LCD		PH4	GPIO_IN	USB0-IDDET			PI19	UART2_RX	BT-UART-RX
	PA5	ETXD2				PC5	NRE#				PD23	LCD0_D23			PH5	GPIO_IN	USB0-VBUSDET			PI20	GPIO_OUT	BT-GPIO0
l	PA6	ETXD1				PC6	NRB0				PD24	LCD0_CLK			PH6	GPIO_OUT	USB1-DRV			PI21	GPIO_OUT	BT-GPIO1
PA(18	PA7	ETXD0	EMAC			PC7	NRB1				PD25	LCD0_DE			PH7	GPIO_OUT	LCD-BL-EN					
l	PA8	ERXCK				PC8	NDQ0				PD26	LCD0_HSYNC			PH8	GPIO_OUT	LCD-PWR					
l	PA9	ERXERR				PC9	NDQ1				PD27	LCD0_VSYNC			PH9	GPIO_OUT	WIFI-SHDN#					
l	PA10	ERXDV				PC10	NDQ2				PE0	CSIO_PCLK			PH10	GPIO_OUT	WIFI-HOST WAKEUP					
l	PA11	EMDC				PC11	NDQ3				PE1	CSIO_MCLK			PH11	GPIO_OUT	WIFI-VDD-EN					
	PA12	EMDIO			PC(25)	PC12	NDQ4	NAME			PE2	CSIO_HSYNC		PH(28)	PH12	GPIO_OUT	WIFI-VCC-EN					
	PA13	ETXEN			-	PC13	NDQ5	NAND			PE3	CSIO_VSYNC		PH(28)	PH13	GPIO_OUT	CSIO-RESET#					
l	PA14	ETXCK				PC14	NDQ6			PE(12)	PE4	CSIO_DO	CCTO		PH14	GPIO_OUT	CSI1-RESET#					
l	PA15	ECRS				PC15	NDQ7			FE(12)	PE5	CSIO_D1	CSI0		PH15	GPIO_OUT	PA-SHDN#					
l	PA16	ECOL				PC16	NWP				PE6	CSIO_D2			PH16	GPIO_OUT	CSI0-1V8-EN					
⊢	PA17	GPIO_OUT	E-RST		-	PC17	NCE2				PE7	CSIO_D3			PH17	GPIO_OUT	CSI1-1V8-EN					
l	PB0	TWIO_SCK	PMU			PC18	NCE3	GPS-			PE8	CSIO_D4			PH18	EINT18	CSIO-STBY-E	8				
	PB1 PB2	TWIO_SDA				PC19	SPI2_CS SPI2_SCLK	SCS GPS-			PE9	CSIO_D5			PH19	EINT19	CSI1-STBY-E	N .				
			PWM CP-RST		-	PC20	SPI2_SCLR SPI2_MOSI	SCLK GPS-			PE10	CSIO_DO			PH20	EINT20	LS-INT					
l	PB3 PB4	GPIO_OUT IRO_RX	IR		-	PC21 PC22	GPIO_OUT	MOSI GPS-			PE11 PF0	_			PH21 PH22	EINT21 SDC1_CMD	TP-INT					
	PB5	GPIO_OUT	BT-RST		-	PC23	NC	VCC-EN			PF1	SDC0_D1			PH23	SDC1_CLK						
l	PB6	I2S_BCLK	BT-PCM		-	PC24	NDOS	1			PF2	SDC0_D0			PH24	SDC1_D0						
l	PB7	I2S LRCK	-CLK BT-PCM			PD0	LCD0_D0			PF(6)	PF3	SDC0_CLK SDC0_CMD	SDC0		PH25	SDC1_D1	SDC1					
	PB8	I2S DO0	-SYNC BT-PCM			PD1	LCD0 D1	1			PF4	SDC0_CMD			PH26	SDC1_D2						
l	PB9	GPIO OUT	-OUT USB0-DRV		-	PD2	LCD0 D2	1			PF5	SDC0_D3			PH27	SDC1 D3						
l	PB10	GPIO_OUT	LCD0-SCK		H	PD3	LCD0_D3	1			PG0	CSI1_PCLK			PIO	GPS_CLK						
PB(24	PB11	GPIO_OUT	LCD0-SDA			PD4	LCD0_D4				PG1	CSI1_MLCK			PI1	GPS_SIGN	GPS					
PB (24	PB12	I2S_DI	BT-PCM-I	N		PD5	LCD0_D5				PG2	CSI1_HSYNC			PI2	GPS_MAG	0.0					
l	PB13	GPIO_OUT	TP-WAKEU	P		PD6	LCD0_D6	†			PG3	CSI1_VSYNC			PI3	PWM1						
l	PB14	JTAG_MS0				PD7	LCD0_D7	1			PG4	CSI1_D0			PI4	SDC3_CMD						
ĺ	PB15	JTAG_CK0	1		PD(28)	PD8	LCD0_D8	LCD			PG5	CSI1_D1			PI5	SDC3_CLK						
ĺ	PB16	JTAG_DO0	JTAG			PD9	LCD0_D9	1		PG(12)	PG6	CSI1_D2	CSI1	PI(22)	PI6	SDC3_D0						
l	PB17	JTAG_DI0				PD10	PD10 LCD0_D10							PI7	SDC3_D1	WIFI						
ĺ	PB18	TWI1_SCK	mu = 3			PD11	LCD0_D11				PG7	CSI1_D3			PI8	SDC3_D2						
l	PB19	TWI1_SDA	TWI1			PD12	LCD0_D12				PG8	CSI1_D4			PI9	SDC3_D3						
ĺ	PB20	TWI2_SCK	mu o	TWI2 JART (DBUG)		PD13	LCD0_D13	5		-					PI10	SPIO_CSO	GS-INT2					
l	PB21	TWI2_SDA	1 W12			PD14	LCD0_D14				PG9	CSI1_D5			PI11	SPIO_CLK	CSI0-AF-EN					
ĺ	PB22	UARTO_TX	113.00			PD15	LCD0_D15				PG10	CSI1_D6			PI12	SPIO_MOSI	TV-EN					
l			(DBUG)			PD16	LCD0_D16					1	-	PI13	SPIO_MISO	CP-INT						
匚	PB23	UARTO_RX				PD17	LCD0_D17				PG11	CSI1_D7			PI14	GPIO_OUT	GPS-OSC-EN					

7



Notes: Unless Otherwise Stated

Scheme Spec:

FLASH: MLC, 3.3V

DRAM: DDR2/3 1.8V /1.5V

Key: NEXT, PREV, Vol+, Vol-, UP, DOWN, ENTER, UBOOT

Power: DCIN, 5V, 2A; BAT, 3.7V, 3600mAH

USBO: OTG USB1: HOST USB2: HOST WIFI: SDIO WIFI Card: TFcard*2

Other: GPS, FM, Headphone, MIC, G-Sensor, camera

Power Supply:

电源分配表:			
电源名称	输出电压	最大供电能力	预计谁在用
AXP209 DCDC2	1.25V	1600mA	CPU
AXP209 DCDC3	1.2V	1200mA	INTERFACE/HDMI
AXP209 LD01	1.3V	30mA	RTC
AXP209 LDO2	3V	200mA	AVCC
AXP209 LDO3	2.8V	200mA	CSI0-IO
AXP209 LDO4	2.8V	200mA	CSI1-IO
WP8965-18VF05GRR	1.8V	600mA	CSI-CORE
WL3022 DC/DC	1.5V/1.8V	1000mA	DRAM
WL3022 DC/DC	3.3V	1000mA	VCC/LCD/NAND/HDMI/WIFI
WP9266PE DC/DC	5V	1000mA	HDMI/USB
WP8935-28VF05GRR	2.8V	200mA-300MA	CSI0-AF-VCC
WP8965-12VF05GRR	1.2V	200mA-600MA	WIFI

Schematics Index:

P01: COVER P02: BLOCK

P03: PIO ASSIGNMENT

P04: POWER TREE

P05: CPU1 P06: CPU2 P07: POWER1 P08: POWER2 P09: BESIDE CPU P10: HDMI-CSI

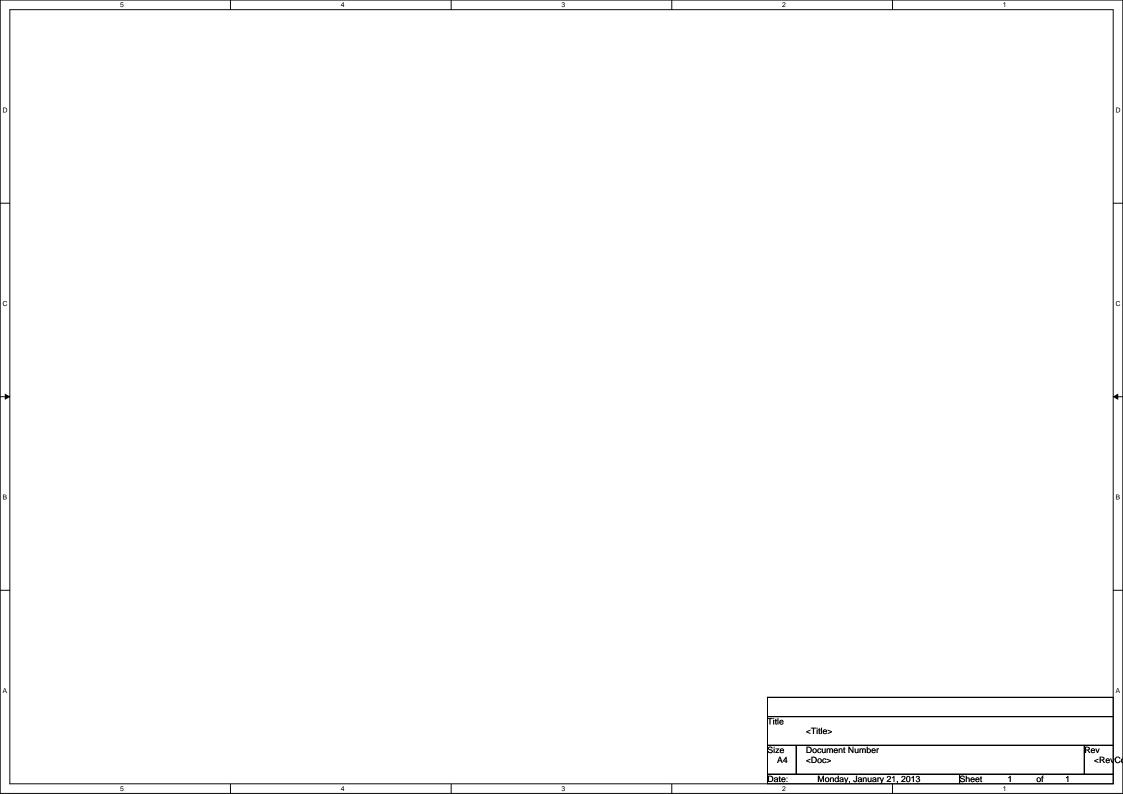
P11: HP-FM-KEY-MIC-IR-TVOUT

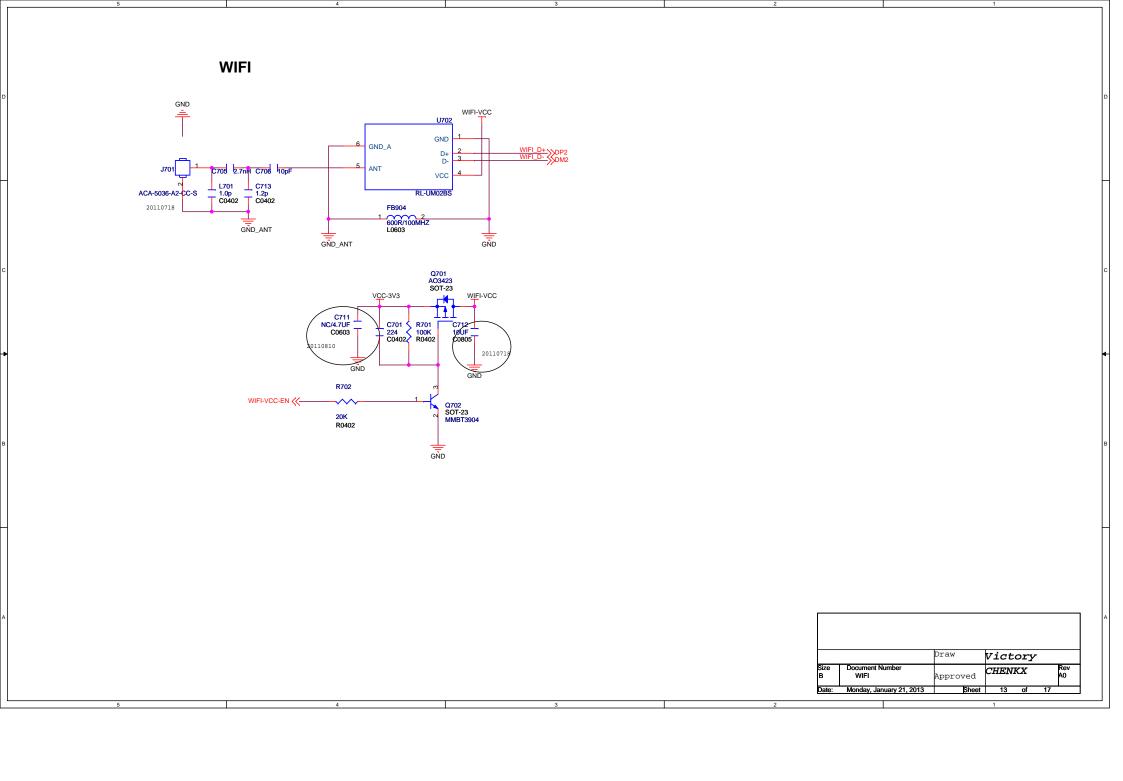
P12: USB-CARD P13: LCD P14: DRAM3 P15: NAND

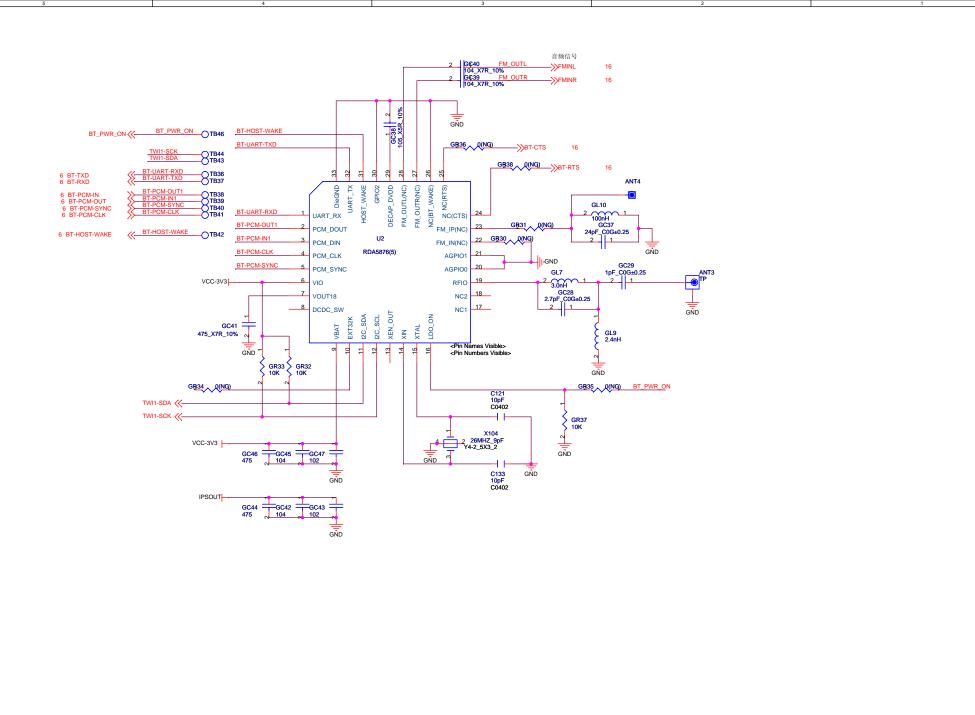
P16: WIFI-GSENSOR

P17: GPS

Rev	Description	Date	Drawn	Checked	Approved
PAD_MAINCHIP_STD_V1.13	没有ACIN的时候与USBVBUS短接	2011-06-30	Leo		
PAD_MAINCHIP_STD_V1.14	增加LVDS和CTP的连接方式	2011-07-06	Leo		
PAD_MAINCHIP_STD_V1.15	更改了UBOOT按键电路,WIFI电源电路	2011-07-07	Leo		
PAD_MAINCHIP_STD_V1.15	更改了USB限流电路	2011-07-08	Leo		
PAD_MAINCHIP_STD_V1.15	更改了电源电路	2011-07-12	Leo		







<Title> Document Number Size A3 Rev <RevCode> <Doc> Tuesday, January 22, 2013 Sheet 16 of

WT-M76:1108(1(V0)版SCH修改记录: (1) 增加测试点-panel (2) 对喇叭功放输出稍调大少许:R312/R318由原4.7k改为3.3k,R314/R317由原6.8k改为7.5k (3)增加了对CPT和群创tn92的屏的不同器件的提示,如下: AT070TN93 1:AVDD=10.4V, VGL=-7V, VGH=16V.VCOM=3.4+/-0.2V.R825=27KD805=7V, D806=16V, R824=27K ,R823=197K CPT7"-800X480 1:AVDD=9.6V, VGL=-6V, VGH=18V.VCOM=3.9+/-0.2V,R825=18K D805=6V, D806=18V, R824=27K ,R823=180K (4) 取消了电容屏的供电, R867/R866=NC. (5)修改了HDMI的5v接口电路,供电电阻R405由原7.5欧姆改为3.3欧姆. (6) 删除了CON801连接器。 (7) 修改了WIFI电路的匹配, C705由原22PF改为1.5PF,原C714改为L701高频电感=2.4NH+/-10%, SMT0402,测试电流/频率: 0.3A@100MHZ (8)修改了定位孔H3/H4。 (9)摄像头的连接器J901用下接 (10)AC18/AB18/AA17/Y17/W18由原悬空改为接地,方便散热。 (11) 在DRAM-VCC和w17间串接增加了R110 OR。使系统稳定。 (12) 在DRAM-VCC和N15/N16间串接增加了R109_OR。使系统稳定。 (13) 在INTVDD和M15/M16间串接增加了R108_OR。使系统稳定。 (14) ddr电源部分R217由原150k改为160k,提高DRAM-VCC电压0.05v,使系统稳定. (15) wifi电源部分wifi-vdd-en增加R214_47k下拉电阻,使系统稳定. (16)c191由原4.7uf改为105,c145由原224改为105,c189由原4.7uf改为105,c192由原10uf改为nc.c172由原10uf-0805改为105-0603. c141由原224改为105,c193由原4.7uf改为105,c168由原10uf改为105.c183由原4.7uf改为105,c711由原4.7uf改为nc. (17)修改了hdmi3v3供电电路,增加了二极管D101-IN5819 SOD123,防止hdmi倒灌。 (18)修改了R809/C806的上拉供电电路,由vcc改为lcd-vcc. (19)usb-id pin上增加了R152_1k串接,防止esd损坏主控。 (20) 省去了摄像头多余的供电电路: R908/R911/R912/Q902/Q901/C906=NC. (21)修改了摄像头的CSI-STANDBY电路,连接到主控的PH18 IO口上C2 PIN.CSI-STANDBY改为CSO-STANDBY.R910c711由原100k改为47k, R909原510K改为NC. (22)001-A03423改为0804-A03423. M76(V0)版PCB修改记录: (1)上面的三个按键整体下移0.1mm. (2) MICRO-USB向外移0.8mm.加大了定位脚焊盘和过孔。 (3)添加了测试点。 (4) 修改了wifi的配匹电路。 (5) 加大了电源输出端的过孔 (6) 电源芯片下面的过孔加大加多,解决散热问题。 (7) 修改了定位孔, 左边的减小0.1mm, 右边靠接插件的孔位改为椭圆形: 2.6mmx2.9mm. (8)板边靠下方的部位减小0.15mm. Title <Title> Size Document Number Rev <RevCode> Monday, January 21, 2013 Sheet