DATA SHEET

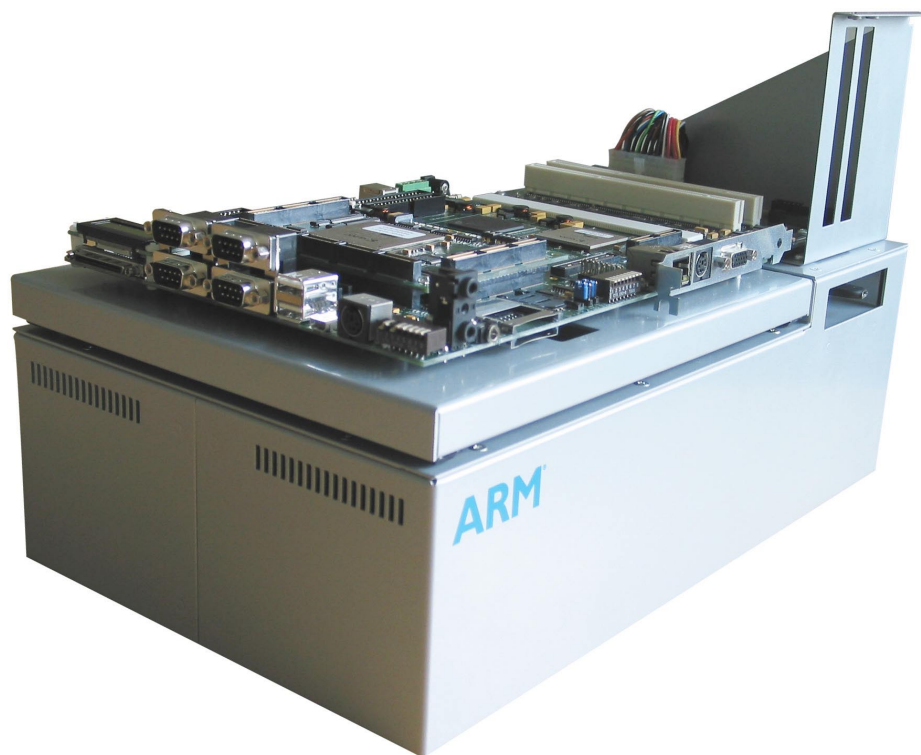# PCI Backplane

The ARM® RealView® Versatile family of development boards provide a feature rich prototyping system for system-on-chip designs. To enhance the Versatile Platform Baseboard there are a range of add-on components including LCD panels and a PCI backplane for peripheral expansion. This datasheet describes the PCI backplane enclosure in more detail.

Versatile Platform Baseboard for ARM926EJ-S mounted on the PCI Backplane Encosure
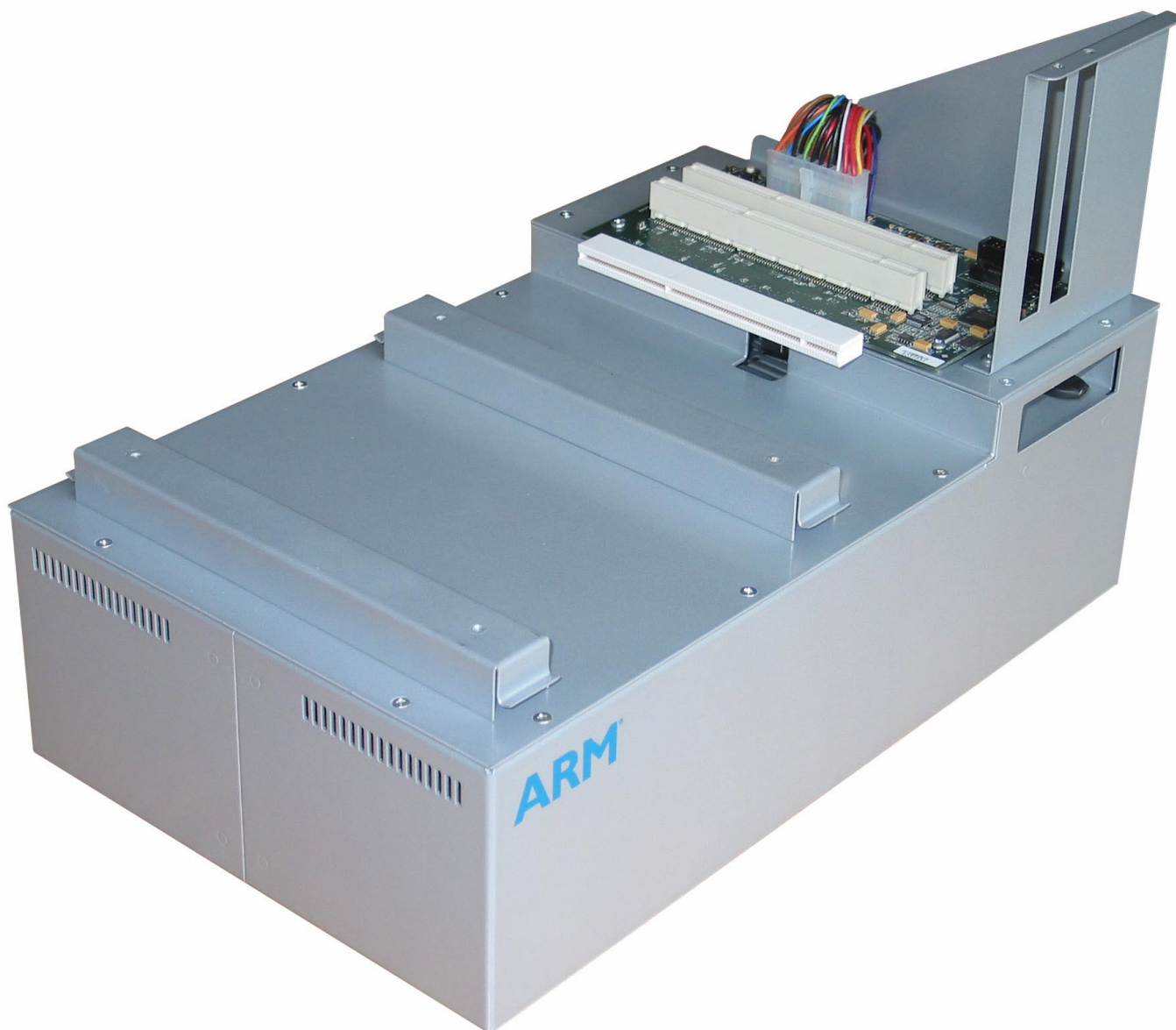
The PCI backplane and enclosure provides,
- An ATX power supply to power PCI cards and Logic Tiles on the baseboard
- A robust metal enclosure that connects directly to the baseboard tray for ease of use
- Release lever for baseboard extraction
- Support for up to 2 plug in cards with mechanical fixing for the PCI back-plate
- Multi-region power cables

The PCI backplane includes power switching, a bus arbiter and clock generation circuitry. It is a 3-slot, 64-bit bus capable of supporting clocks up to 66MHz. The Versatile Platform Baseboard for ARM926EJ-S is usually the host controller and drives the bus at 66MHz with 32-bit wide transfers by default. The baseboard is not 5V tolerant, so PCI cards supporting 3.3V I/O operation must be used, and the backplane connectors are keyed to prevent incompatible cards from being plugged in.

Documentation for the PCI backplane is included in the Versatile Platform Baseboard for ARM926EJ-S User Guide, Appendix D. The Versatile Platform CD will include a PCI library and a utility that shows how to detect and configure PCI cards (contact ARM for availability of PCI firmware).

PCI Backplane Specification

- Three 64-bit 3.3V Slots, one horizontal mount and two conventional vertical mount
- Clock generator supporting 33MHz and 66MHz operation
- PCI bus arbiter implemented in a PLD
- Multi-region ATX power supply mounted in enclosure

Ordering Information

Part number	Description
VPCIB-BD-0191A	64-bit, 66MHz 3V3 PCI Backplane and ATX Power Supply with Custom Enclosure

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