

3.38 A memory address refers to a location in memory.(5'

Memory's addressability is the number of bits stored in each memory location.(5'

(体现 memory address 为数据存储的 location, addressability 是每个 location 的位数宽度即可)

3.40

a) 4 locations (5'

b) 4 bits (5'

c) 0001 (5'

3.53

0 1 1 1 1 0 0 0 (5', 错一个扣两分之后每一个一分, 这一行扣完为止, 以下相同

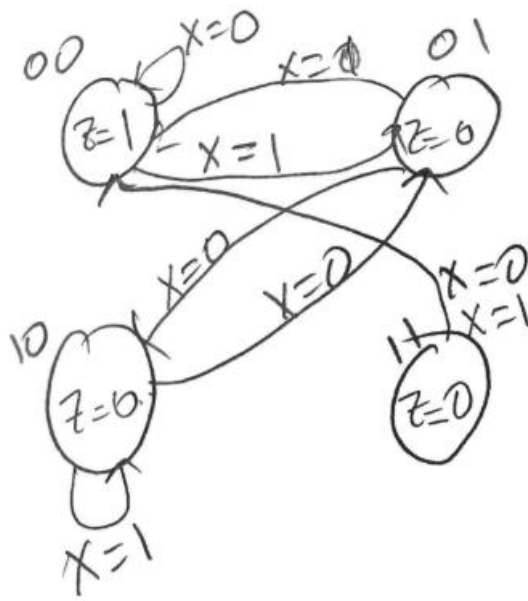
0 1 1 0 0 1 1 0 (5'

0 1 0 1 0 1 0 1 (5'

Each time circle the value do the operation "-1", and if it is 0, next state is

7.(5'

3.61



(25', 每个箭头 5 分, 箭头的指向有与原图不相符的扣 3 分, 箭头上的数字不相同扣 2 分, 对每个状态如果没写 z 的输出也给分 (因为不重要)

4.1(10', 每个元素占 2 分)

Memory: Storage of information (data/program)

Processing Unit: Computation/Processing of Information

Input: Means of getting information into the computer. e.g. keyboard, mouse

Output: Means of getting information out of the computer. e.g. printer, monitor

Control Unit: Makes sure that all the other parts perform their tasks

correctly and at the correct time.

4.7

-32768~32767 (20', 计算出立即数的位数(16 位)即可得 15 分