

C:/Users/Sterm/Documents
/GitHub/Rover_gruppo3/Codice
_Rover/Progetto_Board2/Core
/Src/stm32g4xx_it.c

```
graph TD; Root["C:/Users/Sterm/Documents /GitHub/Rover_gruppo3/Codice _Rover/Progetto_Board2/Core /Src/stm32g4xx_it.c"] --> main_h["main.h"]; Root --> stm32g4xx_it_h["stm32g4xx_it.h"]; Root --> log_wcet_monitor_h["log/wcet_monitor.h"]; main_h --> stm32g4xx_hal_h["stm32g4xx_hal.h"]; log_wcet_monitor_h --> stdint_h["stdint.h"];
```

main.h

stm32g4xx_it.h

log/wcet_monitor.h

stm32g4xx_hal.h

stdint.h