

DMA1_Channel1_IRQHandler

DMA1_Channel3_IRQHandler

UART4_IRQHandler

WCET_Update

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graph LR; A[DMA1_Channel1_IRQHandler] --> D[WCET_Update]; B[DMA1_Channel3_IRQHandler] --> D; C[UART4_IRQHandler] --> D;
```

The diagram illustrates a common code path for multiple interrupt handlers. Three separate handler functions, DMA1_Channel1_IRQHandler, DMA1_Channel3_IRQHandler, and UART4_IRQHandler, are shown on the left. Each handler has a blue arrow pointing to a single, shared function block on the right labeled WCET_Update. This suggests that these different hardware events all trigger the same WCET update logic.