

C:/Users/Sterm/Documents  
/GitHub/Rover\_gruppo3/Codice  
\_Rover/Progetto\_Board1/Core  
/Src/stm32g4xx\_it.c

```
graph TD; Root["C:/Users/Sterm/Documents /GitHub/Rover_gruppo3/Codice _Rover/Progetto_Board1/Core /Src/stm32g4xx_it.c"] --> main_h["main.h"]; Root --> stm32g4xx_it_h["stm32g4xx_it.h"]; Root --> log_wcet_monitor_h["log/wcet_monitor.h"]; main_h --> stm32g4xx_hal_h["stm32g4xx_hal.h"]; log_wcet_monitor_h --> stdint_h["stdint.h"];
```

main.h

stm32g4xx\_it.h

log/wcet\_monitor.h

stm32g4xx\_hal.h

stdint.h