

DMA1\_Channel1\_IRQHandler

DMA1\_Channel3\_IRQHandler

UART4\_IRQHandler

WCET\_Update

```
graph LR; A[DMA1_Channel1_IRQHandler] --> D[WCET_Update]; B[DMA1_Channel3_IRQHandler] --> D; C[UART4_IRQHandler] --> D;
```

The diagram illustrates a common code path for multiple interrupt handlers. Three separate handler functions, DMA1\_Channel1\_IRQHandler, DMA1\_Channel3\_IRQHandler, and UART4\_IRQHandler, are shown on the left. Each handler has a blue arrow pointing to a single, shared function block on the right labeled WCET\_Update. This indicates that regardless of which of these three interrupts occurs, the execution will eventually lead to the WCET\_Update routine.