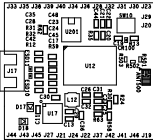



- NOTES: (UNLESS OTHERWISE SPECIFIED)
- 1. ASSEMBLE AND SOLDER PCB PER IPC-CM-770 AND ANSI/IPC-A-610, CLASS 2, LATEST REVISION.
 - 2. ASSEMBLY MUST BE "ROHS" AND "LEAD FREE" COMPLIANT.
 - 3. MARK DATE CODE AS (D/WW/Y) ON THE TOP SIDE OF THE ASSEMBLY USING CONTRASTING PERMANENT INK.
 - 4. ALL MARKINGS MUST BE CLEAR AND LEGIBLE IN PERMANENT NON-CONDUCTIVE INK. PROCESS OR INSPECTION STAMPS MUST BE APPLIED CONSISTENTLY IN THE SAME LOCATION.
 - 5. ALL FPC ASSEMBLIES (FPCA) MUST BE TESTED BEFORE DELIVERY. TEST PLAN MUST BE APPROVED BY INTEL.
 - 6. PACKAGING SHOULD BE ANTI-ESD AND APPROVED BY INTEL.



THIS DRAWING AND THE DATA DISCLOSED HEREIN OR HERewith IS NOT TO BE REPRODUCED, USED OR DISCLOSED IN WHOLE OR IN PART TO ANYONE WITHOUT THE PERMISSION OF INTEL CORP.		
REVISIONS		
REV	DESCRIPTION	ECO
01	PROTOTYPE RELEASE	REFER TO AGILE

CONFIDENTIAL

DRAWN BY: ACS	DATE: 12/29/15	 <div>2200 MISSION COLLEGE BLVD P.O. BOX 58119 SANTA CLARA, CA 95052-8119</div>	
ENGINEER: XXX	DATE: 12/29/15		
CHECKED BY: XXX	DATE: XX/XX/XX		
MATERIALS: SEE BILL OF MATERIALS		PROJECT: ASSEMBLY DRAWING, RADON, FAB 1	
UNLESS OTHERWISE SPECIFIED: DIMENSION ARE IN INCHES TOLERANCE ARE AS FOLLOWS: DECIMALS ± .005 ANGLES ±1°		SIZE B	P/N: H64655-001
		SCALE: NONE	DO NOT SCALE DRAWING
		SHEET 1 OF 1	
		REV 01	