Colin Suckow

Enthusiastic generalist programmer, passionate about creating robust and effective software with Rust and Python at any level of the stack colin@suckow.dev | 541-797-1941

https://github.com/Colin-Suckow https://suckow.dev

Education

B.S. Computer Science

Oregon State University - Cascades, Bend, OR

Cumulative GPA: 3.58

September 2018 - June 2022

Work Experience

Software Development Intern

September 2020 – June 2021

Novunex, Bend, OR

- Assessed customer requirements and developed features to fulfill business needs
- Implemented over 20 new features and user interface flows for an internal quality system using javascript and SQL

Software Engineering Intern

December 2019 - March 2020

Sicamore Semi, Bend, OR

- Prototyped python-based QT UI for tracking hundreds of semiconductor wafers and reducing waste
- Contributed features and bug fixes to an internal web-based quality management system with javascript and SQL
- Designed and built a light indication system using embedded linux and python to increase production efficiency

Software Development Intern

May 2019 - December 2019

Camp Creative, Bend, OR

- Implemented a proof of concept mobile application with React Native to demonstrate the platform's value to clients
- Contributed patches to open source React components to increase compatibility with legacy web browsers
- Architected and implemented search functionality in an existing React web application using Algolia search api

Projects

PlayStation Emulator | Rust

https://github.com/Colin-Suckow/vaporstation

Software interpreter that simulates the internal components of a Sony PlayStation, including the cpu, gpu, dma controller, math accelerator, cd rom drive and video decoder. Capable of running a handful of popular commercial games

Exhibit X | Javascript, Python

https://github.com/osu-cascades/exhibit-x-web

Manages and runs an interactive digital art gallery on campus. Consists of an express based web application, REST api and python script running on an embedded linux host

FPGA VGA Driver | Verilog

https://github.com/Colin-Suckow/fpga_vga_display

Synthesizes a VGA video signal and displays graphical content on a monitor. Display content is received via an SPI interface. Runs on an Altera FPGA

STM32 Synthesizer | C

https://github.com/Colin-Suckow/STM32 Synth

16 channel MIDI synthesizer written to run on an STM32 microcontroller. Listens to commands over a serial port and outputs synthesized music with an onboard digital to analog converter

Oregon State University Cascades Tech Club President

Organized weekly tech workshops, coding competitions, and study sessions for student body