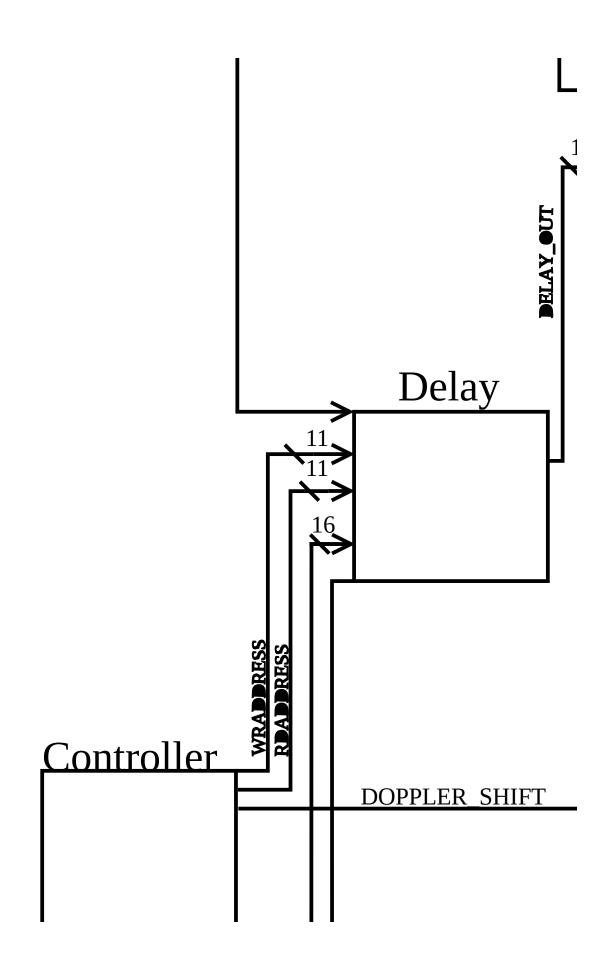
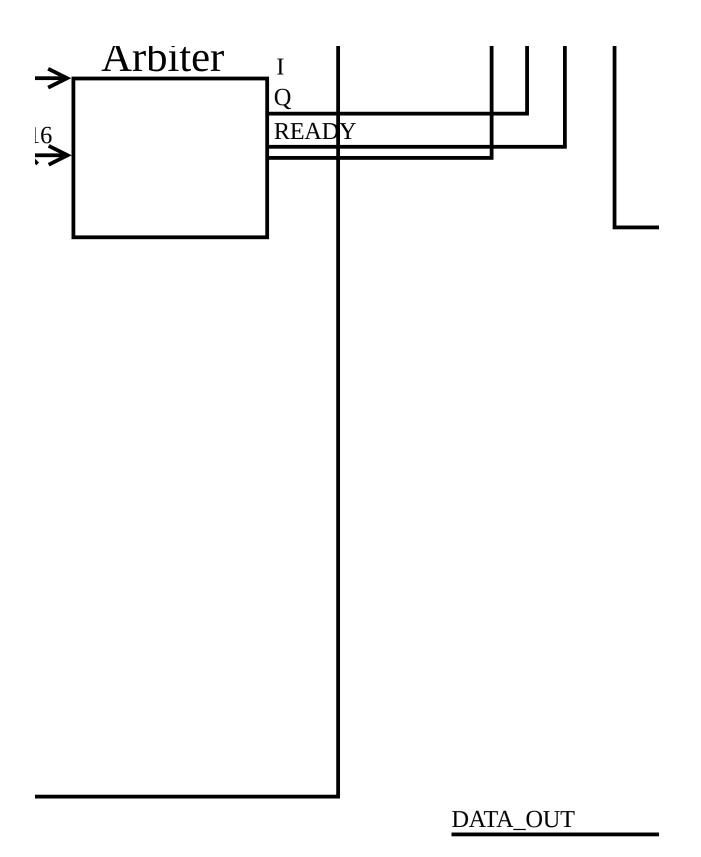
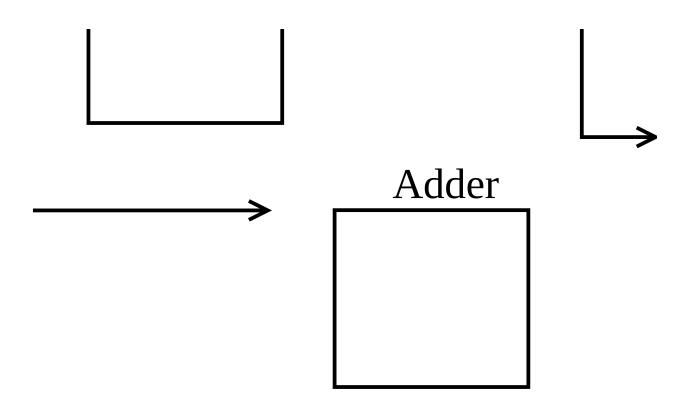
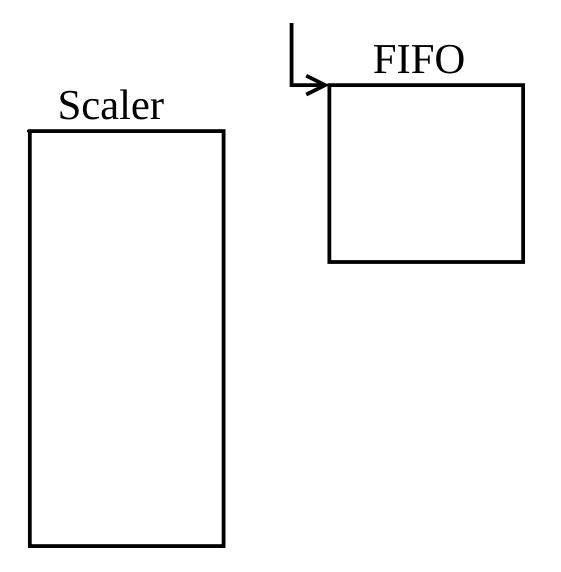


Frequency_Shifter

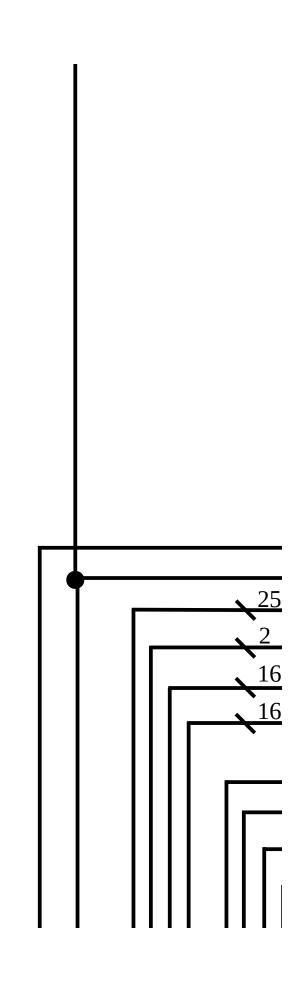






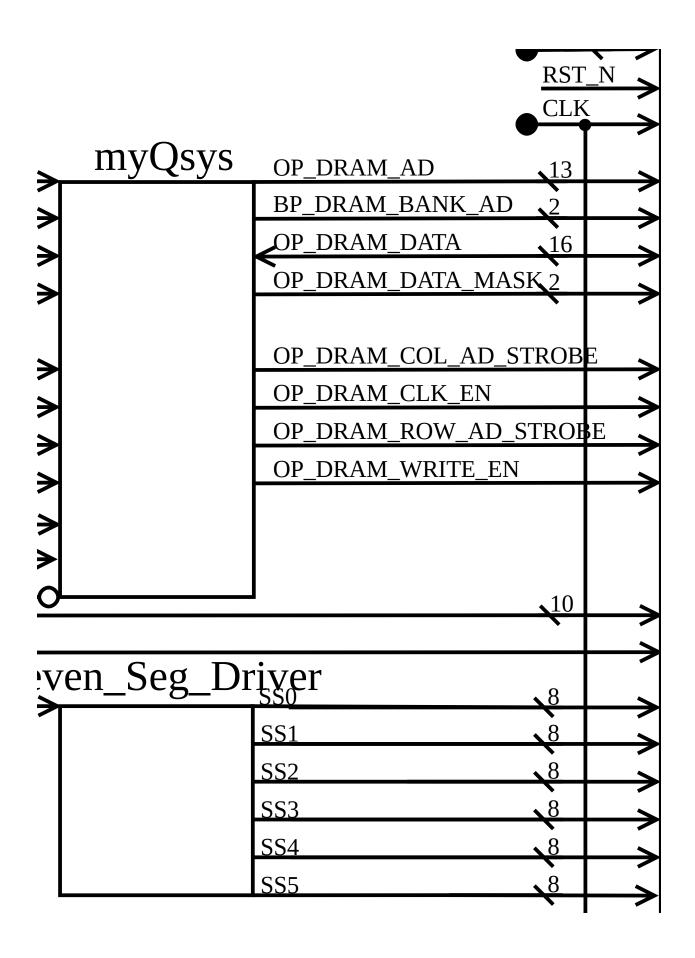


DRFM SS0

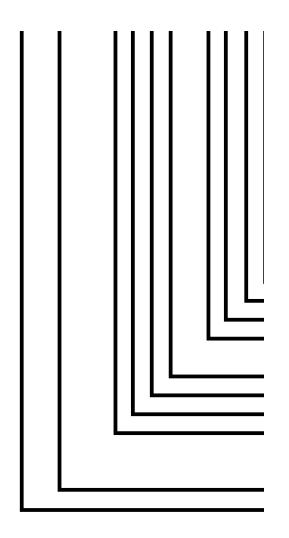


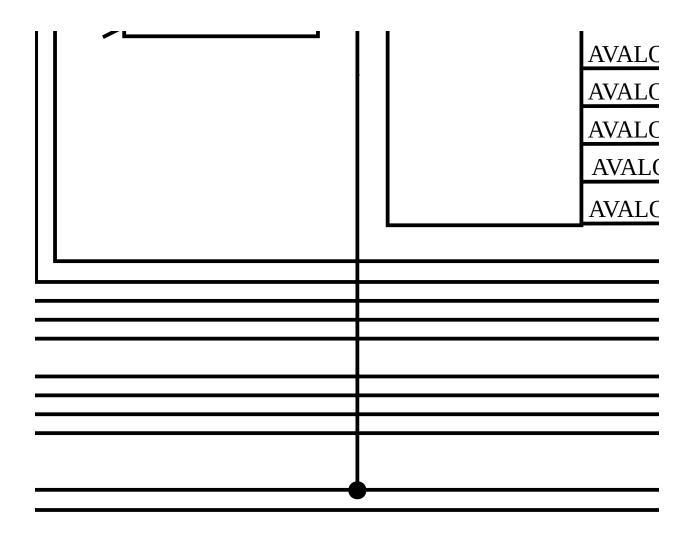
	AVALON_ADDRESS AVALON_BYTE_EN AVALON WRITE D. AVALON_READ_DA
Vi	AVALON_CHIP_ENA AVALON_WAIT_REC AVALON_WRITE AVALON_READ_DA AVALON_READ TUALITAG_MI LED TDO
>	AVALC AVALC AVALC AVALC

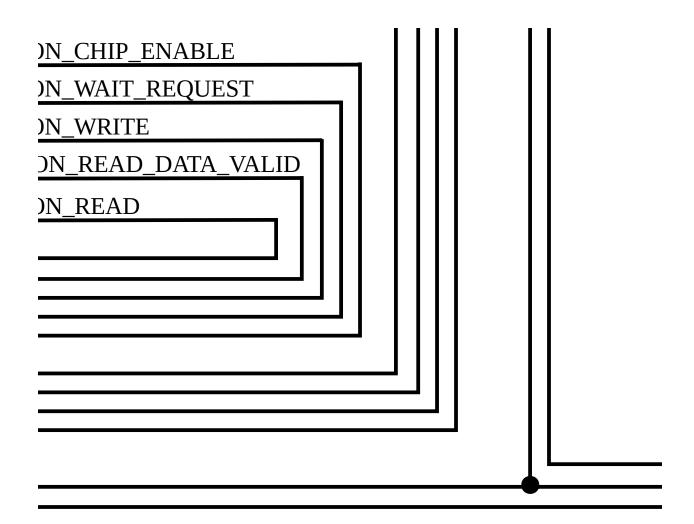
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)N_ADDRESS				
)N_BYTE_EN		_		
<u>)N WRITE DATA</u>				
)N_READ_DATA				

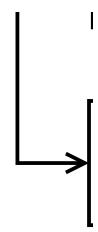


SS2 8
SS3 8
SS4 8
SS5 8
OP_DRAM_AD 13
BP_DRAM_BANK_AD 2
OP_DRAM_DATA 16
OP_DRAM_DATA_MASK_2
OP_DRAM_COL_AD_STROBE OP_DRAM_CLK_EN OP_DRAM_ROW_AD_STROBE
OP_DRAM_WRITE_EN
LED 10
OUT
ARDUINO IO 3
REQUIECT









RDADR

