

List of Schematic Sheets

Sheet: Microcontroller



File: croptop_mcu.sch

Sheet: Display



File: croptop_lcd.sch

Sheet: Onboard Peripherals



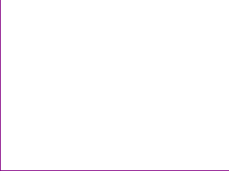
File: croptop_onboard.sch

Sheet: IO Peripherals



File: croptop_io.sch

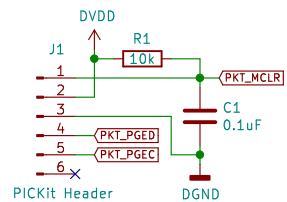
Sheet: Power Supply



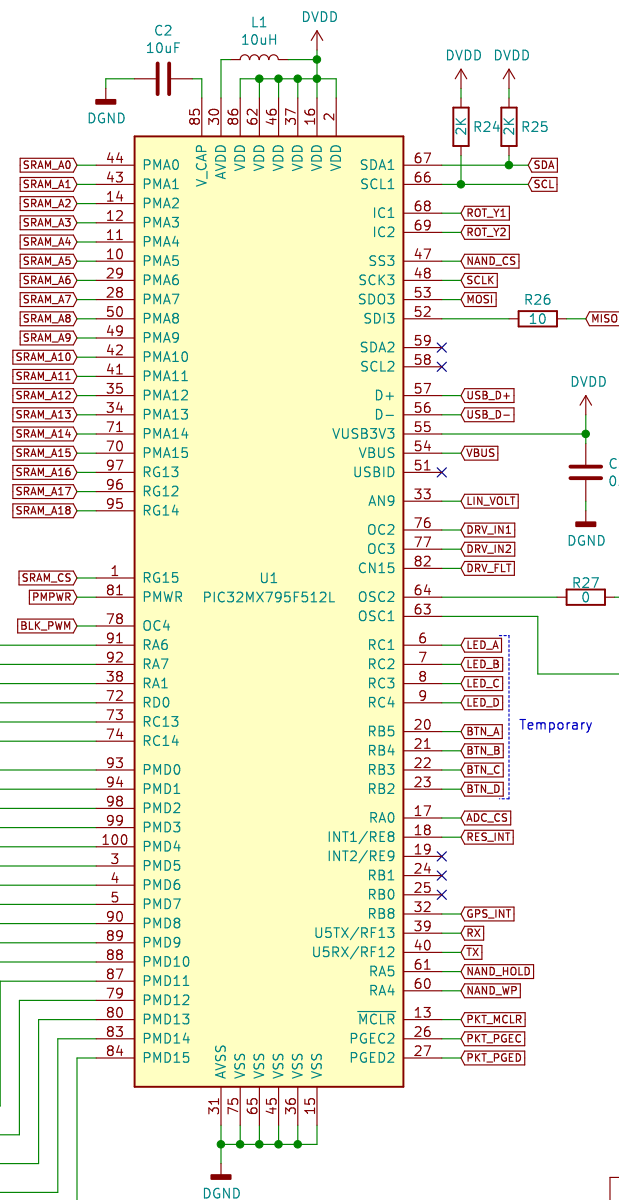
File: croptop_psu.sch

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Sheet: /		
File: croptop.sch		
Title: CropTop		
Size: USLetter	Date: 2018-11-20	Rev: Revision 8
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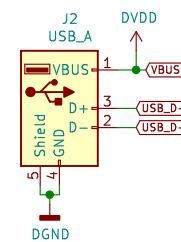
PICKit3 Header



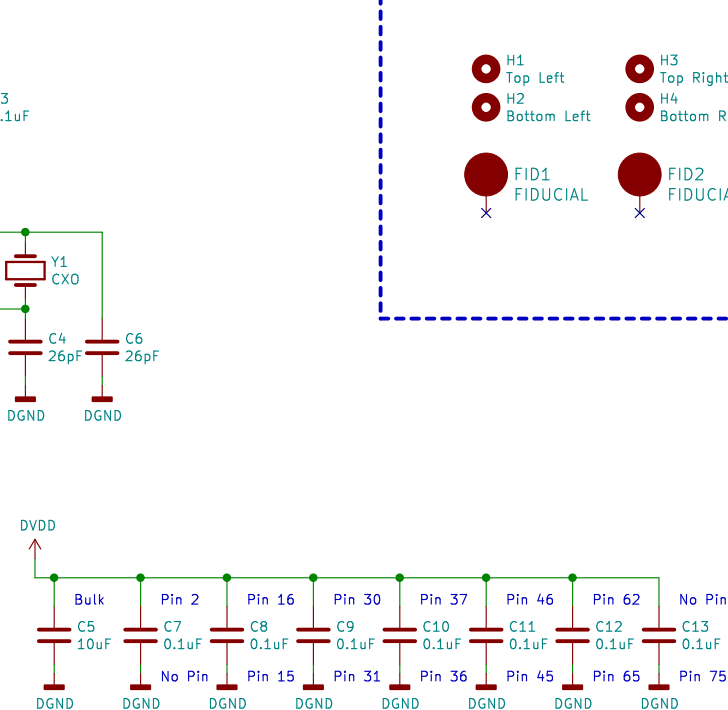
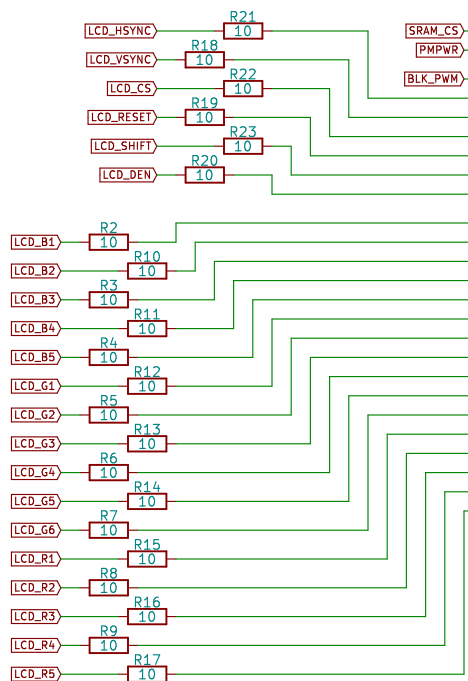
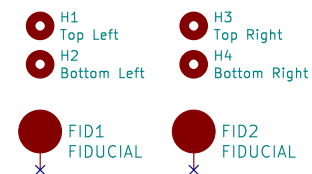
PIC32 Microcontroller



Peripheral USB Port



Mounting Holes & Fiducials



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Sheet: /Microcontroller/
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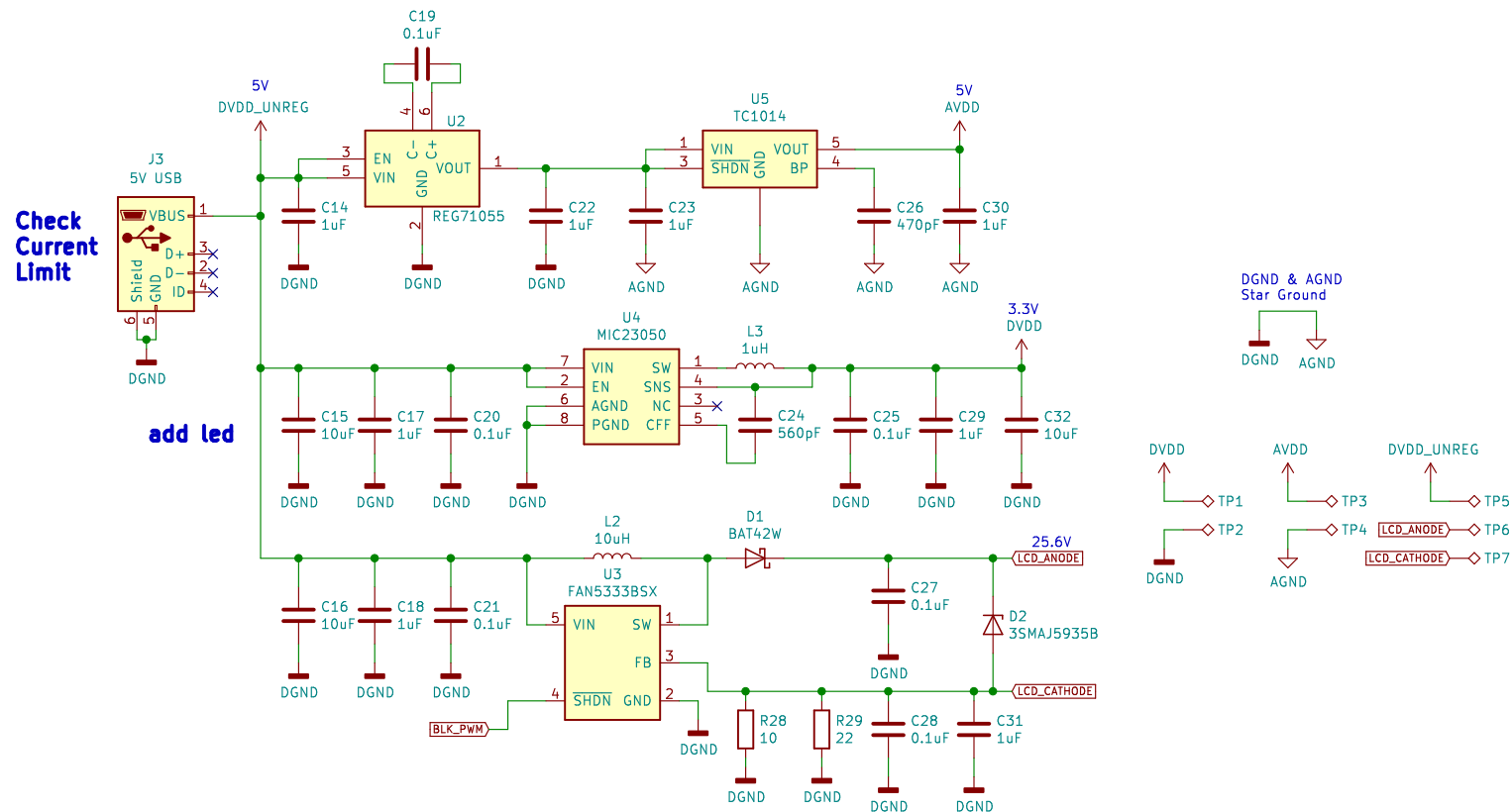
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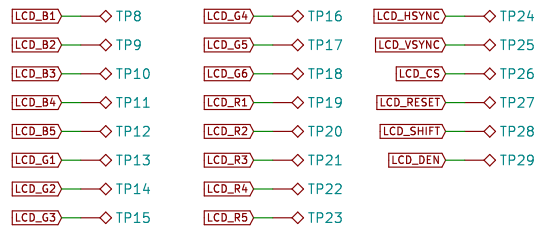
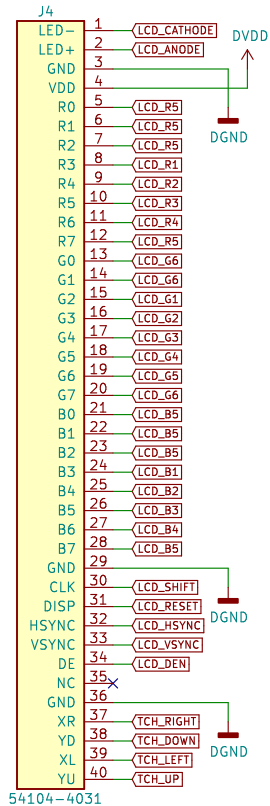
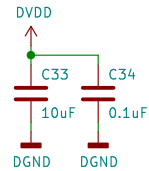
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Digital & Analog Power Supply

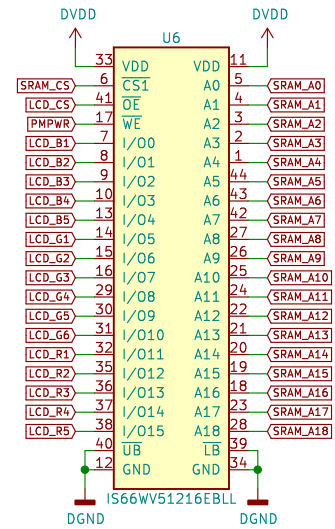
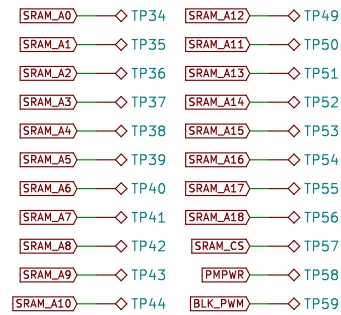
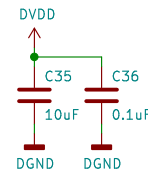


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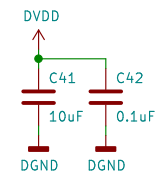
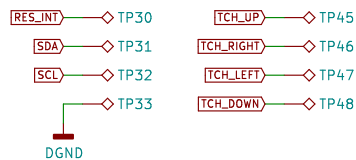
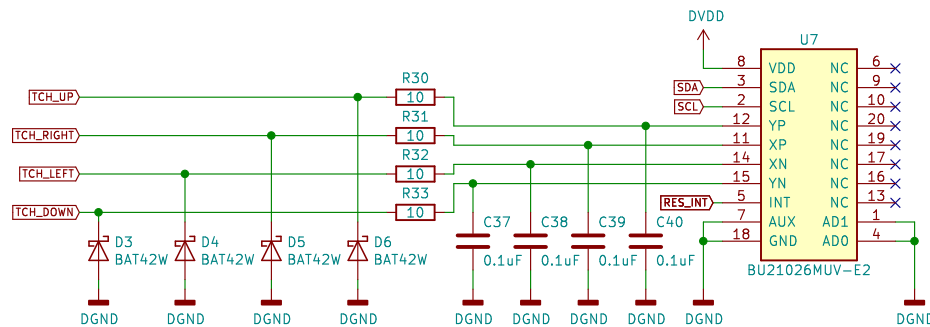
TFT LCD Header



Asynchronous SRAM



Resistive Touch Driver



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[illegible]

Pinout diagram for the J5 Terminal Block:

Pin Number	Function
1	A5C_VDD
2	A5C_GND
3	A5C_A1+
4	A5C_A1-
5	A5C_A1-
6	I2N_GND
7	I2N_VDD
8	I2N_IN
9	RD_VDD
10	RD_A1
11	RD_A2
12	DRV_BAT
13	DRV_GND
14	DRV_OUT1
15	DRV_OUT2

Legend:

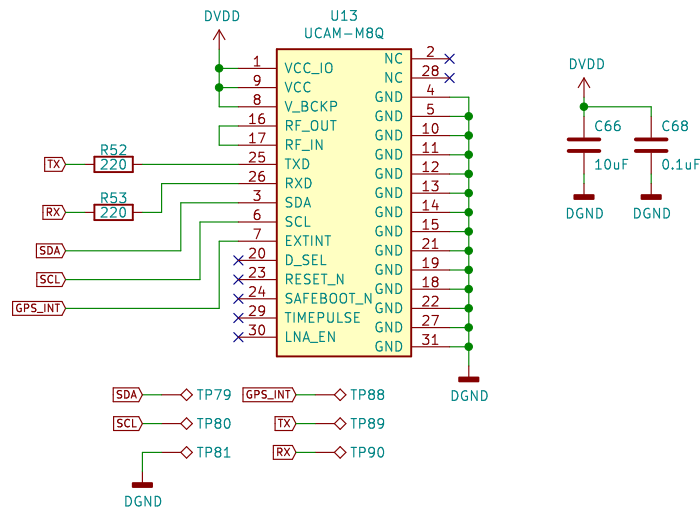
- 5V GND A1+ A1- A1- GND IN 5V GND A 8 12V GND 12V GND
- Load Cell POT Rot Encoder BAT Motor

J5 Terminal Block

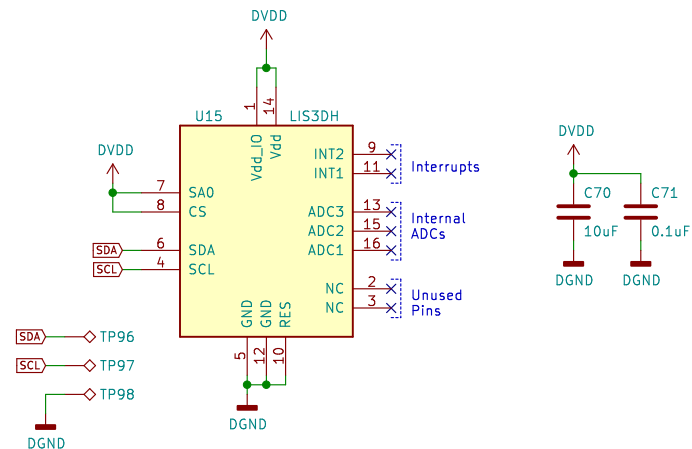
The diagram illustrates the power and signal connections for the LIN_VDD and LIN_IN pins. It shows two columns of components. The left column contains TP68 and TP69, both connected to LIN_VDD. The right column contains D7 and D8, both connected to LIN_VDD. LIN_IN is connected to the LIN_VDD line. The components are connected to DGND and DVDD.

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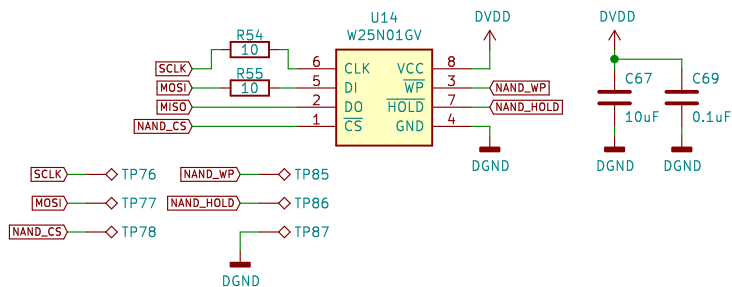
GPS Antenna and Reciever



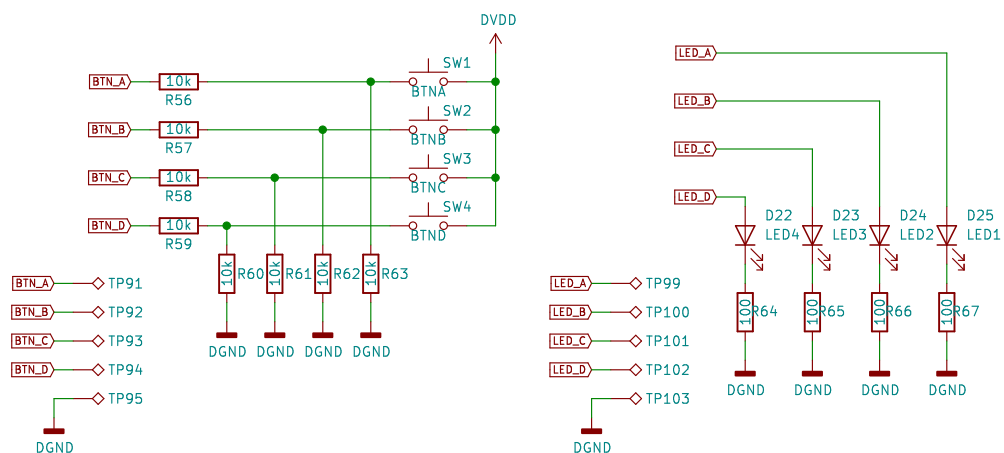
Three Axis Accelerometer



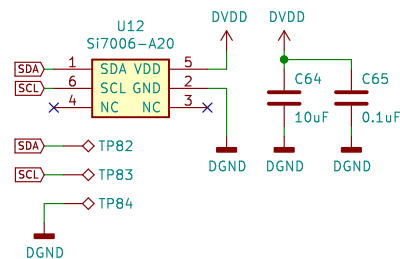
NAND Flash Memory



Debugging Buttons and LEDs



Temperature & Humidity Sensor



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