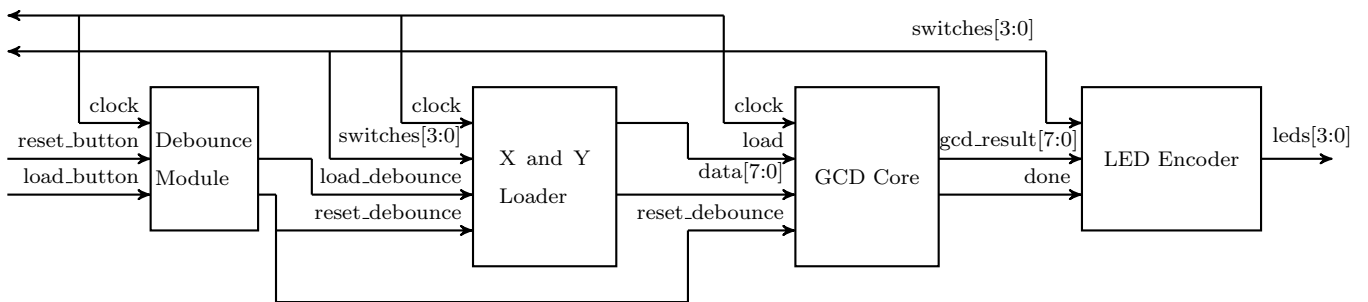# ECE 440 - Homework #4

Collin Heist

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1 Wrapper Module Block Diagram

In this diagram, the debounce module is a **SystemVeriog** module provided to us. The X and Y loader component is a synchronous block of logic that allows the user to piece-wise input X and Y, and then sends those values (along with the Load signal) to the GCD core. The done signal is then used, along with the results and the switches in the final LED Encoder block. This block is purely combinational.