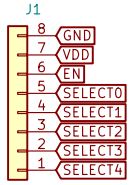
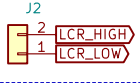


# Input Pins

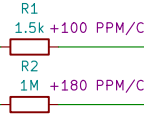


# Output Pins

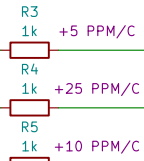


V<sub>dd</sub> needs to be 5V or 3.3V  
The multiplexer is disabled when EN is LOW

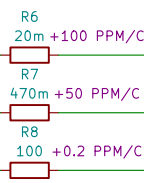
# Metal Element Resistors



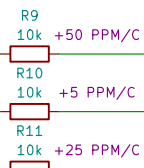
# Metal Film Resistors



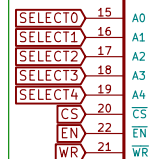
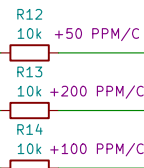
# Metal Foil Resistors



# Thin Film Resistors



# Thick Film Resistors



U1  
ADG732BSUZ

R<sub>on</sub> is 4.0Ω (typically)  
Deviation between channels is ±0.3Ω

# Aluminum-Polymer Capacitor



# Aluminum-Electrolytic Capacitor



# Ceramic Capacitors



# Supercapacitors



# Tantalum-Polymer



# Tantalum



Author: Collin Heist

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