Pin	Tabelle1			
	Function	ESP-8266 Pin	Shield	Comment
TX	TXD	TXD		Do not use
RX	RXD	RXD		Do not use
A0	Analog input, max 3.2V	A0		
D0	IO	GPIO16		Wake pin, connect to RST for Deep Sleep
D1	IO, SCL	GPIO5		I ² C, future use
D2	IO, SDA	GPIO4		I ² C, future use
D3	IO, 10k Pull-up	GPIO0	Button	
D4	IO. 10k Pull-up. BUILTIN LED	GPIO2	LED. DHT	

IO, 10k Pull-up, BUILT IIN_LED IO, SCK D5 GPIO14 SD D6 IO, MISO GPIO12 SD

SD

SD_CS

Check, differs between shields

GPIO13

GPIO15

GND

3.3V **RST**

D7

D8

G

5V

3V3

RST

IO, MOSI

Ground

5V

3.3V

Reset

IO, 10k Pull-down, SS