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Revision

Revision	Date & Author	ChangeLog
V1.0	2022/9/2, Richard	Initial version

Introduction

This document describes how to raise/close ticket by PDM. The ticket is used to track the design issue, meanwhile, it can also be used to reflect soc verification team's workload.

Typically, the ticket raise/close process should be:

1. Soc verification owner create a ticket, the generated ticket number prefix is "TKT", the ticket state is "Resolving"
2. Soc design owner get the ticket, after resolving the issue, design owner will handle the ticket, PDM system will automatically generate a ticket prefix with "DFT"(Soc verification don't need to care about this), and a ticket prefix with "RES" will be send back to soc verification owner
3. Soc verification owner get the ticket prefix with "RES", if the ticket was already fixed then close it, otherwise reject it back to Soc design owner

How to create a ticket

PDM site

<https://designdpm.nxp.com/>

Get BOM info

Chose the issue related BOM tag in pi.bom

```
/home/imxrt700_verif1/richard_nxa28190/V_SS_RT700_SOC_TB_1.15  
awv071676.cn-sha01.nxp.com:>q .pi_md/pi.bom  
awv071676.cn-sha01.nxp.com:>■
```

```

22 testbench/common_blocks/ips_vip
    sync://sync-15038:15038/Projects/sv_ip/ips_vip
    ips_vip.00.00.00.24
    -ppn
    sync://sync-15038:15038/Projects/sv_ip/apb_vip
    apb_vip.00.00.00.10
    -ppn

    sync://sync-14800:14800/Projects/tvip_blocks/mcu_tb_utils
    mcu_tb_utils.00.34.00.02
    -ppn V_SS_RT700_SOC_TB_1.15
    -pn MCU_TB_U

27
28 # Part SOC_imxrt700_CMOS28FDSOI_1.15
29 ICorSoC1-imxrt700
30 pkg: OSS-riscv-/20190524
31 pkg: gnu-gcc_arm_gnueabi-/4.3.3
32 pkg: arm-gcc-/6.2-2016g4
33 pkg: gnu-gcc-/4.1.2
34 pkg: gnu-srecrecord-/1.27
35 pkg: gnu-m4-/1.4
36 pkg: apache-xalan-/1.8.0
37 pkg: apache-xerces-/2.5.0
38 pkg: apache_da-redhawk-/2022R1.2
39 pkg: arm-ds5-/r5p0-19rel1
40 pkg: arm-ambadesigner-/r3p4-00rel0
41 pkg: arm-socrates-/1.3.3
42 pkg: moto-sd587-sray-/2004.10-1
43 pkg: cadence-mdv-/21.09.005

```

Search the tag in PDM

Name	Type	Version	State	Date Created	Date Modified
SOC_imxrt700_CMOS28FDSOI_1.15	ICorSoC	1.15	Development	2022年8月18日	2022年9月

Create ticket in PDM

Chose the selected tag, click “Created Issue”

Properties	Glide BOM	Versions	Compatible PDK Pr...	Classification	Committed Actions	News Bulletins	Access
SOC_imxrt700_CMOS28FDSOI_1.15 SOC_imxrt700_CMOS28FDSOI[1.15] ICorSoC (1.15)	To be filled in by Design State: Development Date Modified: 2022年9月2日 上午10時00分46秒	*Create Issue *Create FMEA					
SOC_imxrt700_CMOS28FDSOI...							
Ticket Rollup Views Where Used FMEA Routes History Lifecycle Meetings Attachments Bookmarks Subscribe Secure BOM Parts	Basics	Secure Status Type Name Version Master Title Version Notes Description Author Date Created Policy State Maturity Source QMS Link Lock CM/BOM BOM Modified Date	ICorSoC SOC_imxrt700_CMOS28FDSOI_1.15 1.15 SOC_imxrt700_CMOS28FDSOI SOC_imxrt700_CMOS28FDSOI[1.15] To be filled in by Design To be filled in by Design Bruce Shen (nxa08363) 2022年8月18日 下午10時47分24秒 ICorSoC Policy Development QMS Derived Open in QMS TRUE 2022年8月26日 下午07時48分53秒	Version Name 1.15			

Fill the information, then click “Done”, a new ticket with prefix “TKT” will be generated.

How to fill the information, please refer [Rules for create a ticket](#)

SOC_imxrt700_CMOS28FDSOI_1.15|Create New Issue |

Fields in red boxes are required.

Type	Issue
Secure	
Issue-Type	Potential Bug
Title	[imx.RT700DMA2 mem wlpd is not connect]
Assigned To	Bruce Shen (nua0563)
Description	
Severity	
Due Date	
Internal	No
Reporting Organization	NXP
Reported Against	SOC_imxrt700_CMOS28FDSOI_1.15
Found On	SOC_imxrt700_CMOS28FDSOI_1.15
Method Of Discovery	
Test Type	
Found By Test	
Verification Check Category	

How to Reproduce

Search within Owning Team

User CC (IPDM User Name):

Entered By User: Richard Xiong (nua28190)

Done Cancel

Rules for create a ticket

- Title

Ticket title should be easy to get what the issue is, the title content:

[Platform]Module: <Bug Summary> possibility

- 1) Platform: MX6DQ_SD, MX6DL_SD, etc
- 2) Module: DMA, UART, etc
- 3) Bug Summary: The info should be simple and describe the bug as clearly as we can;
- 4) Possibility: The frequency of the issue occurs.

Eg: [imx.RT700]DMA2 tcd_mem wlpd is not connect, will cause access TCD register failed. 100%

- Description

- 1) BUG DETAIL DESCRIPTION:

Describe this bug clearly, and you can also add partial log.

- 2) REPRODUCE STEPS:

1.

2.

...

Describe the steps to reproduce this bug.

Please include all necessary conditions, such as test environment, testbench version

- 3) EXPECTED RESULTS:

Normal test results or expected case results

- 4) POSSIBILITY: %

- Affects Versions

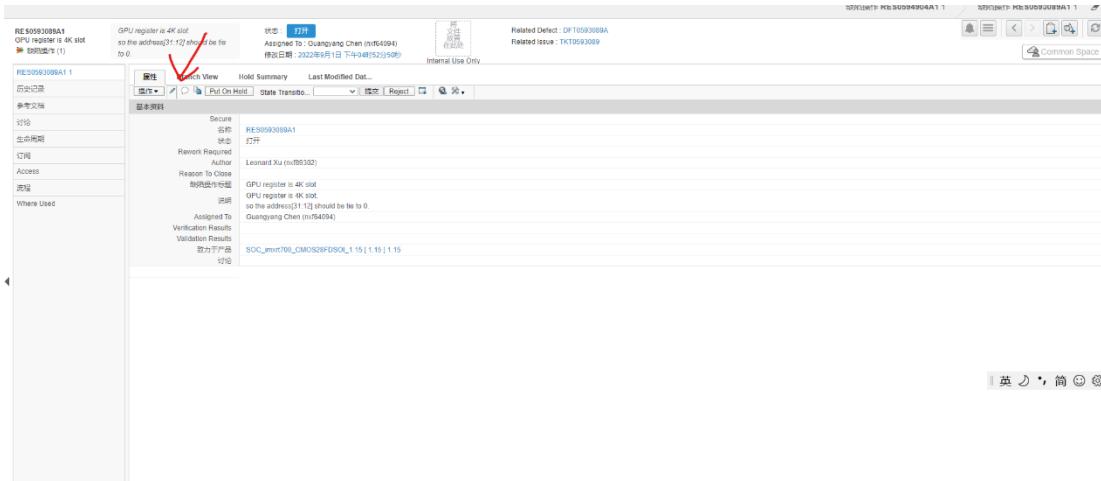
Testbench version, RTL version , etc

- Regression From Version

If this is regression bug please specify previous tested version.

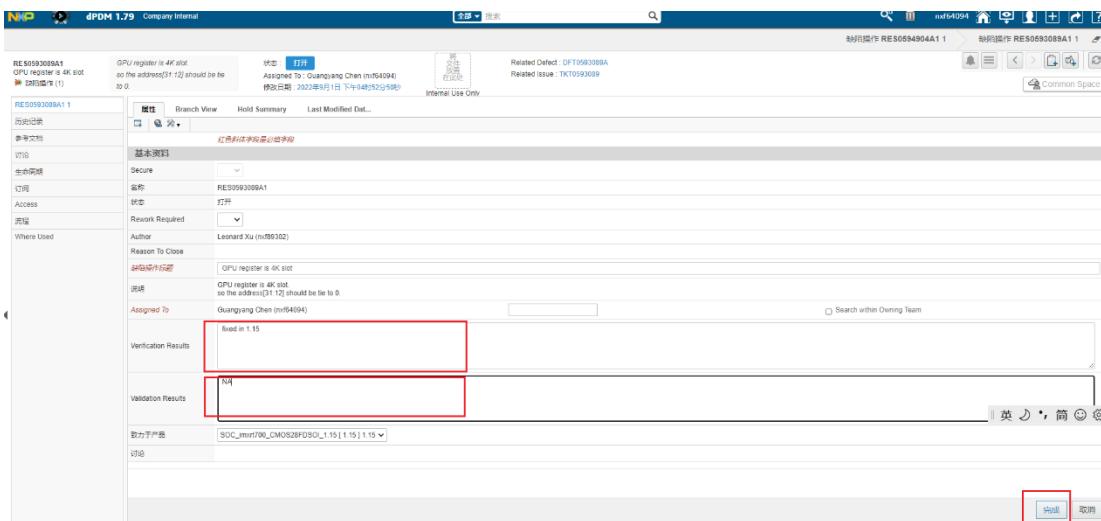
How to close/Reject a ticket

Chose the ticket, click the pen symbol

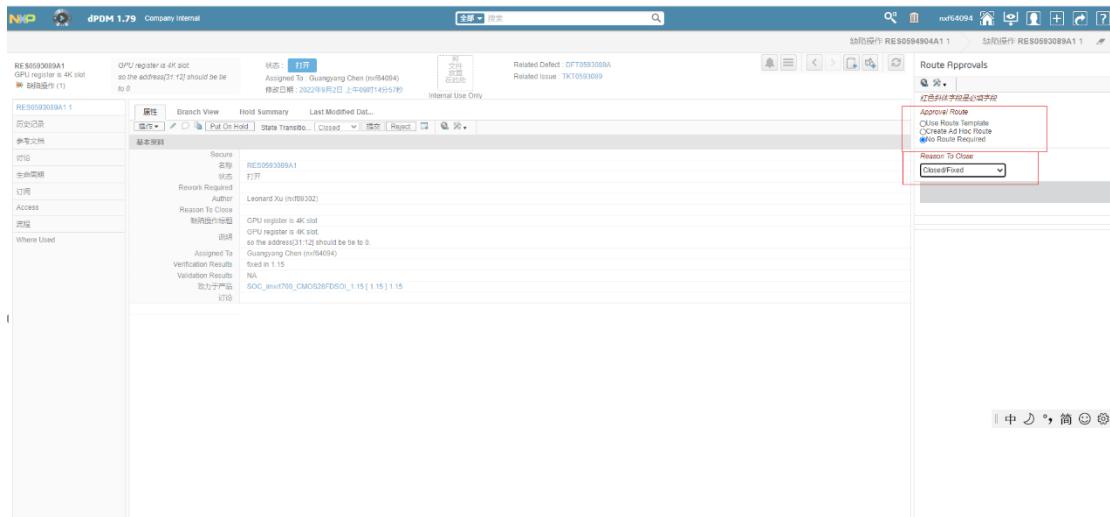


Fill in some information, then click “Done”

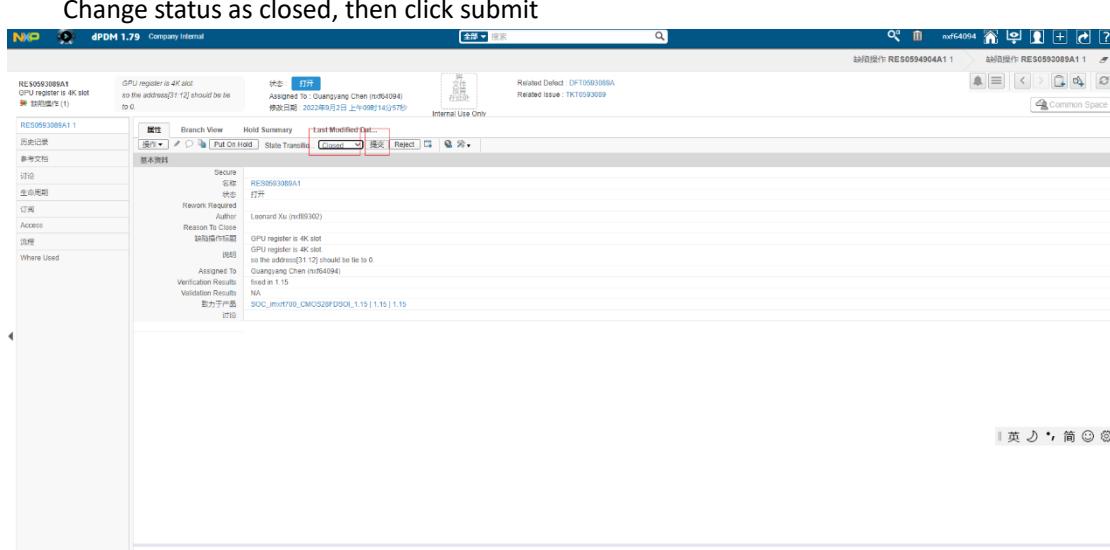
- If want to close a ticket, write information such as “fixed on version V1.15”,
- If want to reject a ticket, write information such as “issues not fixed/new issues included”



Chose a reason about why to close this ticket

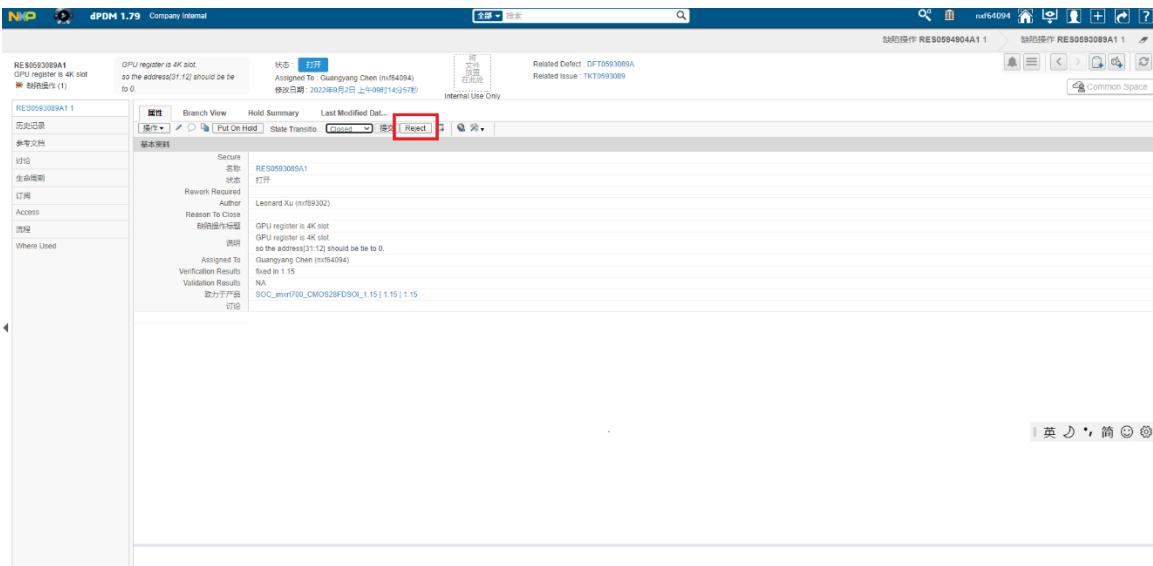


Closed a ticket



Reject a ticket

No need to trans the ticket state, just click “Reject”

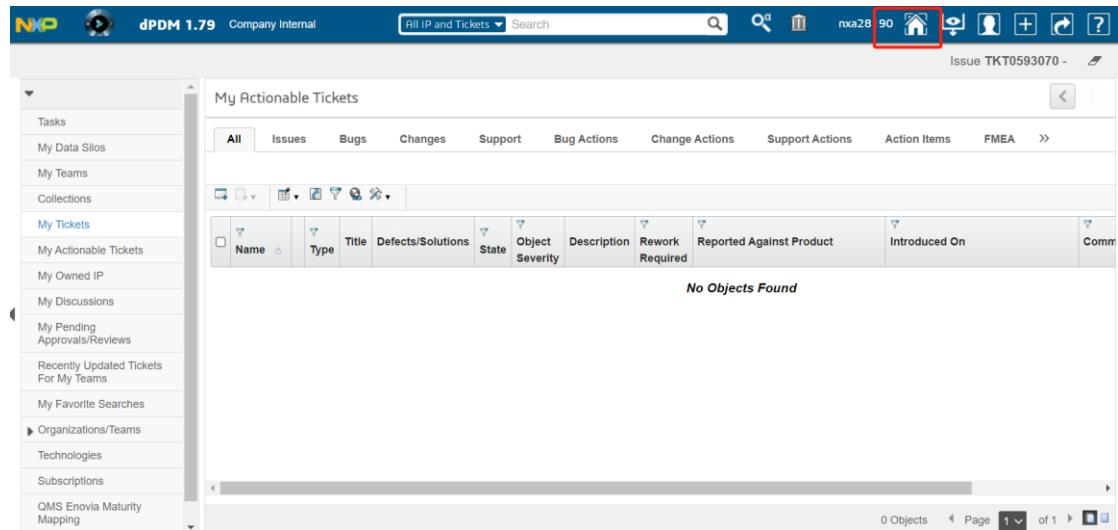


Useful tips

Check all the ticket

Ticket need to be validated

Click the House symbol to check the ticket need to be handled by soc verification owner.
The ticket number prefix should be "RES"



Ticket still in process

Click “My ticket” to check all the ticket still in process

Name	Type	Revision	Defects/Solutions	State	Title	Description	Issue-Type	CC List	Non User CC List	Originated	Severity	Internal
TKT0593070	Issue	-	1/0	Resolving	DMA2 register access failed.Du	Potential Bug	nxa13449,nxf59190	2022年7月22日上午11时22分	Moderate	No	23秒	0

Check the ticket info

Enter 1 ticket, click the “Issue tree view button”

- State transition
Means the ticket status
- Assigned to
Means who is handling this ticket now

Name	State Transition	Rework Required	Defects/Solutions	Type	Issue-Type	Defect
TKT0593070	Resolving	Yes		Issue	Potential...	
DFT0593070A	Solving	Yes		Defect	Bug	
RES0593070A1	Verifying	Yes		Defect A...		

The screenshot shows the dPDM 1.79 interface with the following details:

- Header:** NXP, dPDM 1.79 Company Internal, All IP and Tickets SOC_imxrt700_CMOS28FDSOI, nx28190, Common Space.
- Ticket Details:** TKT0593070, DMA2 register access failed, State: Resolving, Assigned To: Bruce Shen (nx08363), Date Modified: 2022年9月1日 下午05時37分29秒.
- Issue Tree View:** A tree view showing TKT0593070, DFT0593070A, and RES0593070A1. The RES0593070A1 node is expanded.
- Table Headers:** Properties, Issue Tree View, Hold Summary, Last Modified Date..., Classification.
- Table Data:** A table listing issues with columns: Name, Due Date, Author, Entered For User, Assigned To, Resolved By, Solution, Owning Team, and Reported Against. The 'Assigned To' column for all three rows is highlighted with a red box.
- Bottom:** Filter: Issue Tree View, 3 objects.

Reassign the soc owner

If you assign the ticket to un-expected soc designer, below steps shows how to re-assign the ticket to the correct person.

The screenshot shows the dPDM 1.79 ticket edit screen for TKT0593089:

- Header:** TKT0593089, GPU register is 4K slot, State: Resolving, Assigned To: Bruce Shen (nx08363), Date Modified: August 2, 2022 4:01:56 PM GMT+08:00.
- Properties Tab:** Secure, Name: TKT0593089, State: Resolving, Author: Guangyang Chen (nx64094). The 'Assigned To' field is highlighted with a red box and set to Bruce Shen (nx08363).
- Issue View Tab:** Description: GPU register is 4K slot so the address[31:12] should be tie to 0. Severity: Moderate, Due Date: July 31, 2022 9:00:00 PM GMT+08:00. Originated Date: July 22, 2022 3:36:19 PM GMT+08:00. Entered For User: Guangyang Chen (nx64094). Modified Date: August 2, 2022 4:01:56 PM GMT+08:00. Last State Change: August 2, 2022 4:01:56 PM GMT+08:00. Issue-Type: Potential Bug/Change. Subscribers: None. Discussions: None. How to Reproduce: testbench top media_vdd.media_ahb_matrix_haddr_p1[31:12]. Found On: SOC_imxrt700_CMOS28FDSOI_1.9. Reporting Organization: NXP.
- Classification Tab:** Classification Path: Issue Object Classifications → Issue Detection Discovery. Method Of Discovery: Verification - Logic. Test Type: Direct. Found By Test: simple pattern. Verification Check Category: Timeout.

State : **Resolving**
 Assigned To : Bruce Shen (nx08363)
 Date Modified : August 2, 2022 4:01:56 PM GMT+08:00

Related Solution : SOLO593089A
Drag and Drop here
Internal Use Only

Properties		Issue Tree View	Hold Summary	Last Modified Dat...	Classification
<i>Fields in red italics are required</i>					
<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>					
Secure	<input type="button" value="..."/>				
Name	TKT0593089	Internal	<input type="button" value="..."/>		
State	Resolving	Author	Guangyang Chen (nx164094)		
Rework Required					
Reason To Close					
Type	<input checked="" type="checkbox"/> ISSUE				
Assigned To	Bruce Shen (nx08363)		<input type="button" value="..."/>		
Title	GPU register is 4K slot				
Description	GPU register is 4K slot. so the address[31:12] should be tie to 0.				
Owning Team	MICR SoC Design JV				
Severity	Moderate <input type="button" value="..."/>	User CC (dPDM User Name)	<input type="button" value="..."/>	<input type="button" value="..."/>	
Due Date	July 31, 2022 <input type="button" value="..."/>				
Originated Date	July 22, 2022 3:38:19 PM GMT+08:00	Modified Date	August 2, 2022 4:01:56 PM GMT+08:00		
Entered For User	Guangyang Chen (nx164094)	Last State Change	August 2, 2022 4:01:56 PM GMT+08:00		
Issue-Type	Potential Bug/Change <input type="button" value="..."/>	Descendant On Hold			
Subscribers	<input type="button" value="..."/>	<input type="button" value="..."/>			
Discussions					
How to Reproduce	testbench top.media_vdd2.media_aht_matrix_haddr_p[31:12]				