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# Revision

Revision	Date & Author	ChangeLog
V1.0	2022/9/2, Richard	Initial version

# Introduction

This document describes how to raise/close ticket by PDM. The ticket is used to track the design issue, meanwhile, it can also be used to reflect soc verification team's workload.

Typically, the ticket raise/close process should be:

1. Soc verification owner create a ticket, the generated ticket number prefix is "TKT", the ticket state is "Resolving"
2. Soc design owner get the ticket, after resolving the issue, design owner will handle the ticket, PDM system will automatically generate a ticket prefix with "DFT"(Soc verification don't need to care about this), and a ticket prefix with "RES" will be send back to soc verification owner
3. Soc verification owner get the ticket prefix with "RES", if the ticket was already fixed then close it, otherwise reject it back to Soc design owner

## How to create a ticket

### PDM site

<https://designpdm.nxp.com/>

### Get BOM info

Chose the issue related BOM tag in pi.bom

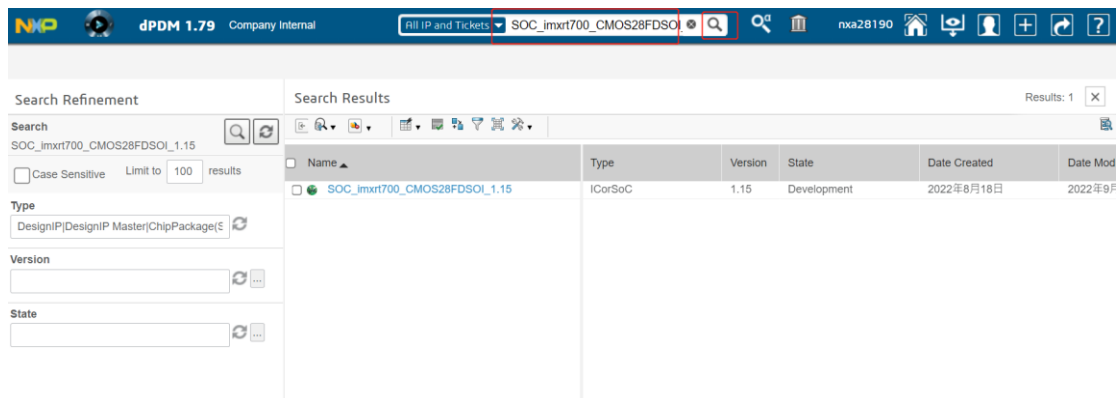
```
/home/imxrt700_verif1/richard_nxa28190/V_SS_RT700_SOC_TB_1.15  
awv071676.cn-sha01.nxp.com:>q .pi_md/pi.bom  
awv071676.cn-sha01.nxp.com:>█
```

```

22 testbench/common_blocks/ips_vip          sync:/sync-15038:15038/Projects/sv_ip/ips_vip
00_S0G_TB_1.15                             ips_vip.00.00.24          -pn V_SS_RT7
23 testbench/common_blocks/v_ip_apb        sync:/sync-15038:15038/Projects/sv_ip/apb_vip
00_S0G_TB_1.15                             apb_vip.00.00.10         -pn V_SS_RT7
24
25 # Part MCU_TB_UTILS_1.60
26 testbench/common_blocks/mcu_tb_utils     sync:/sync-14800:14800/Projects/tvip_blocks/mcu_tb_utils
TIIS_1.60                                  mcu_tb_utils.00.34.00.02 -pn MCU_TB_U
27
28 # Part S0G_imxrt700_CMOS28FDSOI_1.15
29 dm_processor=imxrt700
30 pkg: OSS-riscv-/20190524
31 pkg: gnu-gcc_arm_gnueabi-/4.3.3
32 pkg: arm-gcc-/6.2-2016q4
33 pkg: gnu-gcc-/4.1.2
34 pkg: gnu-record-/1.27
35 pkg: gnu-m4-/1.4
36 pkg: apache-xalan-/1.8.0
37 pkg: apache-xercesc-/2.5.0
38 pkg: apache_da-redhawk-/2022R1.2
39 pkg: arm-ds5-/r5p0-19rel1
40 pkg: arm-ambadesigner-/r3p4-00rel0
41 pkg: arm-socrates-/1.3.3
42 pkg: moto-ad587-sray-/2004.10-1
43 pkg: cadence-mdv-/21.09.005

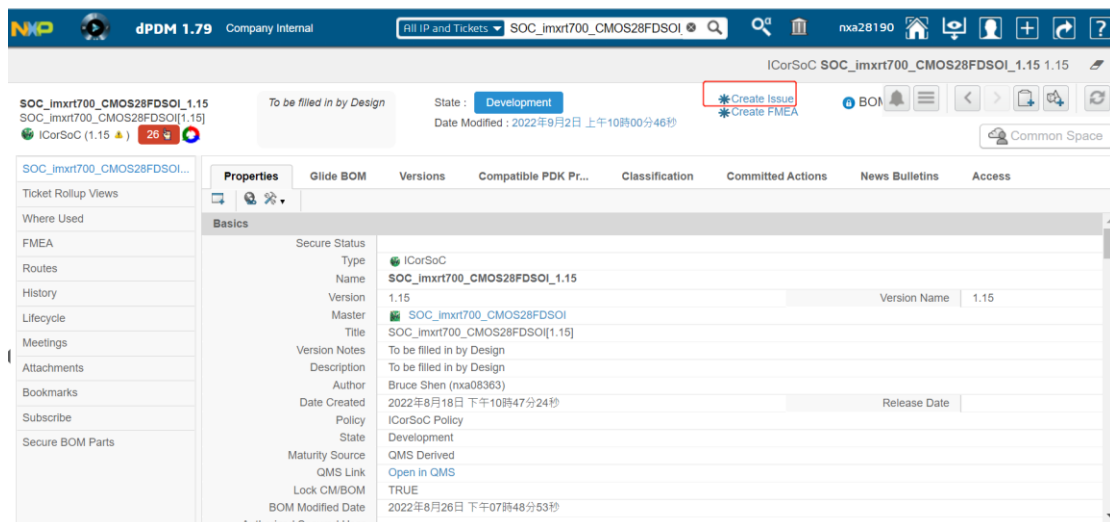
```

Search the tag in PDM



## Create ticket in PDM

Chose the selected tag, click “Created Issue”



Fill the information, then click “Done”, a new ticket with prefix “TKT” will be generated.  
How to fill the information, please refer [Rules for create a ticket](#)

SOC\_invert700\_CM0528FD5DL\_1.15[Create New Issue]

Fields in red italics are required

Type: Issue

Secure: ☐

Issue-Type: Potential Bug

Title: [imx-RT700]DMA2 mem wlpd is not con

Assigned To: Bruce Shen (ma28193)

Search within Owning Team: ☐

Description:

How to Reproduce:

Severity:

Due Date:

User CC (JPEM User Name):

Internal: No

Entered For User: Richard Kiang (ma28193)

Reporting Organization: NXP

Reported Against: SOC\_invert700\_CM0528FD5DL\_1.15

Found On: SOC\_invert700\_CM0528FD5DL\_1.15

Method Of Discovery:

Test Type:

Found By Test:

Verification Check Category:

Done Cancel

## Rules for create a ticket

### - Title

Ticket title should be easy to get what the issue is, the title content:

[Platform]Module: <Bug Summary> possibility

1) Platform: MX6DQ\_SD, MX6DL\_SD, etc

2) Module: DMA, UART, etc

3) Bug Summary: The info should be simple and describe the bug as clearly as we can;

4) Possibility: The frequency of the issue occurs.

Eg: [imx.RT700]DMA2 tcd\_mem wlpd is not connect, will cause access TCD register failed. 100%

### - Description

1) BUG DETAIL DESCRIPTION:

Describe this bug clearly, and you can also add partial log.

2) REPRODUCE STEPS:

1.

2.

...

Describe the steps to reproduce this bug.

Please include all necessary conditions, such as test environment, testbench

version

3) EXPECTED RESULTS:

Normal test results or expected case results

4) POSSIBILITY: %

### - Affects Versions

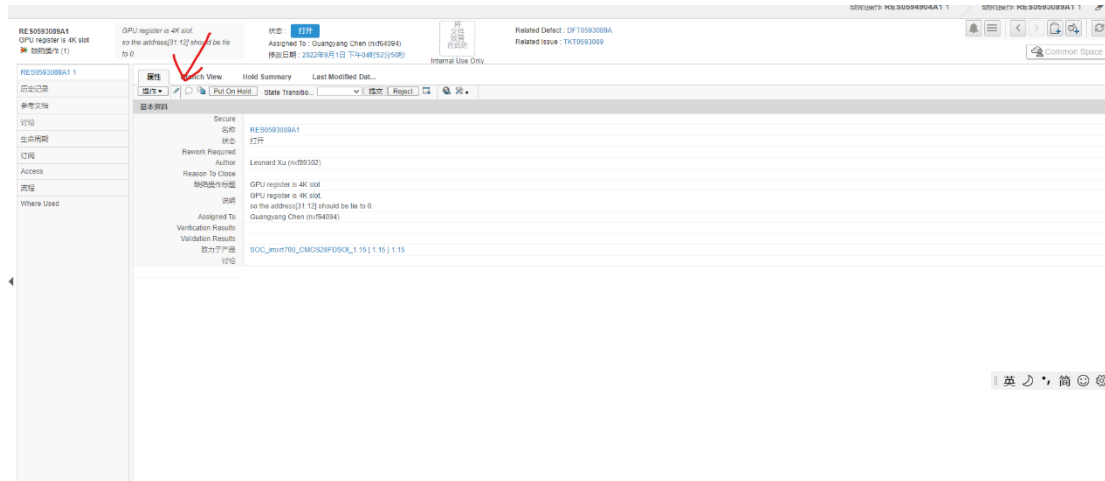
Testbench version, RTL version , etc

### - Regression From Version

If this is regression bug please specify previous tested version.

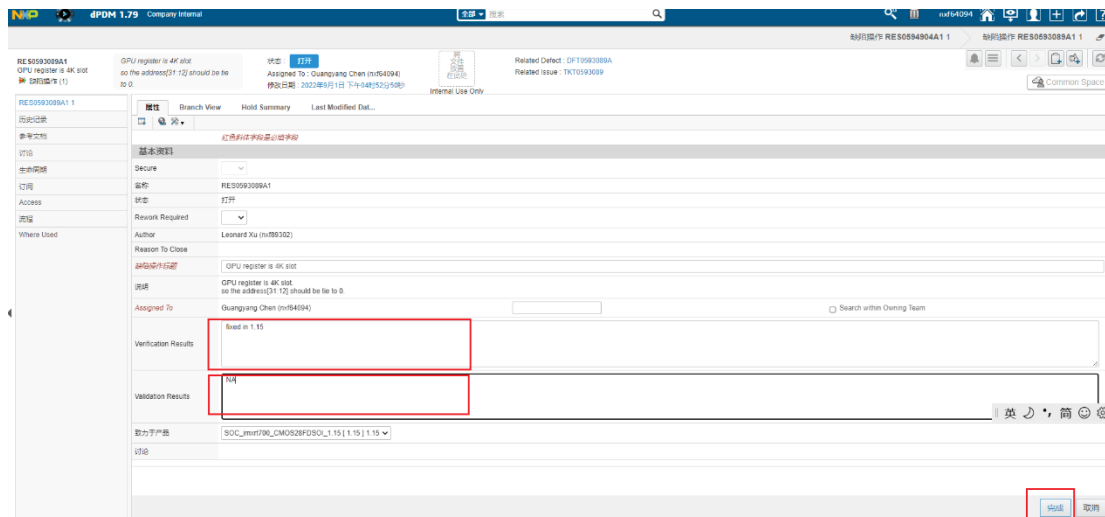
## How to close/Reject a ticket

Chose the ticket, click the pen symbol

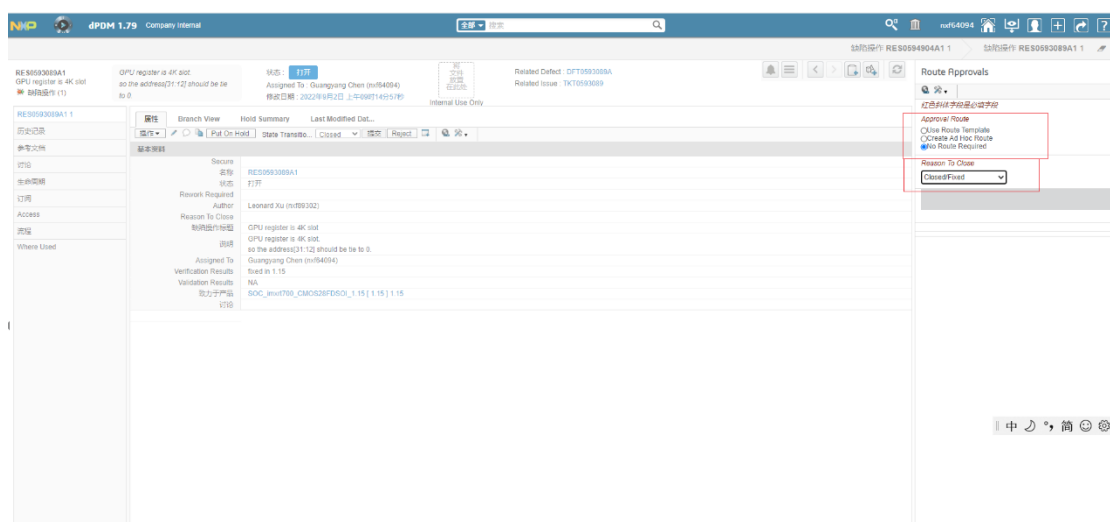


Fill in some information, then click “Done”

- If want to close a ticket, write information such as “fixed on version V1.15”,
- If want to reject a ticket, write information such as “issues not fixed/new issues included”

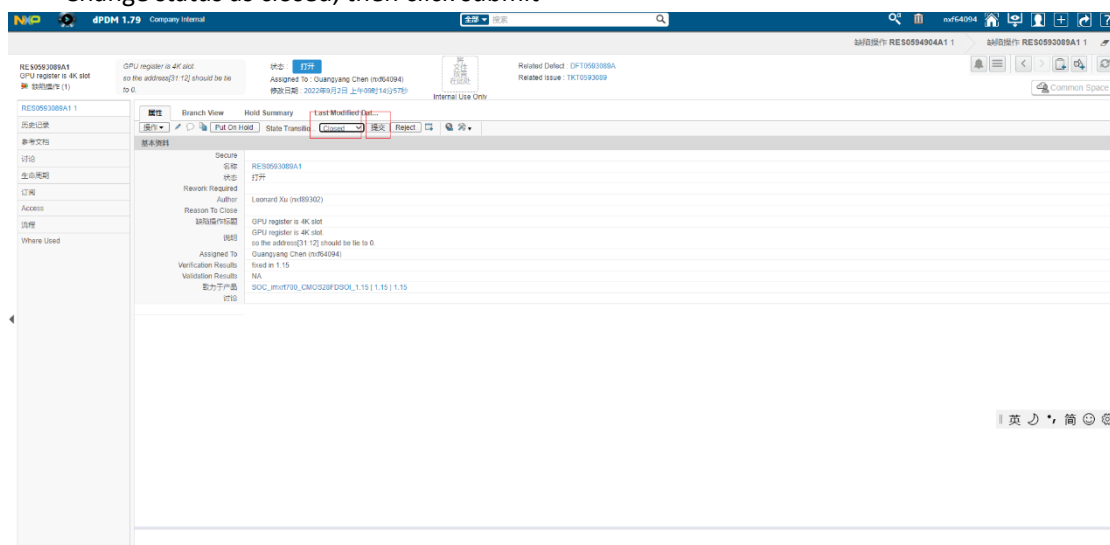


Chose a reason about why to close this ticket



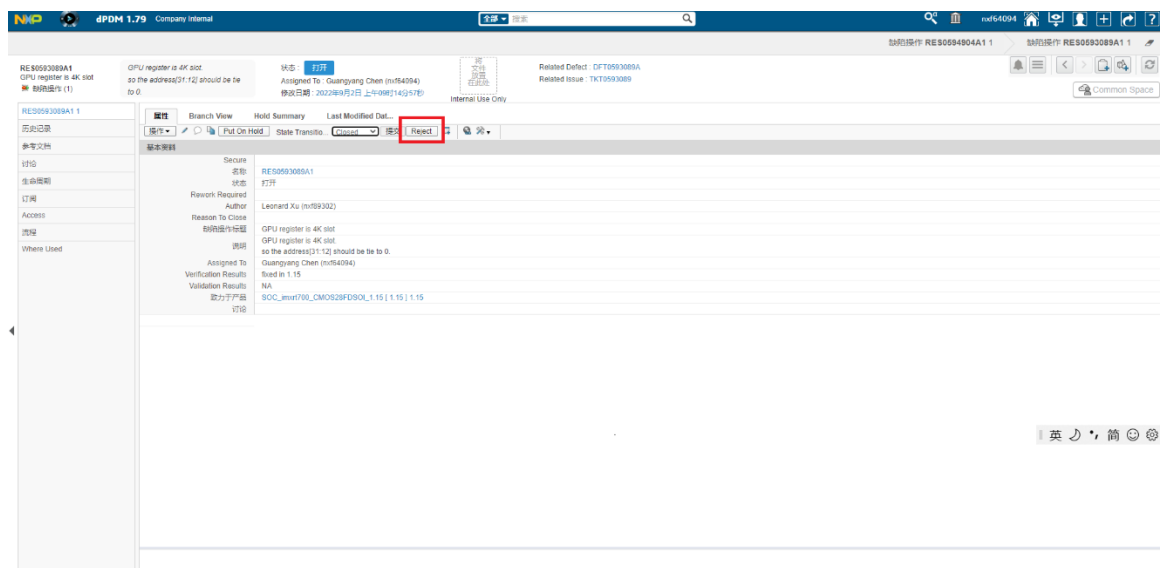
## Closed a ticket

Change status as closed, then click submit



## Reject a ticket

No need to trans the ticket state, just click “Reject”

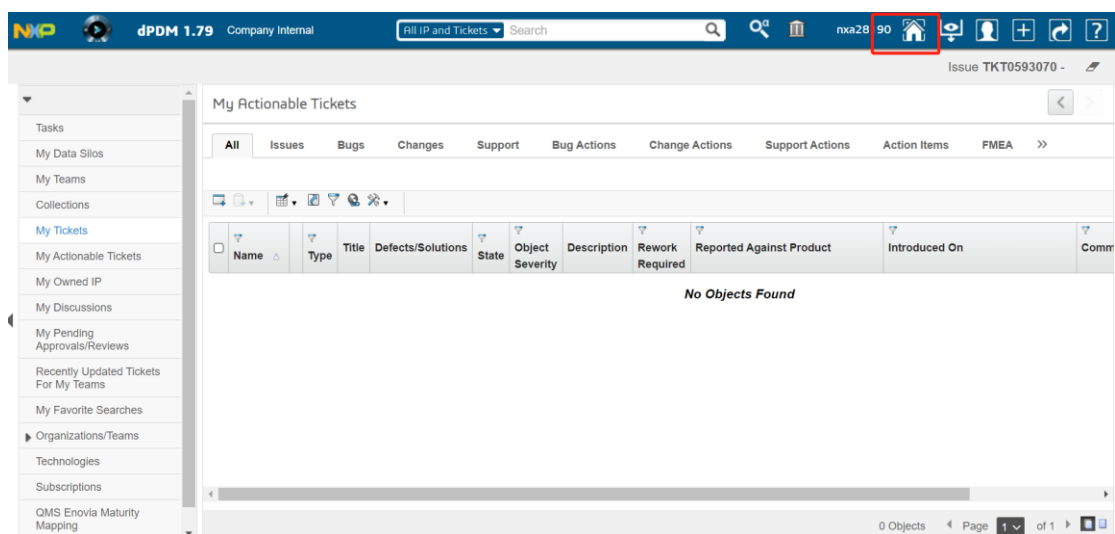


## Useful tips

### Check all the ticket

### Ticket need to be validated

Click the House symbol to check the ticket need to be handled by soc verification owner.  
The ticket number prefix should be "RES"





## Ticket still in process

Click “My ticket” to check all the ticket still in process

The screenshot shows the dPDM 1.79 interface. On the left sidebar, 'My Tickets' is highlighted with a red box. The main area displays 'My Originated Tickets' with a table of tickets. The table has columns: Name, Type, Revision, Defects/Solutions, State, Title, Description, Issue-Type, CC List, Non User CC List, Originated, Severity, Internal, and D. One ticket is listed: TKT0593070, Issue, 1/0, Resolving, DMA2 register access failed, DMA2 register access failed, Potential Bug, nxa13449,nxf59190, 2022年7月22日 上午11时22分23秒, Moderate, No.

## Check the ticket info

Enter 1 ticket, click the “Issue tree view button”

- State transition  
Means the ticket status
- Assigned to  
Means who is handling this ticket now

The screenshot shows the dPDM 1.79 interface for ticket TKT0593070. The 'Issue Tree View' tab is selected and highlighted with a red box. The table shows the state transitions for the ticket:

Name	State Transition	Rework Required	Defects/Solutions	Type	Issue-Type	Defect
TKT0593070	Resolving	Yes		Issue	Potential...	
DFT0593070A	Solving	Yes		Defect		Bug
RES0593070A1	Verifying	Yes		Defect A...		

TKT0593070  
DMA2 register access failed  
Issue

State: **Resolving**  
Assigned To: Bruce Shen (nxa08363)  
Date Modified: 2022年9月1日 下午05时37分29秒

Internal Use Only

Drag and Drop files here

Rework Required

Common Space

TKT0593070

Attachments

Reported Against

Resolved By

Routes

Discussions

Lifecycle

History

Subscribe

Solution Management

Access

FMEA

Impact Analysis

Sync With Collabnet

IDR Report

Duplicates

Properties

Issue Tree View

Hold Summary

Last Modified Dat...

Classification

Name	Due Date	Author	Entered For User	Assigned To	Resolved By	Solution	Owning Team	Reported Against
TKT0593070	2022年7月...	Richard ...	Richard Xio...	Bruce S...			Team MIC...	ICorSoC SOC_im
DFT0593070A		Bruce S...		Bruce S...				ICorSoC SOC_im
RES0593070A1		Bruce S...		Bruce S...				

Filter: Issue Tree View

3 objects

## Reassign the soc owner

If you assign the ticket to un-expected soc designer, below steps shows how to re-assign the ticket to the correct person.

TKT0593089  
GPU register is 4K slot  
Issue

State: **Resolving**  
Assigned To: Bruce Shen (nxa08363)  
Date Modified: August 2, 2022 4:01:56 PM GMT+08:00

Related Solution: SOL0593089A

Internal Use Only

Drag and Drop files here

TKT0593089

Attachments

Reported Against

Resolved By

Routes

Discussions

Lifecycle

History

Subscribe

Solution Management

Access

FMEA

Impact Analysis

Sync With Collabnet

IDR Report

Duplicates

Properties

Issue Tree View

Hold Summary

Last Modified Dat...

Classification

Put On Hold

State Transito...

Submit

Secure

Name

State

Author

Rework Required

Reason To Close

Type

Title

Description

Severity

Due Date

Originated Date

Entered For User

Modified Date

Last State Change

Issue-Type

Subscribers

Discussions

How to Reproduce

Found On

Reporting Organization

Classification Path

Issue Detection Discovery

Method Of Discovery

Test Type

Found By Test

Verification Check Category

Assigned To

Bruce Shen (nxa08363)

Owning Team

MICR SoC Design JV

User CC (dPDM User Name)

Descendant On Hold

Internal

No

GPU register is 4K slot.

so the address[31:12] should be tie to 0.

Moderate

July 31, 2022 9:00:00 PM GMT+08:00

July 22, 2022 3:36:19 PM GMT+08:00

Guangyang Chen (nxf64094)

August 2, 2022 4:01:56 PM GMT+08:00

August 2, 2022 4:01:56 PM GMT+08:00

Potential Bug/Change

testbench.top.media\_vd2.media\_ahb\_matrix\_haddr\_p1[31:12]

SOC\_imxrt700\_CMOS28FDSOI\_1\_9

NXP

Issue Object Classifications → Issue Detection Discovery

Verification - Logic

Direct

simple pattern

Timeout

State

Resolving

Assigned To

Bruce Shen (nra08363)

Date Modified

August 2, 2022 4:01:56 PM GMT+08:00

Related Solution

SOL0593089A

Drag and Drop files here

Internal Use Only

Properties

Issue Tree View

Hold Summary

Last Modified Dat...

Classification

Fields in red italics are required

Secure

Name

TKT0593089

Internal

No

State

Resolving

Author

Guangyang Chen (mrf54094)

Rework Required

Reason To Close

Type

Issue

Assigned To

Bruce Shen (nra08363)

Search within Owning Team

Title

GPU register is 4K slot

Description

GPU register is 4K slot so the address[31:12] should be tie to 0.

Owning Team

MICR SoC Design JV

Severity

Moderate

User CC (dPDM User Name)

Due Date

July 31, 2022

Originated Date

July 22, 2022 3:36:19 PM GMT+08:00

Modified Date

August 2, 2022 4:01:56 PM GMT+08:00

Entered For User

Guangyang Chen (mrf5409)

Last State Change

August 2, 2022 4:01:56 PM GMT+08:00

Issue-Type

Potential Bug/Change

Descendant On Hold

Subscribers

Discussions

How to Reproduce

testbench.top.media\_vdd2.media\_ahb\_mstr/c\_haddr\_p1[31:12]