# **NEC**

# NPN SILICON TRANSISTOR 2SC2785

**DESCRIPTION** 

The 2SC2785 is designed for use in driver stage of AF amplifier and low speed switching.

iow speed switching

**FEATURES** 

High Voltage

VCEO: 50 V MIN.

Excellent h<sub>FE</sub> Linearity

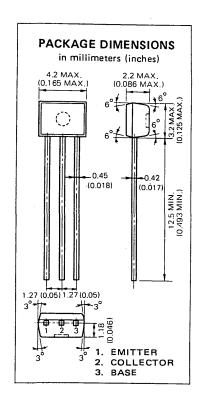
: 0.92 TYP.

 $h_{E1} (0.1 \text{ mA})/h_{E2} (1.0 \text{ mA})$ 

• Complementary to the NEC 2SA1175 PNP Transistor.

#### **ABSOLUTE MAXIMUM RATINGS**

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#### ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
h <sub>FE1</sub>	DC Current Gain	50	185			V <sub>CE</sub> =6.0 V, I <sub>C</sub> =0.1 mA
hFE2	DC Current Gain	110	200	600		V <sub>CE</sub> =6.0 V, I <sub>C</sub> =1.0 mA
NF	Noise Figure		8.0	15	∘dB	$V_{CE}$ =6.0 V, $I_{C}$ =0.1 mA, $R_{G}$ =2.0 k $\Omega$ ,
		•				f=1.0 kHz
fΤ	Gain Bandwidth Product	150	250	450	MHz	V <sub>CE</sub> =6.0 V, I <sub>E</sub> =-10 mA
Cob	Collector to Base Capacitance		3.0	4.0	pF	V <sub>CB</sub> =6.0 V, I <sub>E</sub> =0, f=1.0 MHz
СВО	Collector Cutoff Current			100	nA	V <sub>CB</sub> =60 V, I <sub>E</sub> =0
IEBO	Emitter Cutoff Current			100	nA	V <sub>EB</sub> =5.0 V, I <sub>C</sub> =0
VBE	Base to Emitter Voltage	0.55	0.62	0.65	V	V <sub>CE</sub> =6.0 V, I <sub>C</sub> =1.0 mA
V <sub>CE(sat)</sub>	Collector Saturation Voltage		0.15	0.3	V	I <sub>C</sub> =100 mA, I <sub>B</sub> =10 mA
V <sub>BE(sat)</sub>	Base Saturation Voltage		0.86	1.0	V	I <sub>C</sub> =100 mA, I <sub>B</sub> =10 mA

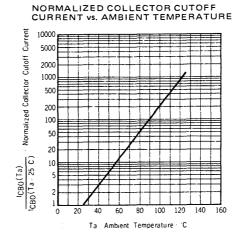
#### Classification of h<sub>FE2</sub>

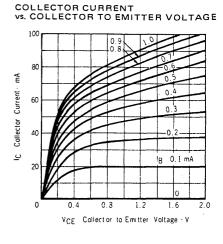
Rank	RF	JF	HF	FF	EF	KF ,
Range	110 – 180	135 – 220	170 — 270	200 — 320	250 — 400	300 – 600

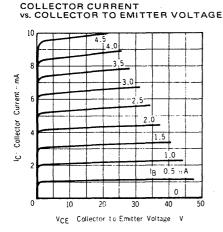
hFE2 Test Conditions : VCE=6.0 V, IC=1.0 mA

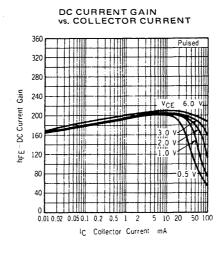
#### TYPICAL CHARACTERISTICS (Ta = 25 °C unless otherwise noted)

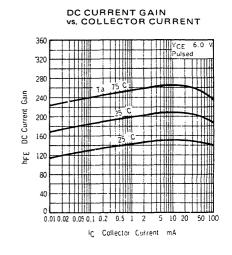
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE 300 Free Air 25 ΜW Dissipation 200 150 Power Total 100 50 0 25 50 75 100 125 Ta Ambient Temperature -- 'C

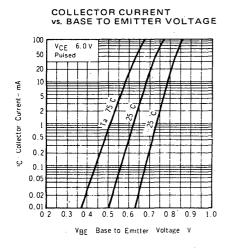


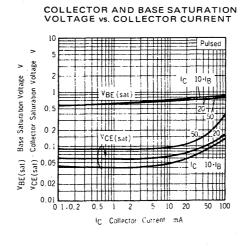


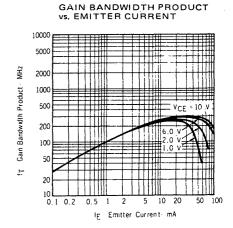




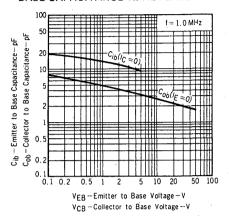




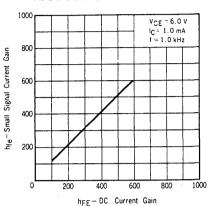




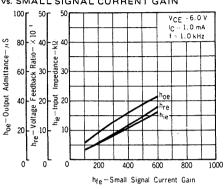
## EMITTER TO BASE AND COLLECTOR TO BASE CAPACITANCE vs. REVERSE VOLTAGE



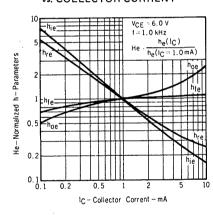
# SMALL SIGNAL CURRENT GAIN vs. DC CURRENT GAIN



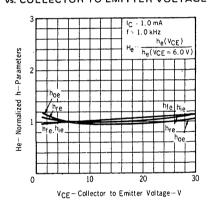
INPUT IMPEDANCE, VOLTAGE FEEDBACK RATIO AND OUTPUT ADMITTANCE vs. SMALL SIGNAL CURRENT GAIN



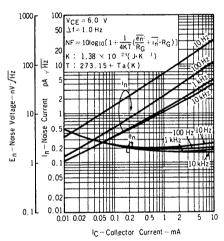
### NORMALIZED h-PARAMETERS vs. COLLECTOR CURRENT



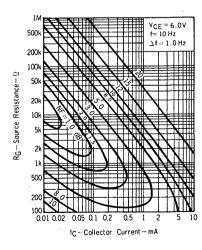
NORMALIZED h-PARAMETERS vs. COLLECTOR TO EMITTER VOLTAGE



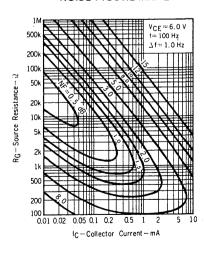
En AND In vs. COLLECTOR CURRENT



#### NOISE FIGURE MAP 1



#### NOISE FIGURE MAP 2



NOISE FIGURE MAP 3

