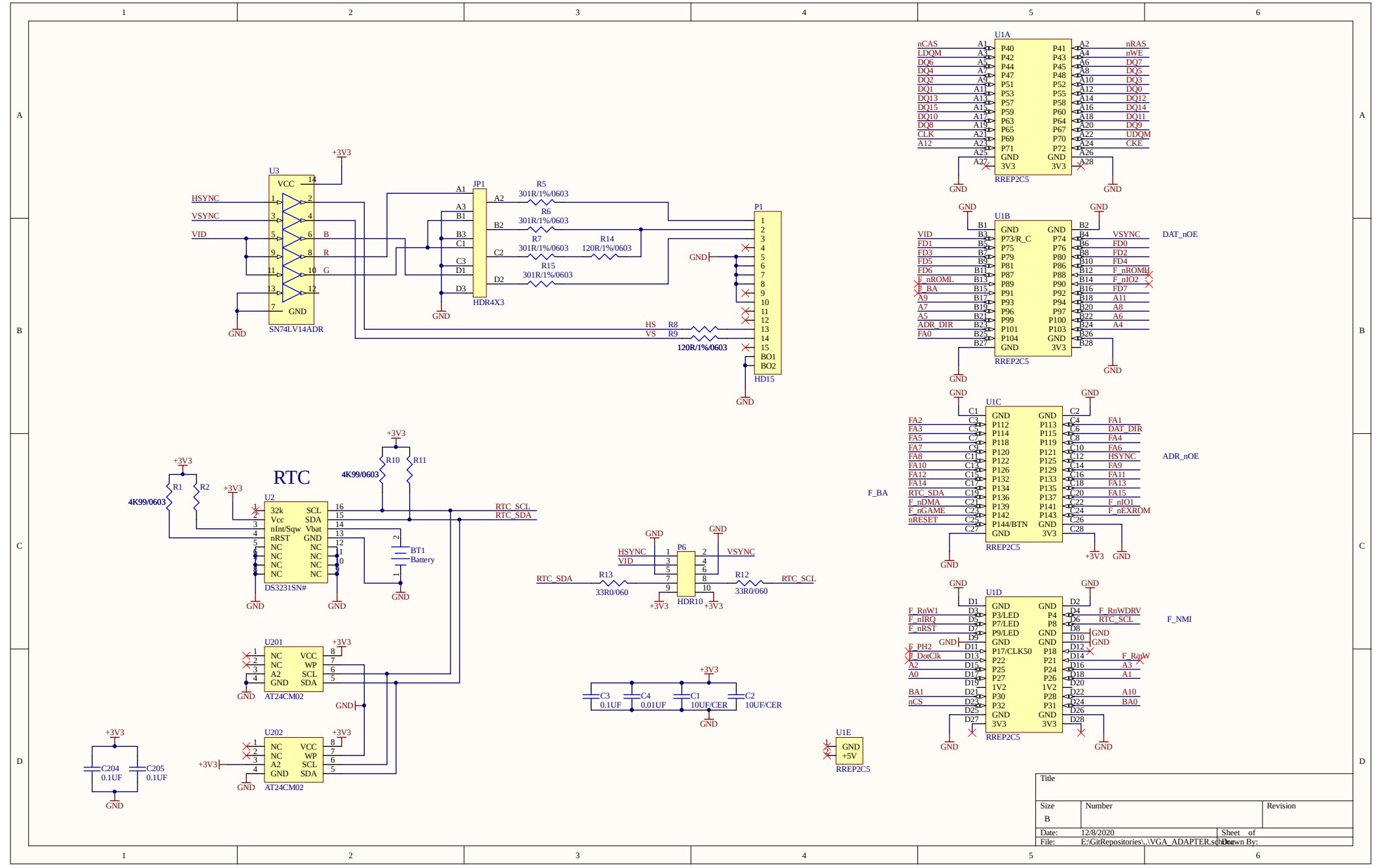
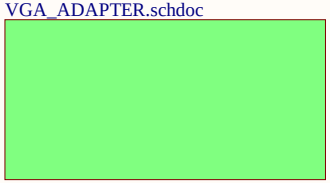
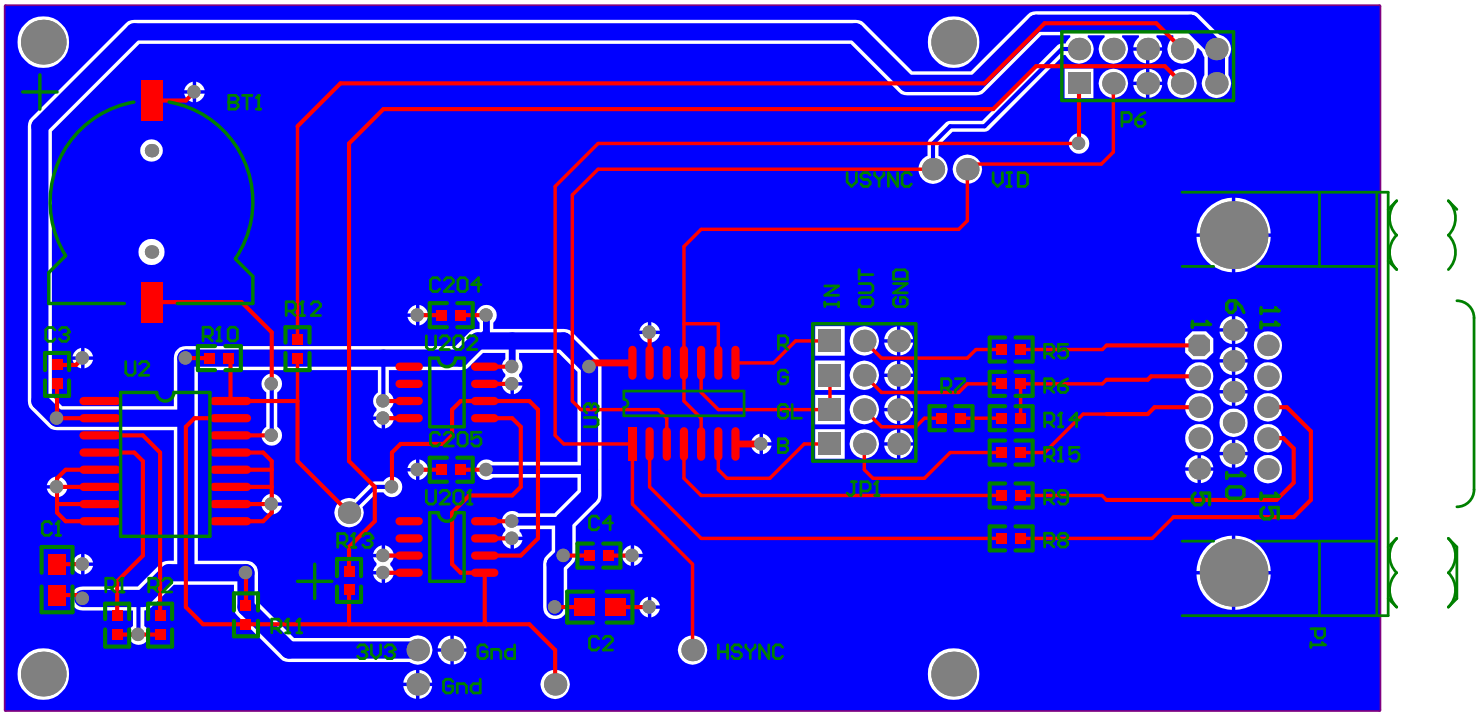
Comment	Description	Designator	Footprint	LibRef	Quantity
Battery	Battery Holder (Open) Coin, 12.0mm 1 Cell SMD (SMT) Tab	BT1	BC501SM	BC501SM-TR	1
10UF/CER	10µF ±10% 25V Ceramic Capacitor X7S 0805	C1, C2	0805	10UF/CER	2
0.1UF	CAP .10UF 16V CERAMIC X7R 0603	C3, C204, C205	0603	0.1UF	3
0.01UF	10000pF ±10% 10V Ceramic Capacitor X7R 0603 (1608 Metric)	C4	0603	0.01UF	1
HDR4X3		JP1	HDR4X3	HDR4X3	1
HD15	CONN DSUB HD RCPT 15POS R/A SLDR	P1	DB15FLC	HD15	1
HDR10		P6	HDR10	HDR10	1
4K99/0603	RES 4.99K OHM 1/10W 1% 0603 SMD	R1, R2, R10, R11	0603	4K99/0603	4
301R/1%/0603	301 Ohms ±1% 0.1W, 1/10W Chip Resistor 0603 (1608 Metric) Moisture Resistant Thick Film	R5, R6, R7, R15	0603	301R/1%/0603	4
120R/1%/0603	120 Ohms ±1% 0.1W, 1/10W Chip Resistor 0603 (1608 Metric) Moisture Resistant Thick Film	R8, R9, R14	0603	120R/1%/0603	3
33R0/060	33 Ohms ±1% 0.1W, 1/10W Chip Resistor 0603 (1608 Metric) Moisture Resistant Thick Film	R12, R13	0603	33R0/060	2
RREP2C5	RioRand EP2C5T144 Altera Cyclone II FPGA Mini Development Board	U1	RR2C5	RREP2C5	1
DS3231SN#	Real Time Clock (RTC) IC Clock/Calendar I²C, 2-Wire Serial 16-SOIC (0.295", 7.50mm Width)	U2	SOL16	DS3231SN#	1
SN74LV14ADR	Inverter IC 6 Channel Schmitt Trigger 14-SOIC	U3	SOP14	SN74LV14ADR	1
AT24CM02	EEPROM Memory IC 2Mb	U201, U202	SO-8	AT24CM02	2

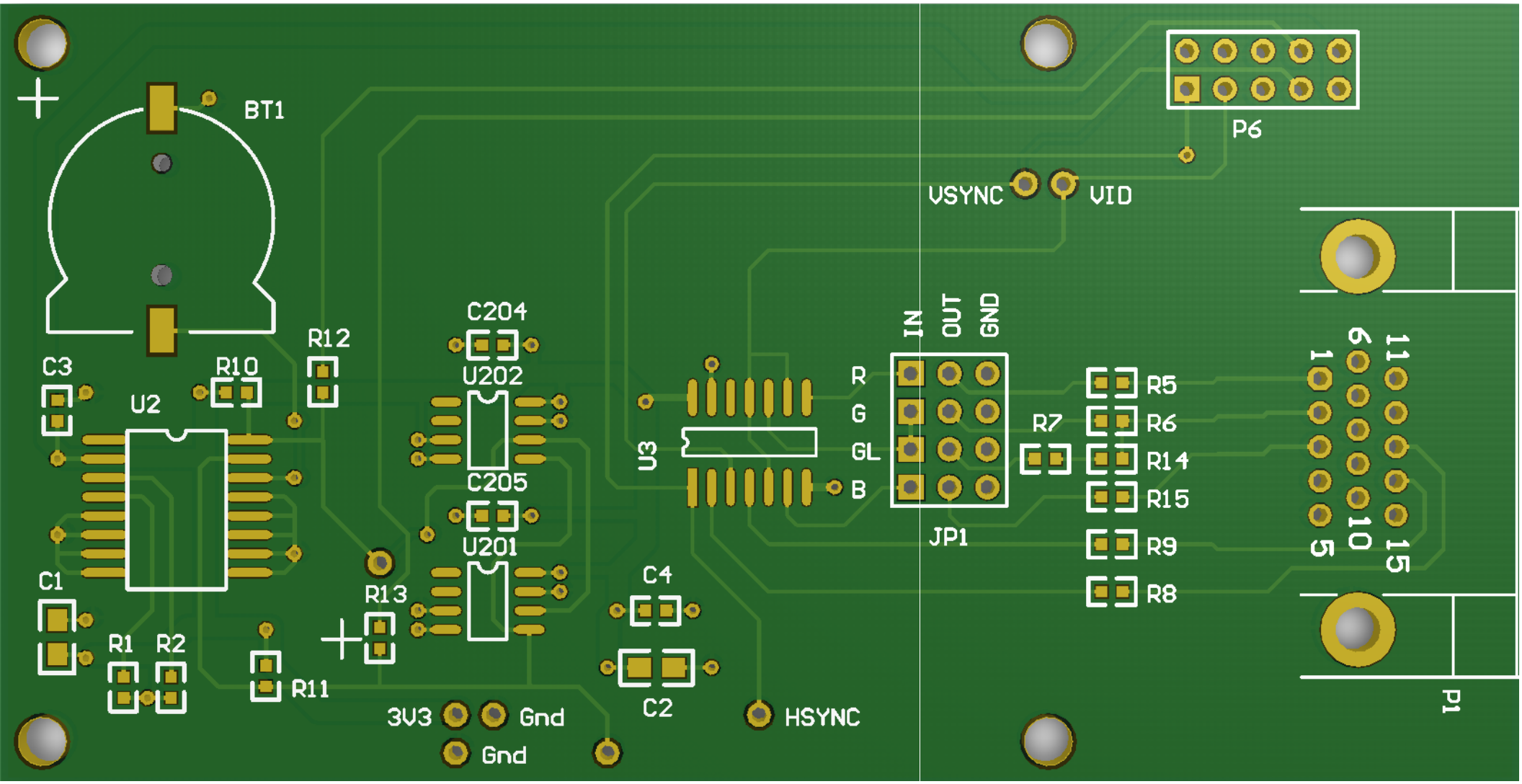

RTC
U3
SN74LV14ADR
JP1
HDR4X3
P1
HD15
U2
DS3231SN#
BT1
Battery
U201
U202
AT24CM02
P6
HDR10
U1A
U1B
U1C
U1D
U1E
RREP2C5
F_BA
DAT_nOE
ADR_nOE
F_NMI
Title
Size
B
Number
Revision
Date: 12/8/2020
File: E:\GitRepositories\..\VGA_ADAPTER.schDoc
Drawn By:

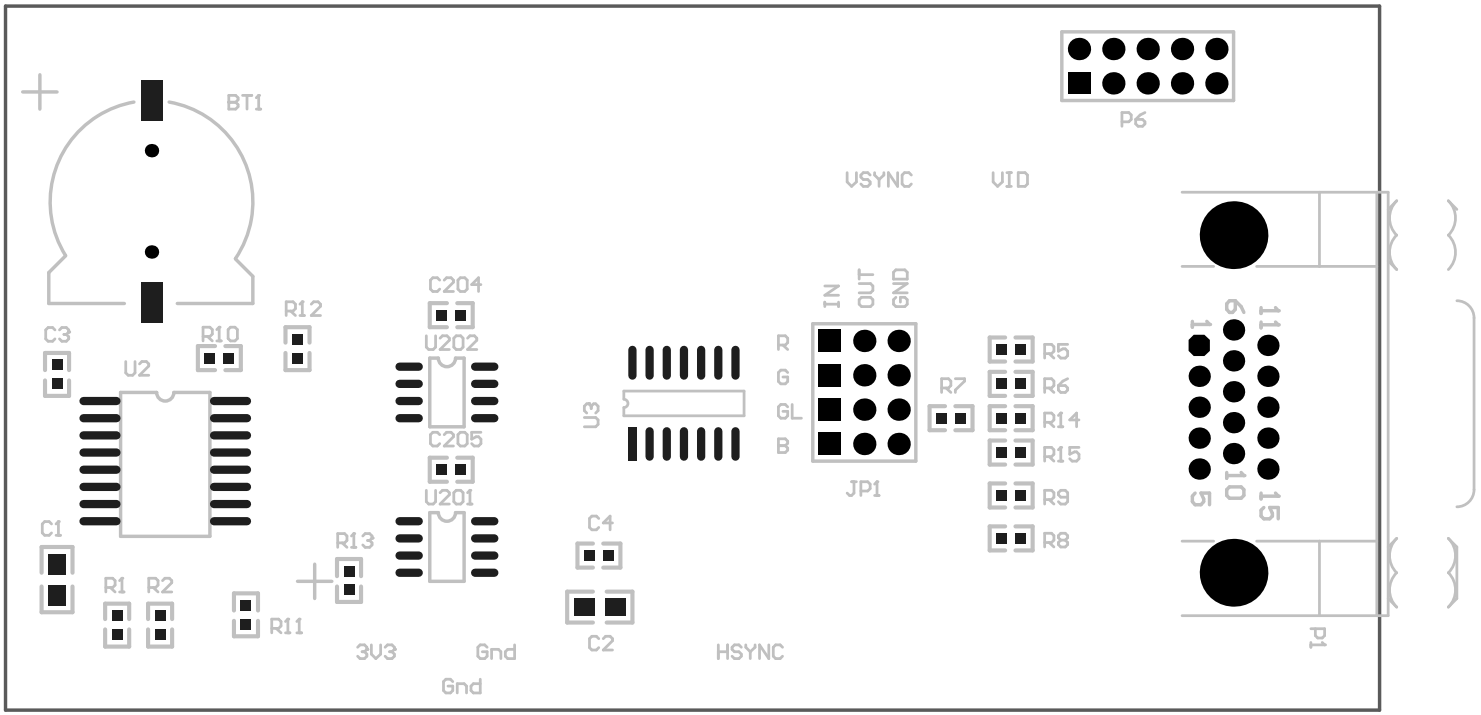
U_VGA_ADAPTER
VGA_ADAPTER.schdoc



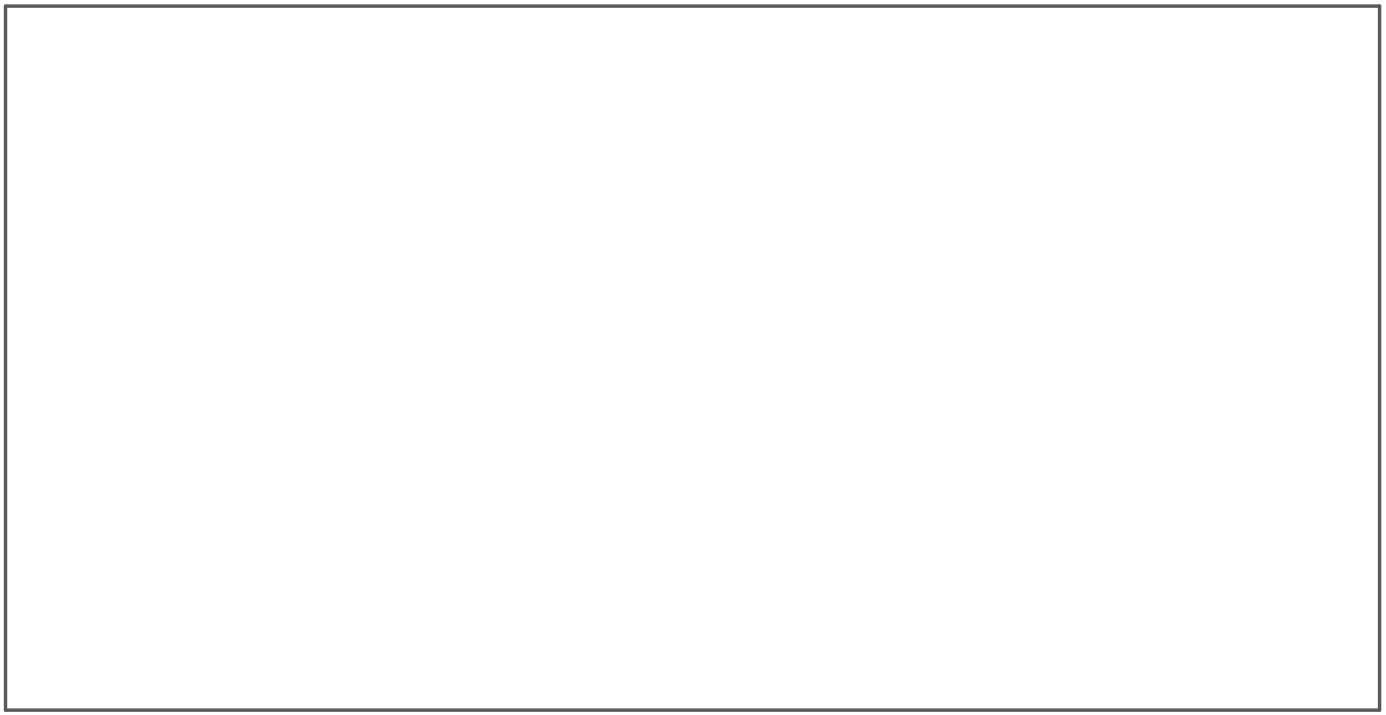
Title		
Size: A	Number	Revision
Date: 12/8/2020		Sheet of
File: E:\GitRepositories\..\VGA_ADAPTERTop.SchDoc		Drawn By:


BT1
P6
VSYNC
VID
C204
R12
R10
C3
U2
C205
U204
C1
R3
R11
C4
C2
3V3
Gnd
Gnd
HSYNC
IN
OUT
GND
R
G
BL
B
IDT
R7
R5
R6
R14
R15
R9
R8
J1
J9
J11
J12
J13
J14
J15
J16
J17
J18
J19
J20
J21
J22
J23
J24
J25
J26
J27
J28
J29
J30
J31
J32
J33
J34
J35
J36
J37
J38
J39
J40
J41
J42
J43
J44
J45
J46
J47
J48
J49
J50


BT1
C3
R10
R12
U2
C204
U202
C205
U201
R13
C1
R1
R2
R11
C4
C2
3V3
Gnd
Gnd
HSYNC
U3
R
G
GL
B
IN
OUT
GND
JP1
R7
R5
R6
R14
R15
R9
R8
P6
VSYNC
VID
1
6
11
5
10
15
P1


BT1
P6
VSYNC
VID
C204
R12
C3
R10
U202
U2
C205
U201
C1
R13
C4
R1
R2
R11
C2
3V3
Gnd
Gnd
HSYNC
U3
R
G
GL
B
IN
OUT
GND
JP1
R7
R5
R6
R14
R15
R9
R8
1
6
11
5
10
15
P1

Top Overlay



Bottom Overlay