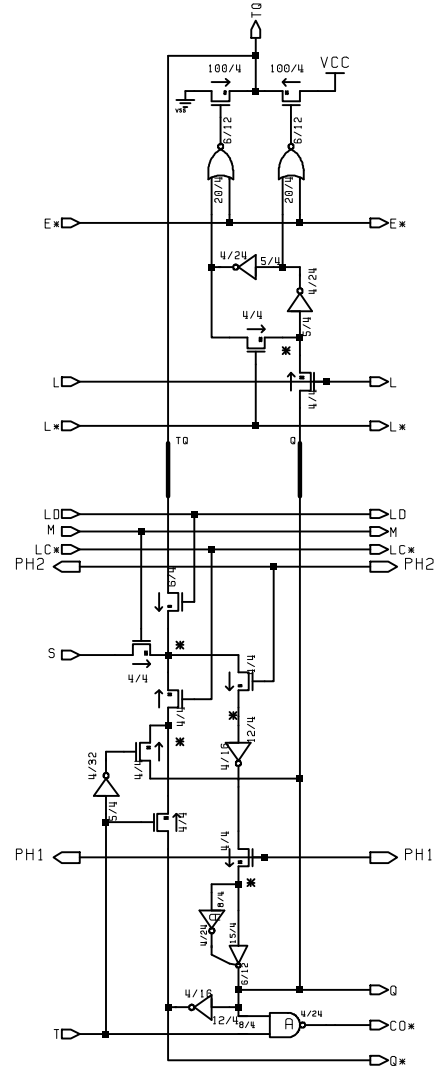
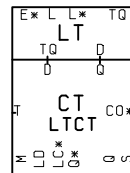


NOTE: * = SENSITIVE NODE, KEEP SHORT AND NO CROSSOVERS. (USE LARGER RATIO ON INVERTER)

M = MODIFY, CAUSES SET IF S=1, RESET IF S=0

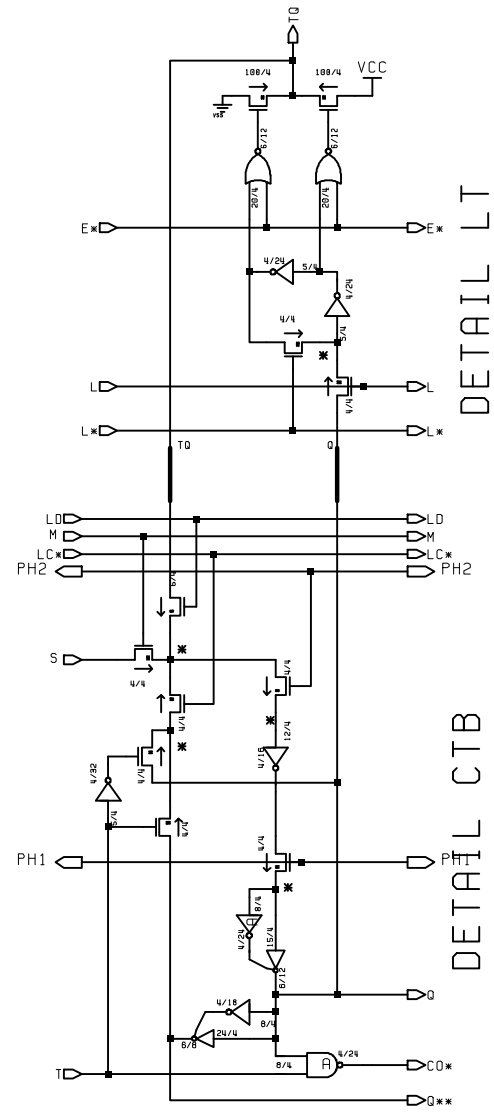
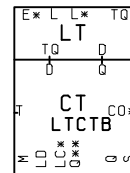
DETAIL LTCP



NOTE: * = SENSITIVE NODE, KEEP SHORT AND NO CROSSOVERS. (USE LARGER RATIO ON INVERTER)

M = MODIFY, CAUSES SET IF S=1, RESET IF S=0

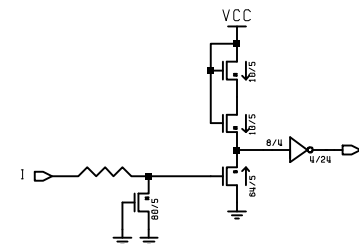
DETAIL LTCT



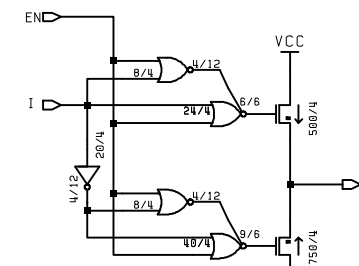
NOTE: * = SENSITIVE NODE, KEEP SHORT AND NO CROSSOVERS. (USE LARGER RATIO ON INVERTER)

M = MODIFY, CAUSES SET IF S=1, RESET IF S=0

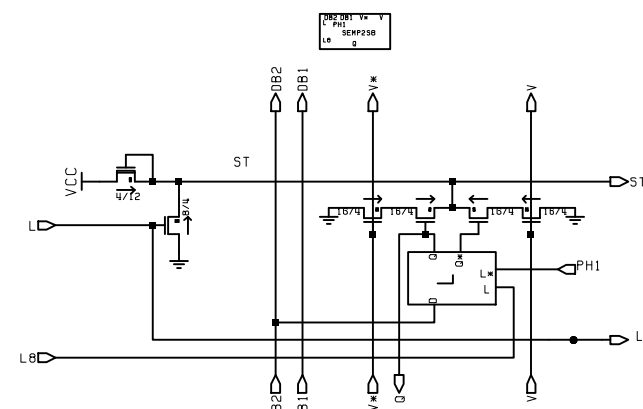
DETAIL LTCTB



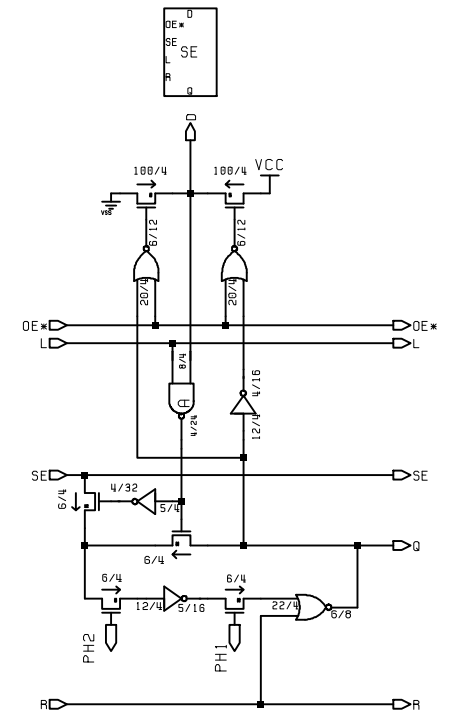
DETAIL RINP



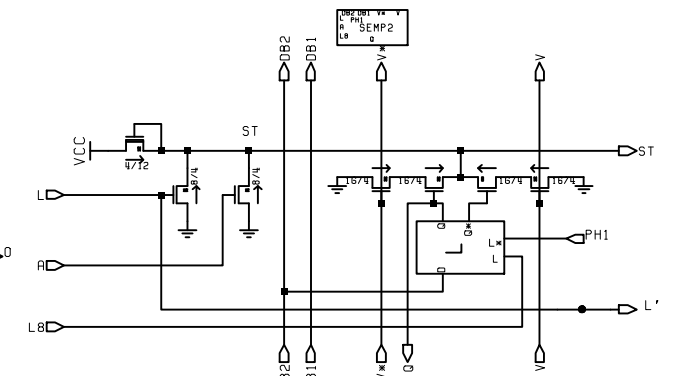
DETAIL RTI



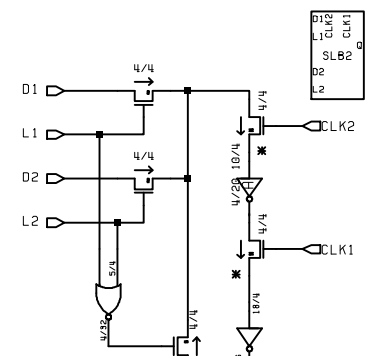
DETAIL SEMP2S8



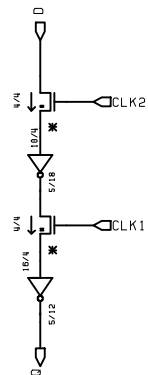
DETAIL SE



DETAIL SEMP2

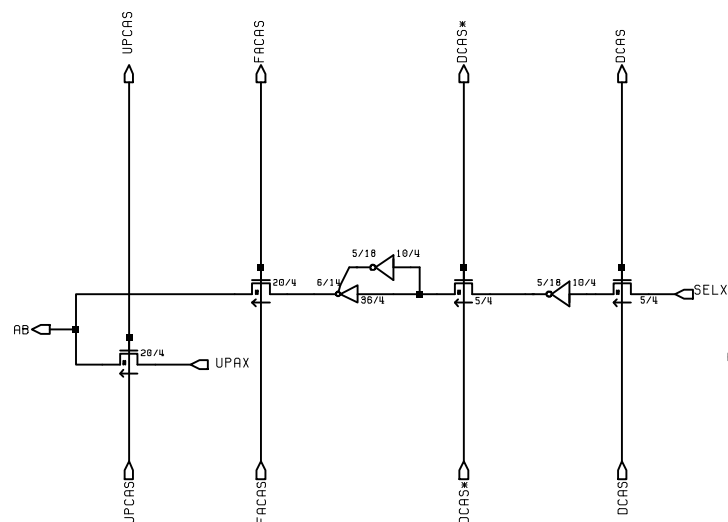


DETAIL SLB2

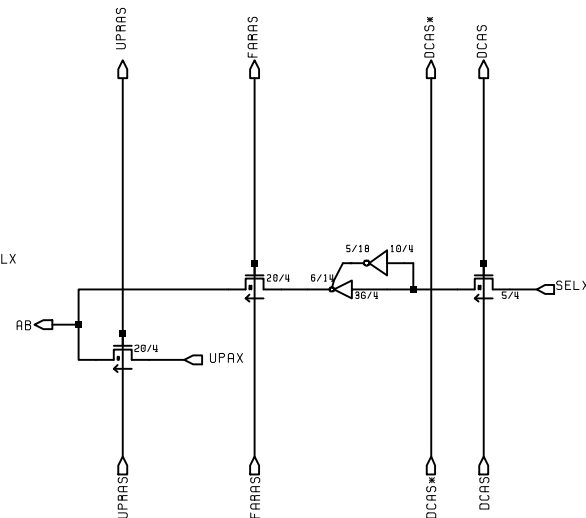


DETAIL FAD

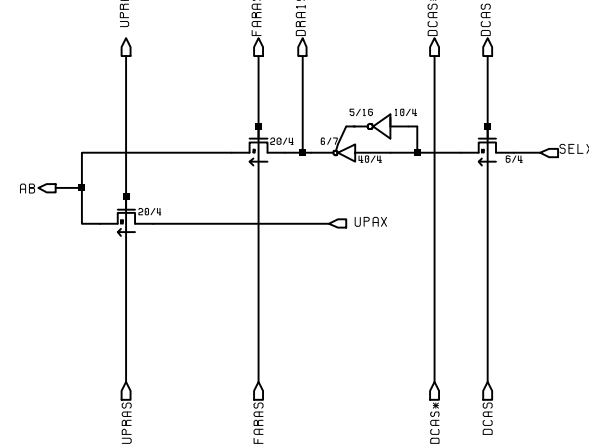
NOTE: * = SENSITIVE NODE, KEEP SHORT AND NO CROSSOVERS. (USE LARGER RATIO ON INVERTER)



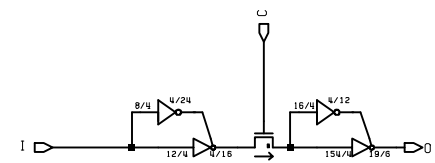
DETAIL FADH



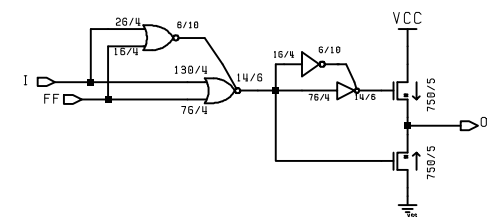
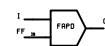
DETAIL FADL



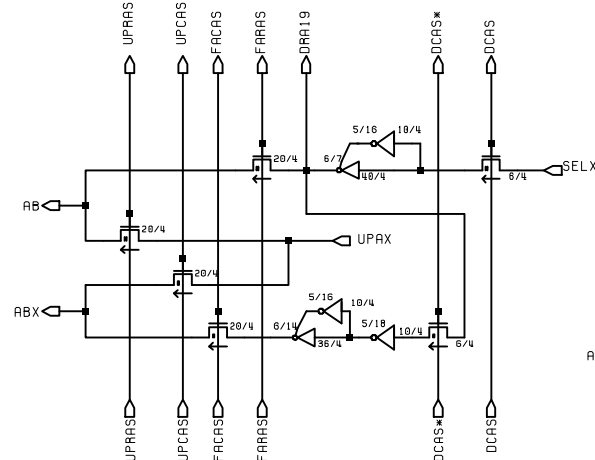
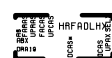
DETAIL HRFADL



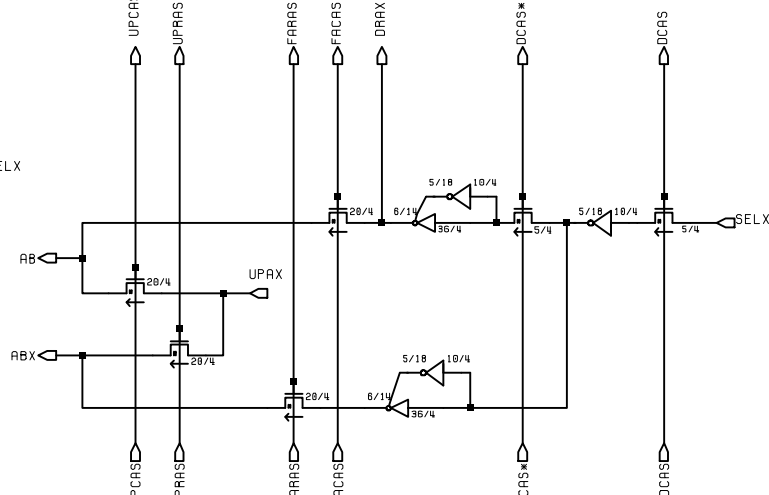
DETAIL FAINB



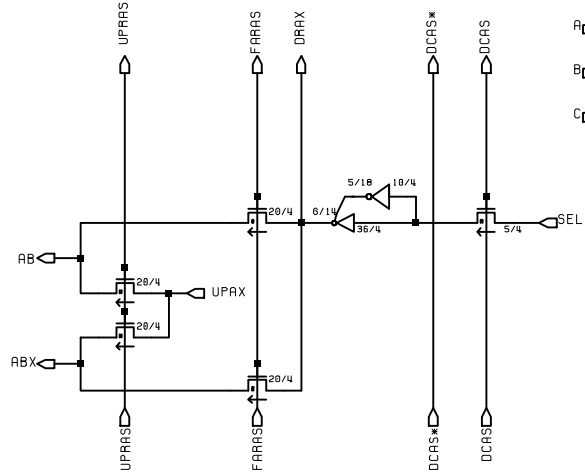
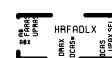
DETAIL FAPD



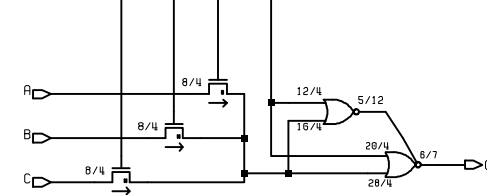
DETAIL HRFADLHX



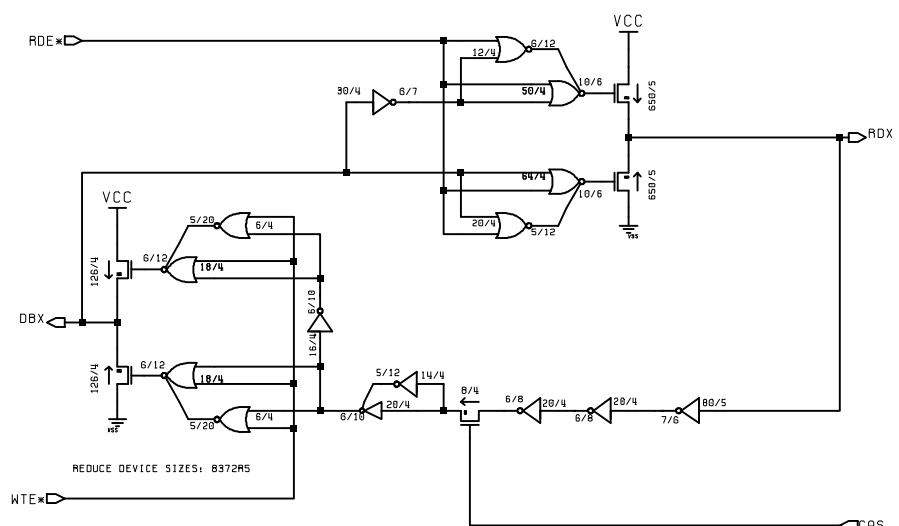
DETAIL HRFADHLX



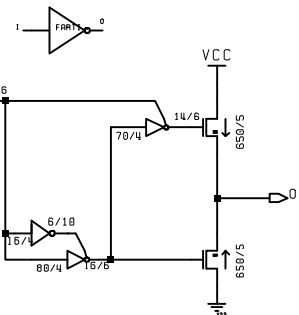
DETAIL HRFADLX



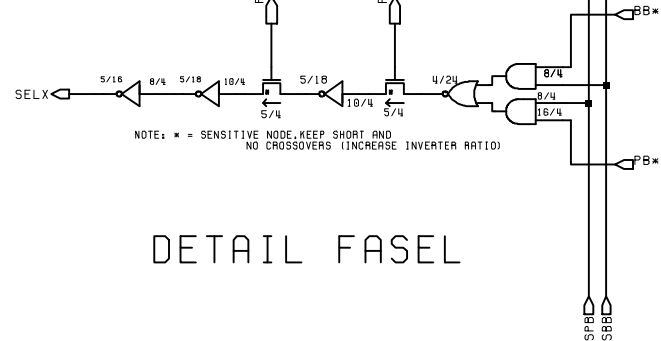
DETAIL FAMUX



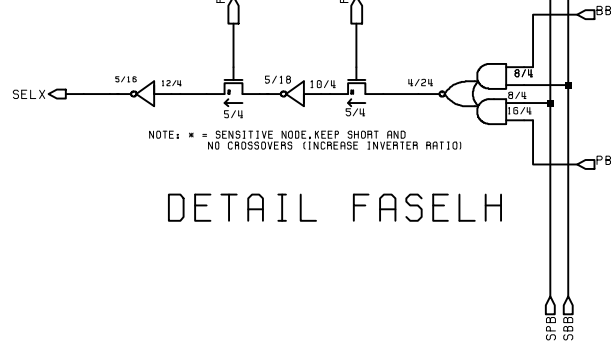
DETAIL FAPADVR (I/O PAD DRIVER)



DETAIL FARTI



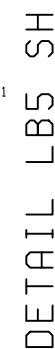
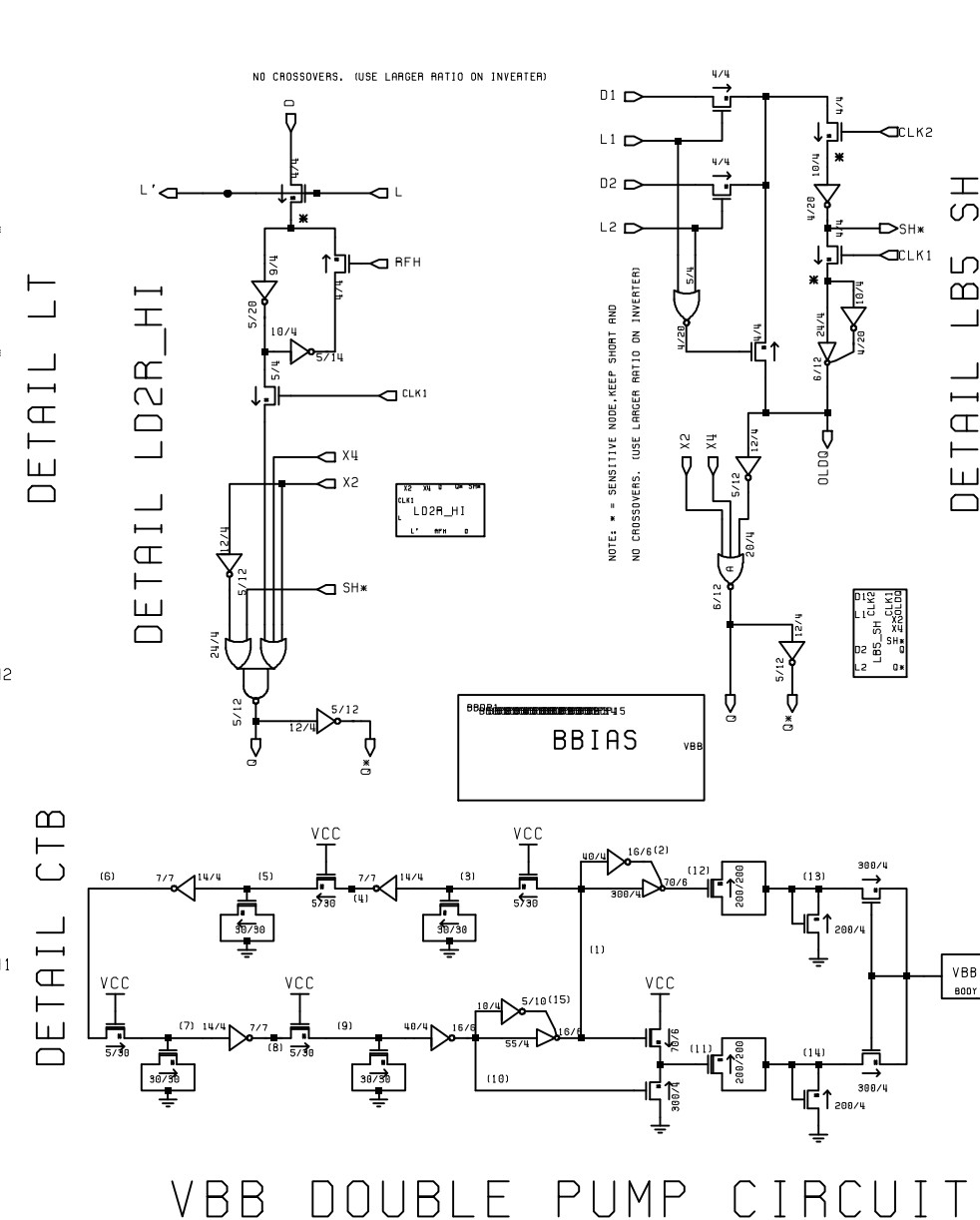
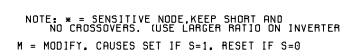
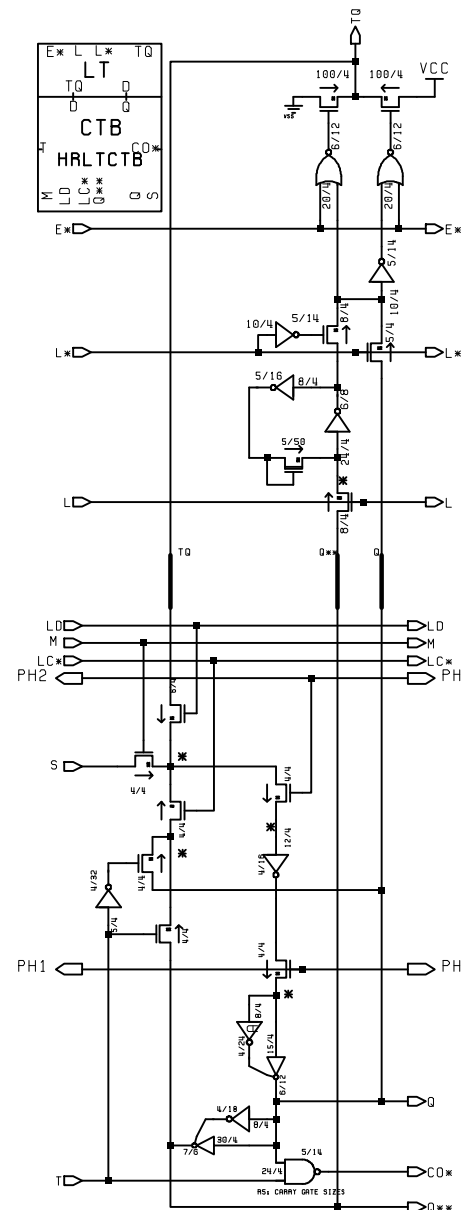
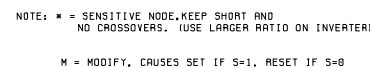
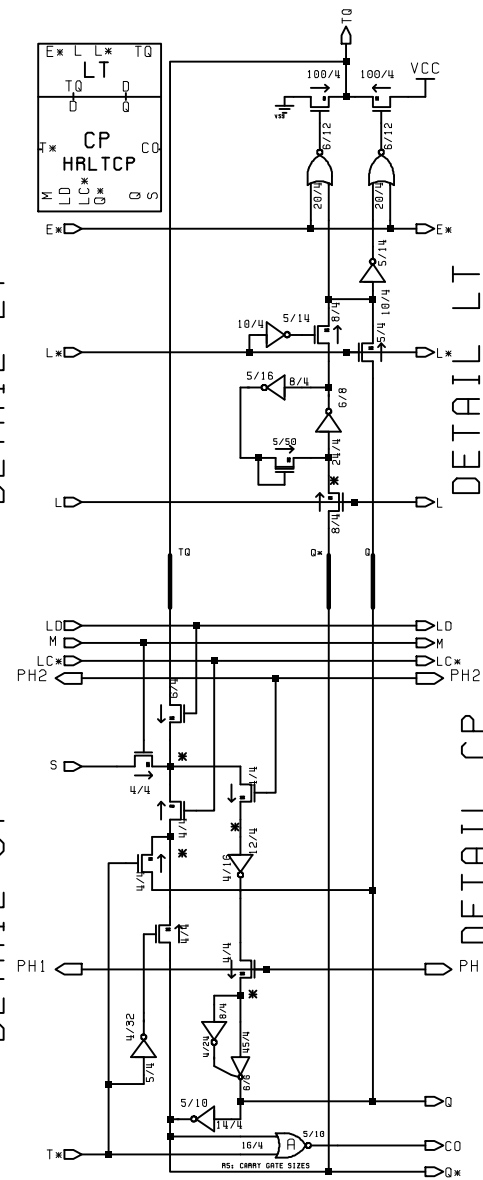
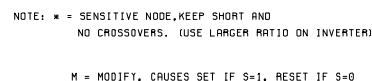
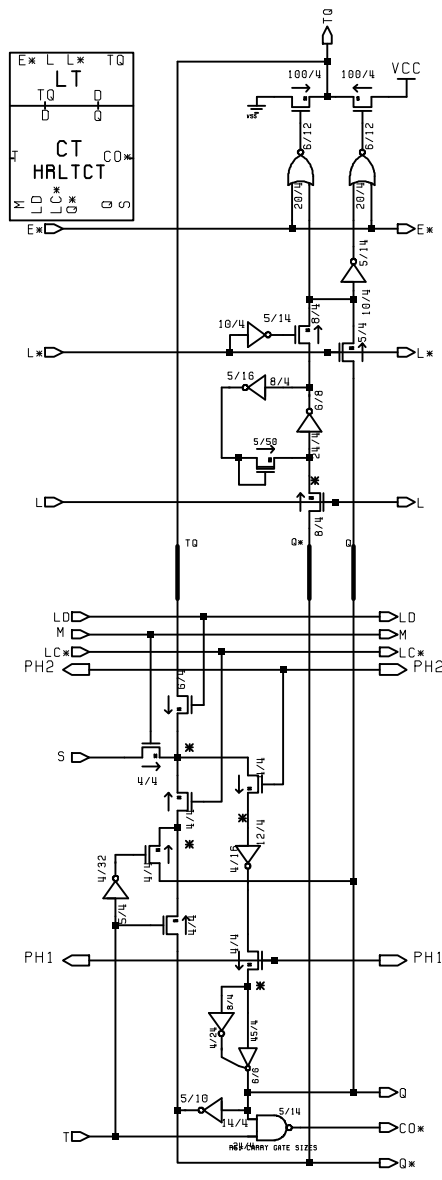
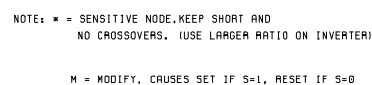
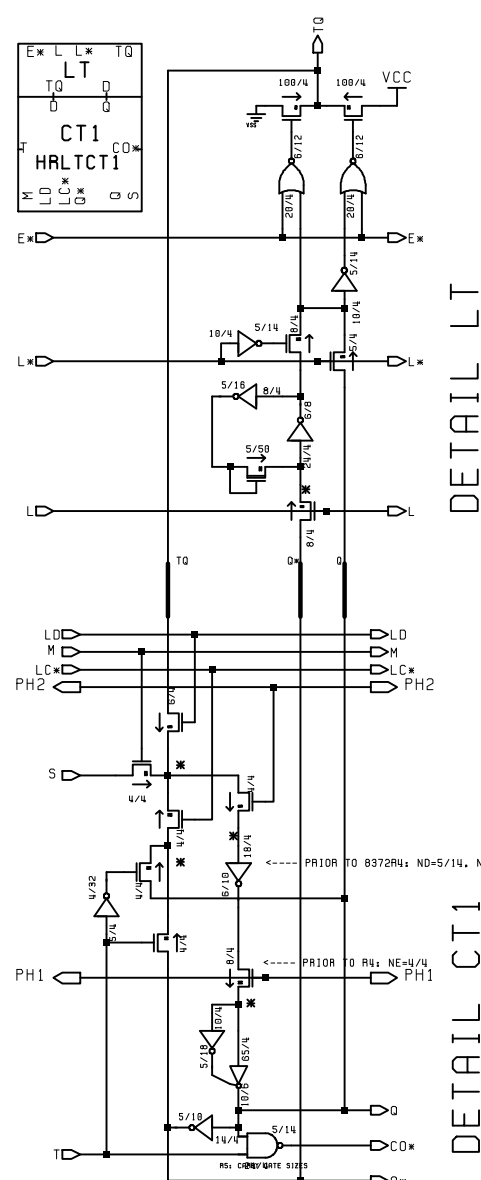
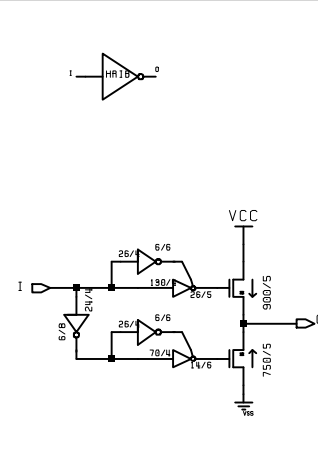
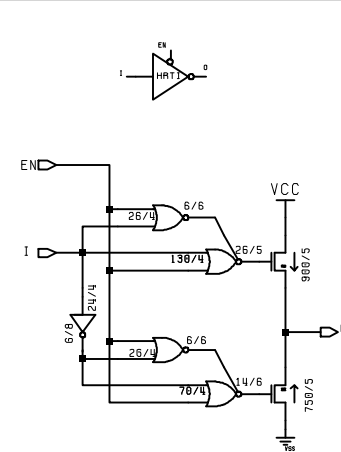
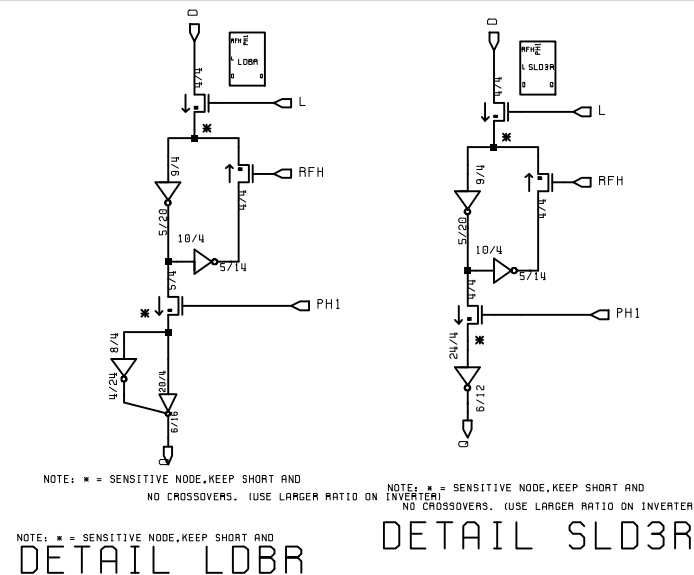
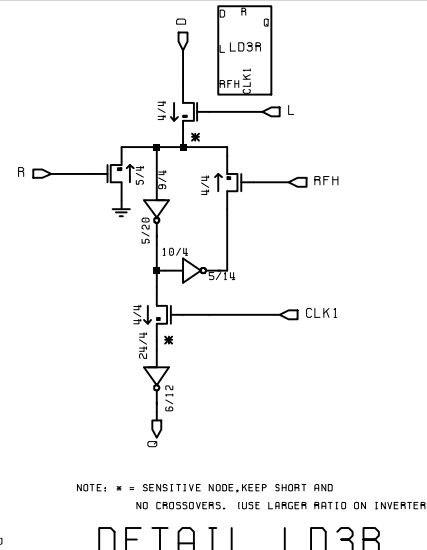
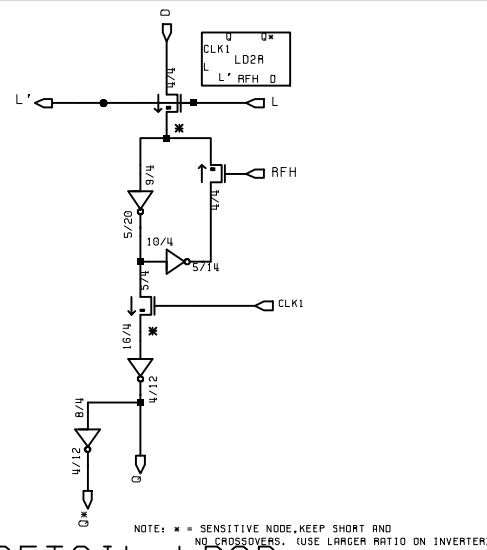
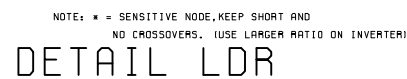
DETAIL FASEL

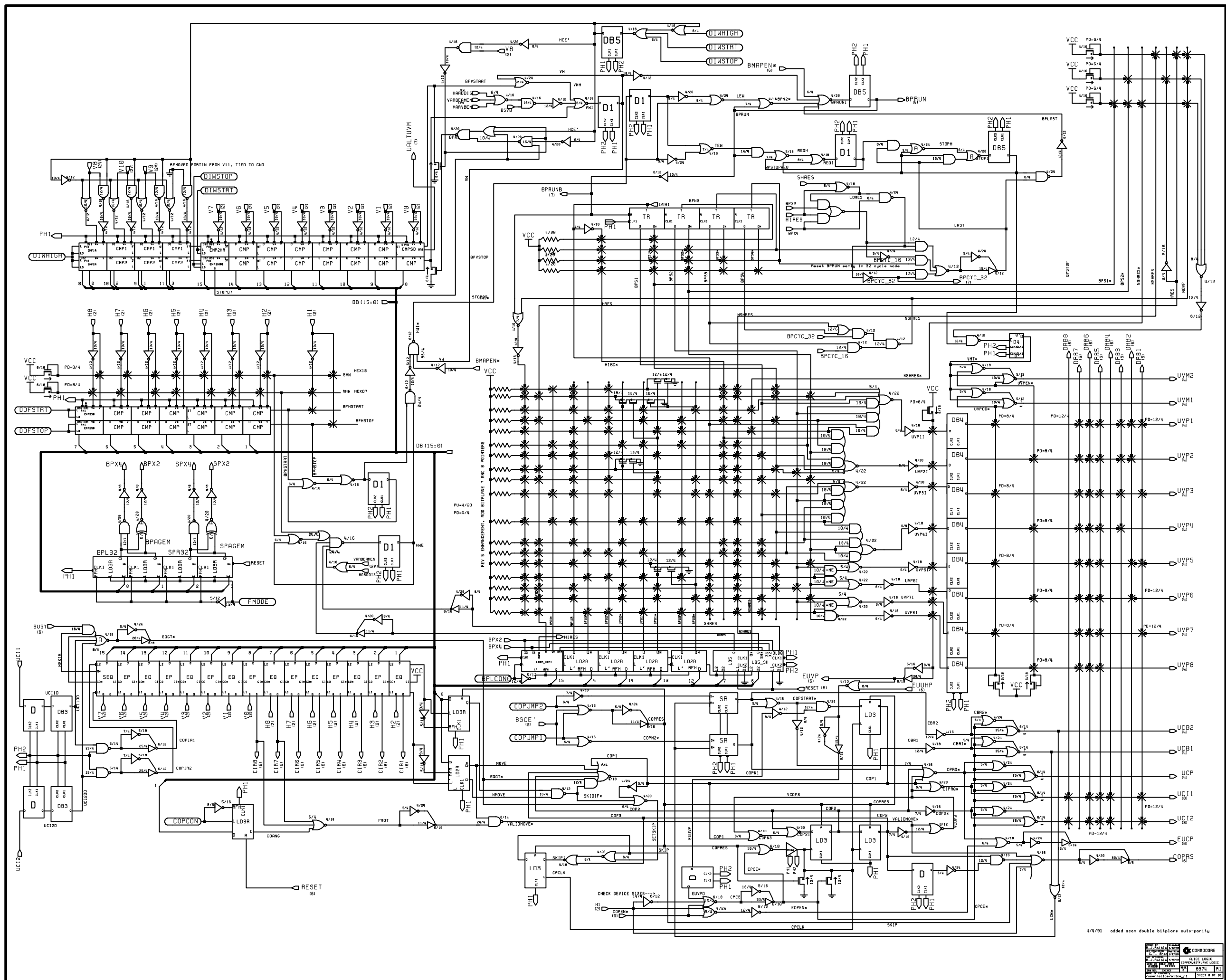


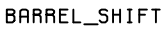
DETAIL FASELH

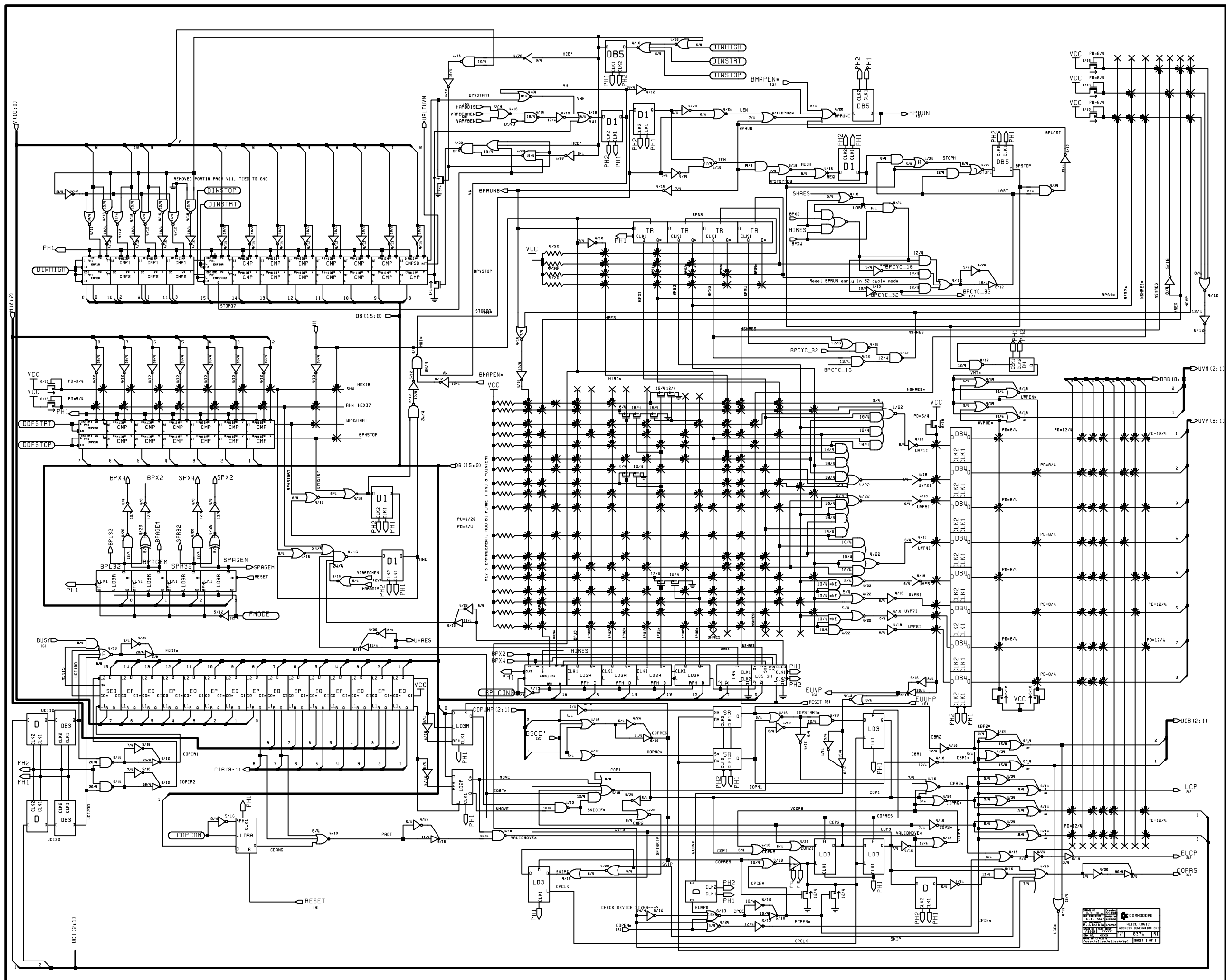
ALSO APPLIES TO 8370 AND 8371 ---->

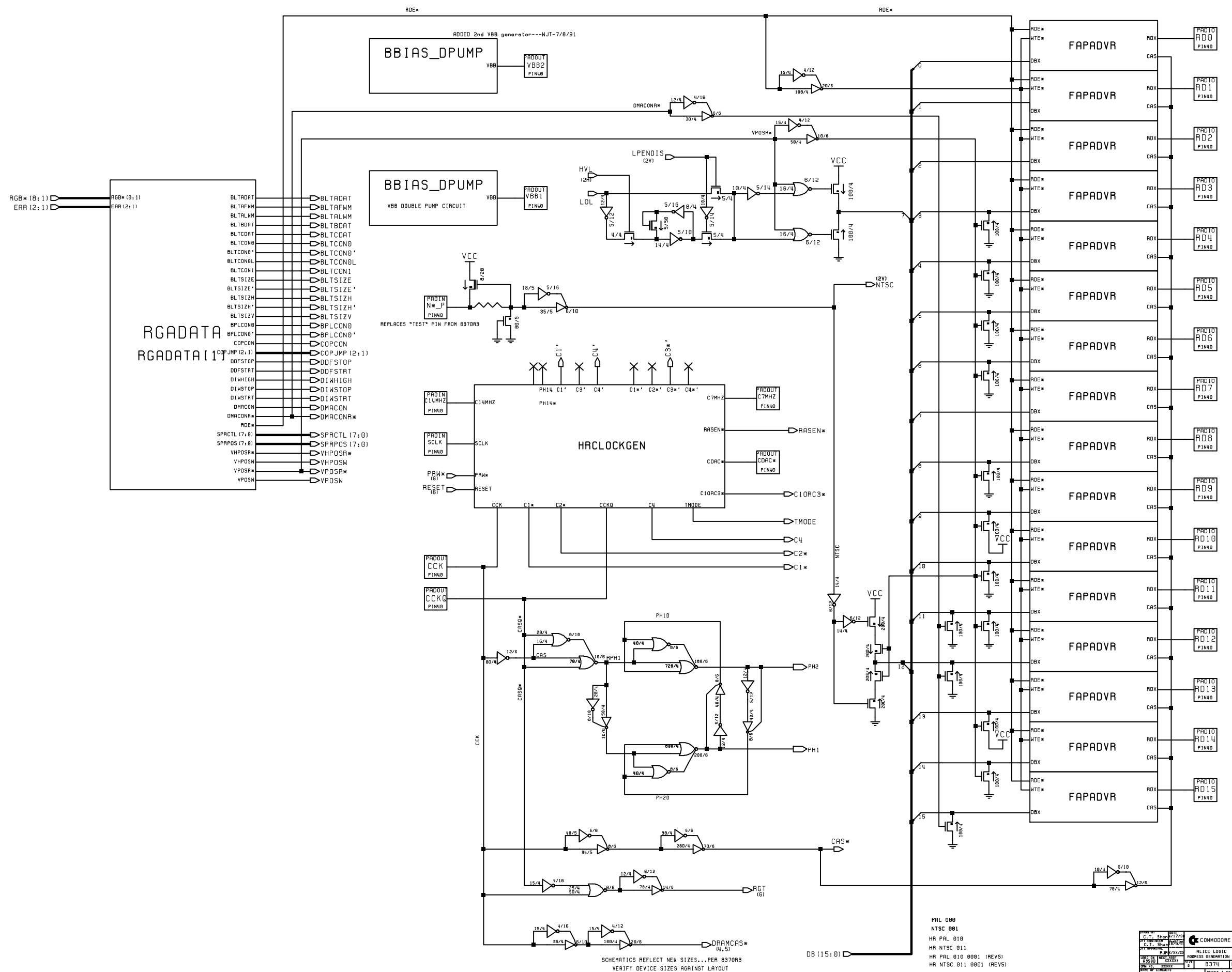
LOGIC	CMOS	COMMODORE
TYPE	FAST	8372
FUNCTION	CLOCK GENERATION	AS
PACKAGE	16	16
MANUFACTURER	8372	AS

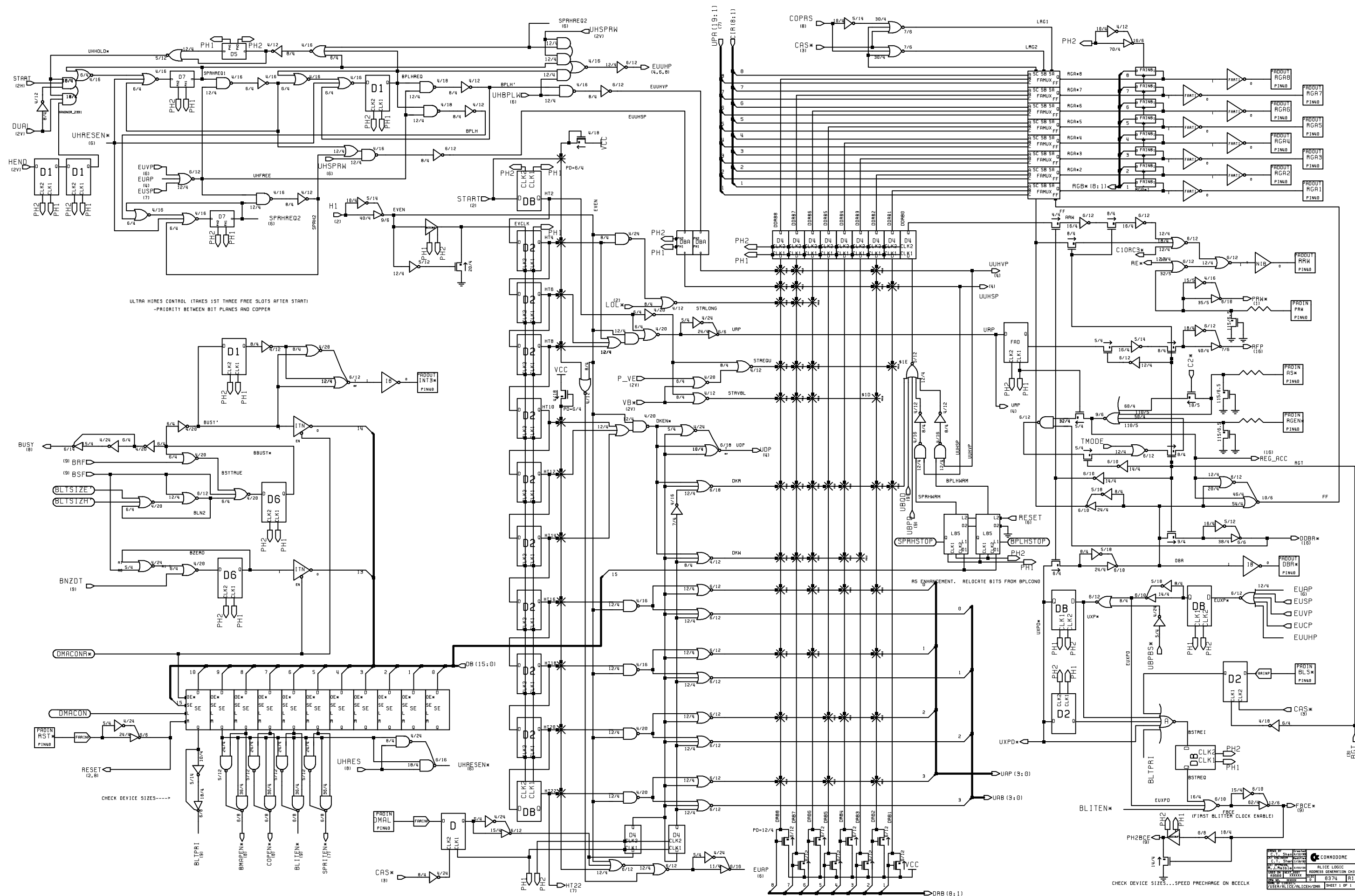


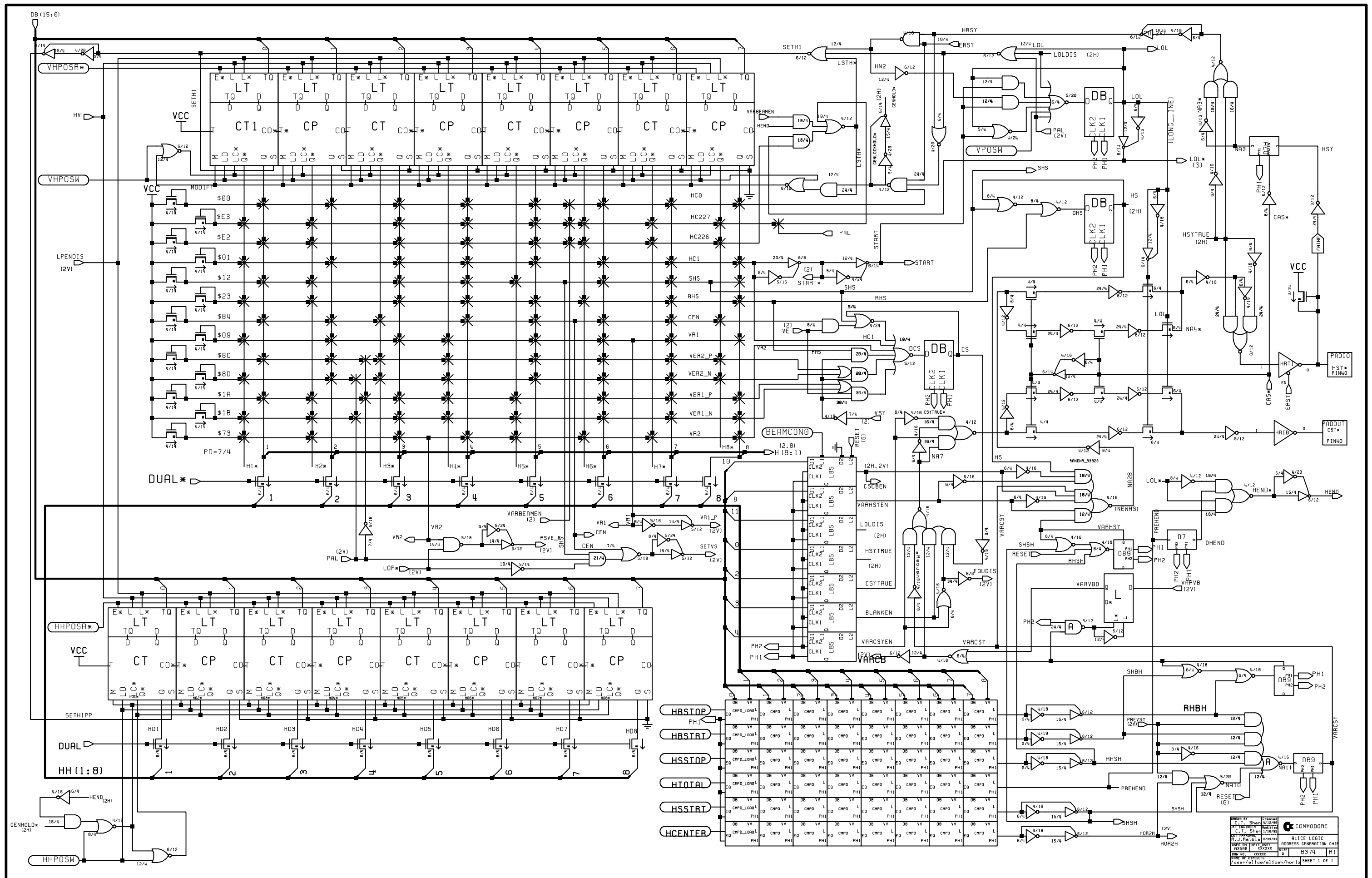


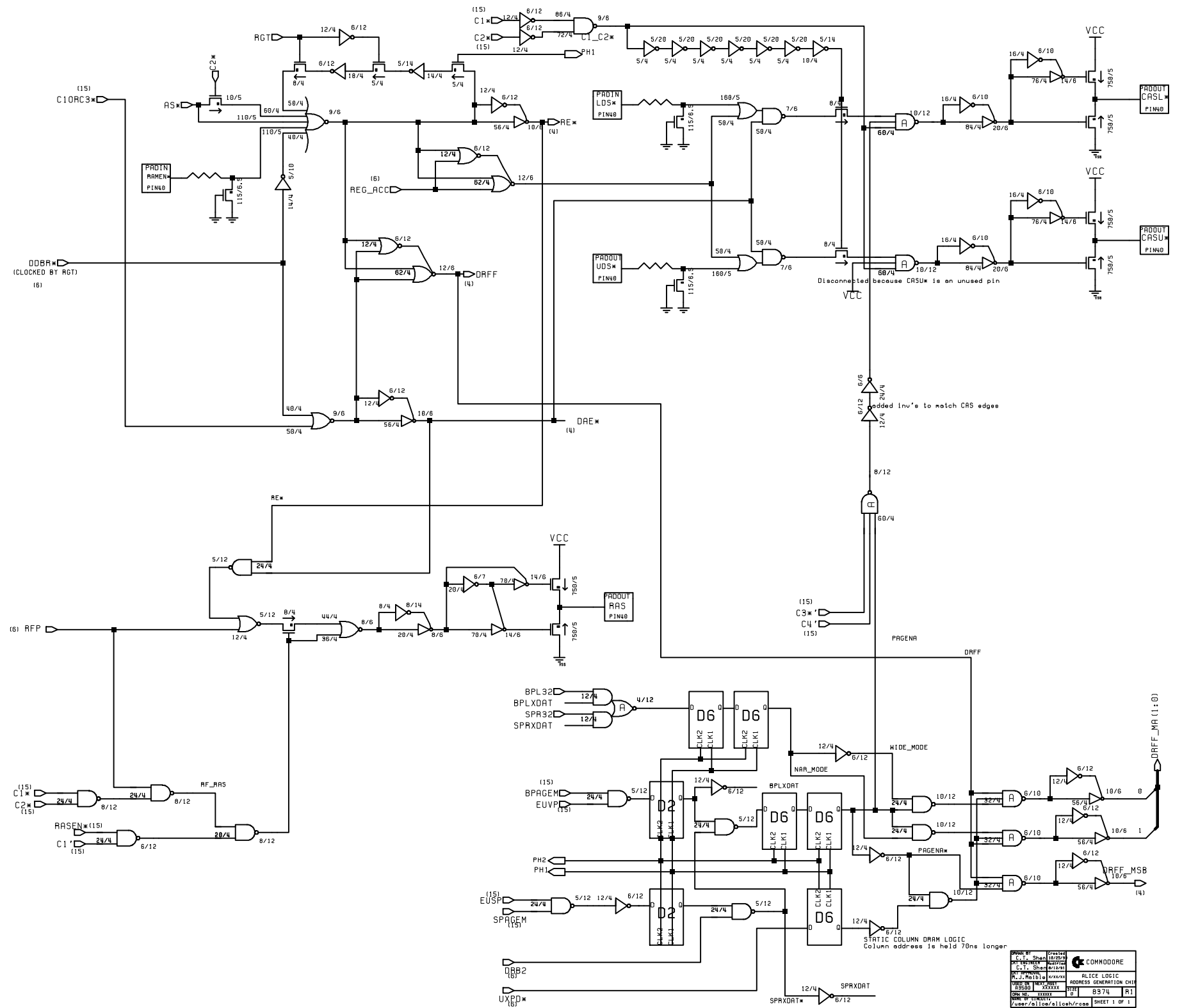


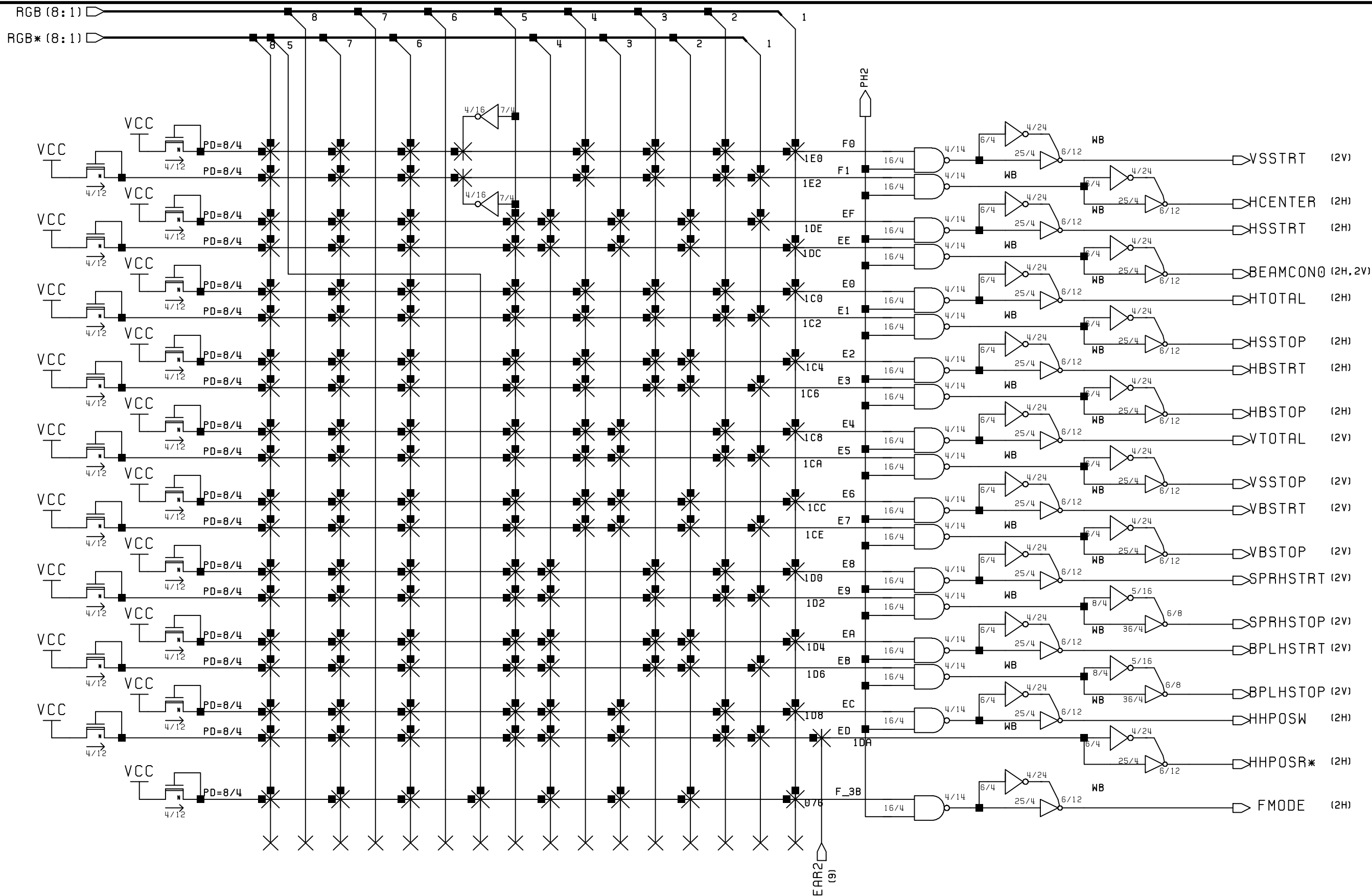













DRAWN BY	DATE	
C.T. Shen	4/18/90	
CKT ENGINEER	Modified	ALICE LOGIC
C.T. Shen	18/15/90	
CKT APPROVAL	RJR/K/XX/XX	ADDRESS GENERATION CHIP
USED ON	NEXT ASST	
A3500	XXXXXX	SIZE
DRAW NO.	XXXXXX	
NAME OF CIRCUIT	/user/alice/aliceh/rga	8374
		R1
		SHEET 1 OF 1

