

APPLICATION		REVISION			
NEXT ASSY.	USED ON	LTR	DESCRIPTION	DATE	APPROVED
	A600/A2200	A	SPECIFICATION RELEASE		

1.0 DESCRIPTION

This specification describes the requirements for a custom audio and peripherals controller chip (PAULA). For electrical specificatioons refer to specification 252127.

1.1 CONFIGURATION

This device shall be configured as a 52-pin plastic PLCC chip. See Figure 1 for pinouts and Figure 2 for package dimensions.

1.2 SOURCES

Refer to Approved Vendors List.

1.3 APPLICABLE DOCUMENTS

Commodore Engineering Policy 1.02.007 Integrated Circuit Qualification Procedure
Commodore Engineering Policy 1.02.008 Integrated Circuit Process Test Specification

COMMODORE P. N.	STATUS				
391077-01	ACTIVE				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES. TOLERANCES: ANGLES +/- 1 DEGREE 2 PLACE DECIMALS +/- 0.02 3 PLACE DECIMALS +/- 0.010		DRAWN Mike Rivers	DATE	<div>Commodore</div> <div>1200 WILSON DRIVE WEST CHESTER, PA. 19380 (215) 431-9100</div>	
		SYSTEM ENG.	DATE		
		TEST ENG	DATE		
		COMP. ENG Drew Shannon	DATE	TITLE: IC, SM, PAULA, 8364	
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				SIZE A	DRAWING NUMBER
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				SCALE	SHEET 1 OF 4

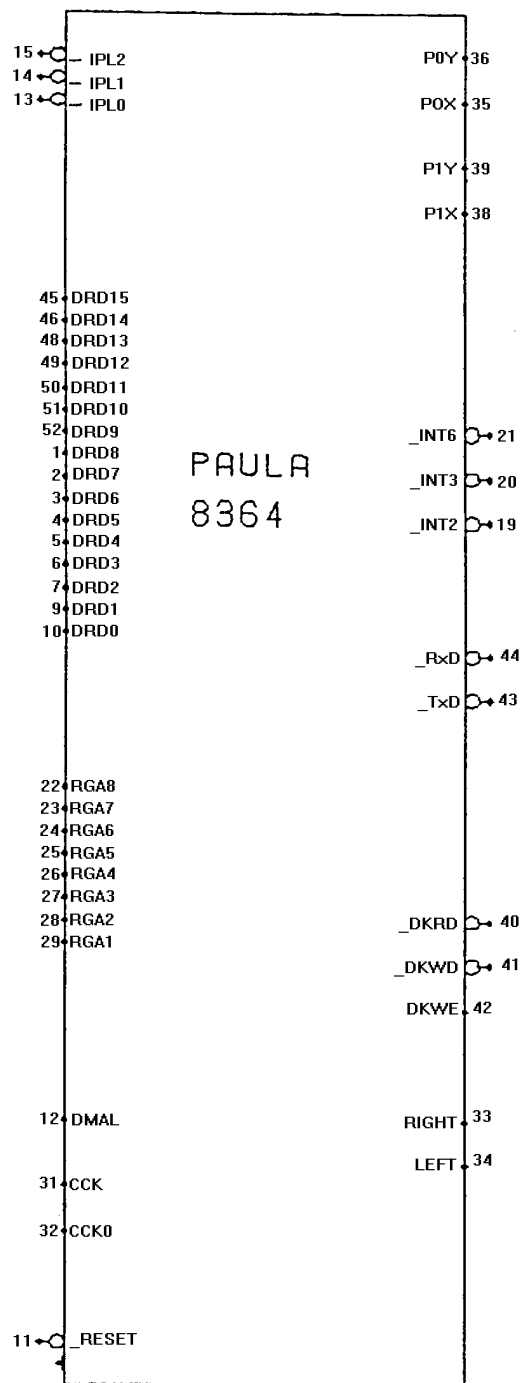


FIGURE 1 - PINOUTS

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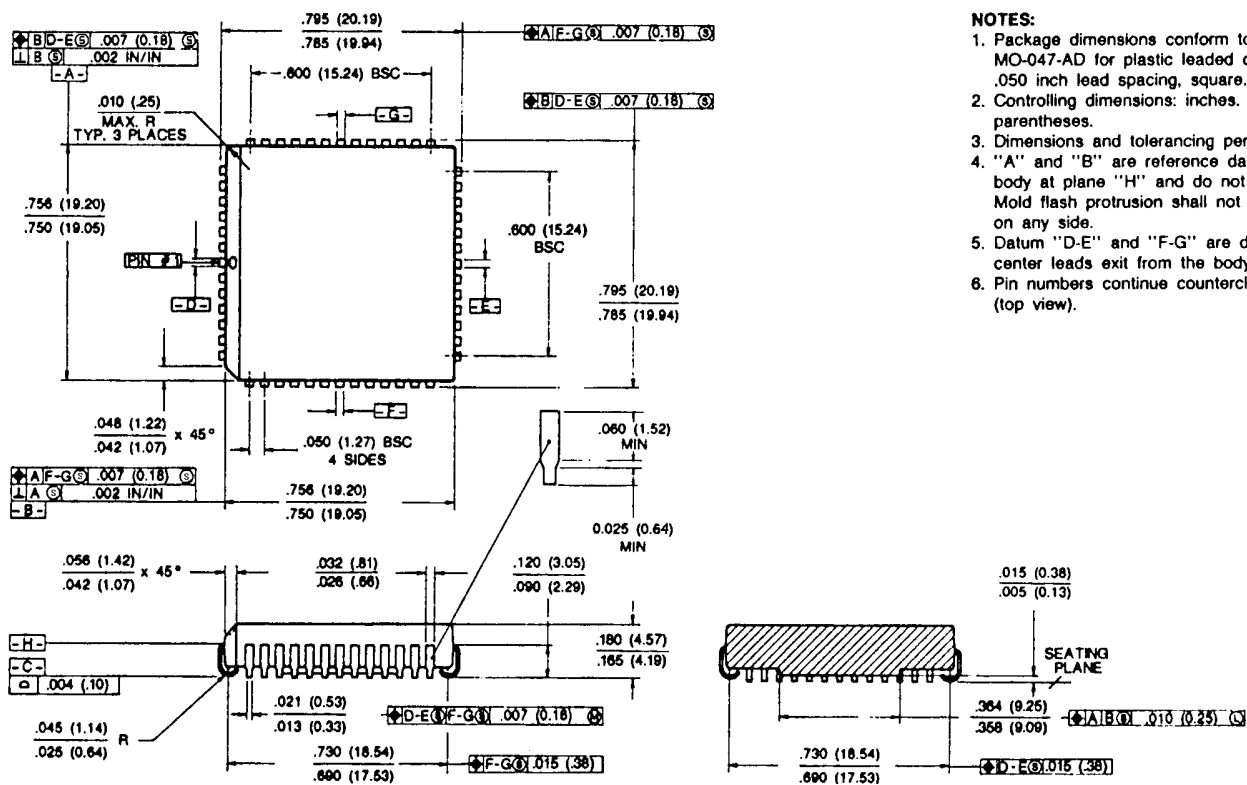
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SCALE

SHEET 2 OF 4

391077

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NOTES:

1. Package dimensions conform to JEDEC specification MO-047-AD for plastic leaded chip carrier 52 leads, .050 inch lead spacing, square. (issue A, 10/31/84).
2. Controlling dimensions: inches. Metric are shown in parentheses.
3. Dimensions and tolerancing per ANSI Y14.5M-1982.
4. "A" and "B" are reference datums on the molded body at plane "H" and do not include mold flash. Mold flash protrusion shall not exceed .006" (.15mm) on any side.
5. Datum "D-E" and "F-G" are determined where these center leads exit from the body at plane "H".
6. Pin numbers continue counterclockwise to pin #52 (top view).

Commodore

TITLE

IC, SM, PAULA, 8364

SIZE

DRAWING NUMBER

REV.

SCALE

SHEET 3 OF 4

391077

A

3.0 PHYSICAL REQUIREMENTS

3.1 MARKING

Parts shall be marked with Manufacturer's Part Number, Manufacturer's Identification, and EIA Date Code.

3.2 PACKAGING

The interconnected logic circuitry shall be contained in a 52-pin PLCC plastic package with exterior dimensions per Figure 2.

4.0 ENVIRONMENTAL REQUIREMENTS

Units furnished to the requirements of this specification shall meet the following environmental resistance requirements (vendors shall furnish supporting documentation upon request):

Operating Temperature	0 to 70 deg. C
Operating Humidity	5 to 95% RH non-condensing
Operating Altitude	0 to 3000 meters
Storage Temperature	- 20 to + 85 deg. C
Storage Humidity	5 to 95% RH non-condensing
Storage Altitude	0 to 15,000 meters

4.1 PROCESS QUALIFICATION TESTS

Integrated circuits supplied to the requirements of this specification shall meet the requirements of Engineering Policy No. 1.02.008. Supporting documentation shall be supplied by vendor upon request.

4.2 ENVIRONMENTAL TEST CONDITIONS

Devices shall comply with the following environmental resistance tests per Commodore Engineering Policy 1.02.007.

1. Temperature/humidity (85 deg. C and 95% RH non-condensing) for 168 hours.
2. Operating life (1000 hours at 70 deg. C ambient temperature)
3. Solderability per MIL-STD-883, Method 2003
4. Pressure cooker (15 psig, 120 deg. C, and 100% RH for 24 hours)
5. Solvent resistance per MIL-STD-883, Method 2015, using water and trichloroethane
6. Solder temperature resistance (250 deg. C for five seconds)
7. ESD requirement MIL-STD 1686 Group 3

Note: Devices shall meet this specification's operating performance requirements after the above tests are completed.

4.3 MINIMUM ACCEPTANCE LEVEL

The minimum acceptance level of any lot shall be an AQL of 0.65 as defined by MIL-STD 105 single sampling techniques.

4.4 AGE OF DEVICES

Unit shall be rejected if EIA Date Code indicates an age of three (3) or more years.

Commodore

TITLE

IC, SM, PAULA, 8364

SIZE

DRAWING NUMBER

REV.

SCALE

SHEET 4 OF 4

391077

A

APPROVED VENDOR LIST

This sheet must be removed from this document before the document is shown or transmitted to a vendor.

Commodore Part Number

391077-01

Vendor

CSG

Vendor Part Number

8364 R7

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TITLE

IC, SM, PAULA, 8364

SIZE

DRAWING NUMBER

REV.

SCALE

SHEET i OF i

391077

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