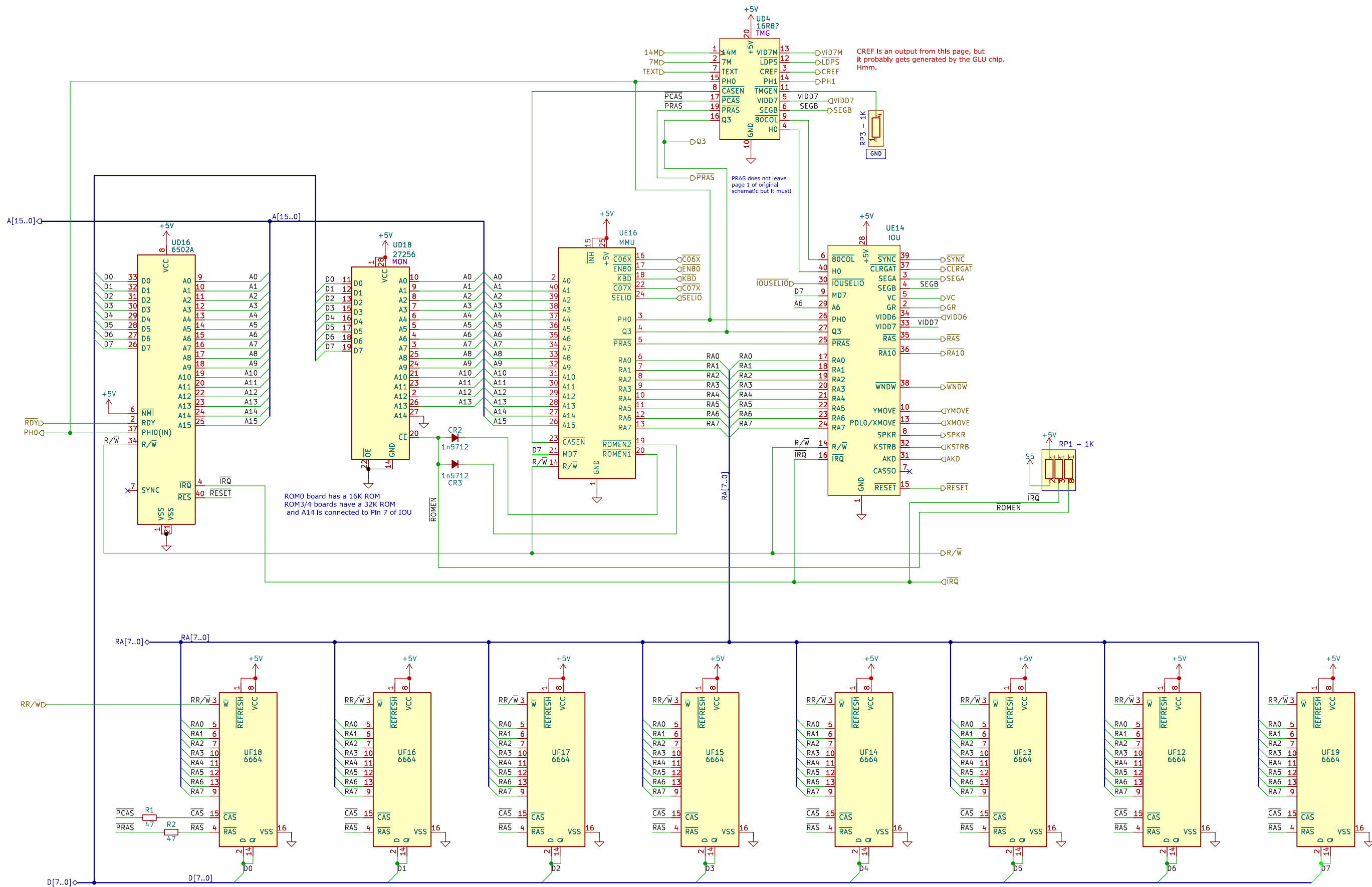


PRAS doesn't leave CPU page.
And It's the INPUT to the Video page
but I don't see what drives it

Changes from OG schematic

- + No Connectors Page
- ++ Connectors placed on pages close to their function
- + IWM and Keyboard
- ++ Keyboard and Gameport functions removed from IWM page
- ++ IWM is only its own now
- + Resistor Packs 1 and 3 (RP1 and RP3)
- ++ These were not in logical places in OG schematic.
- +++ Now they are multi-unit devices and placed closer to logical location.
- + Decoupling capacitors
- ++ Group of decoupling capacitors moved to its own page.



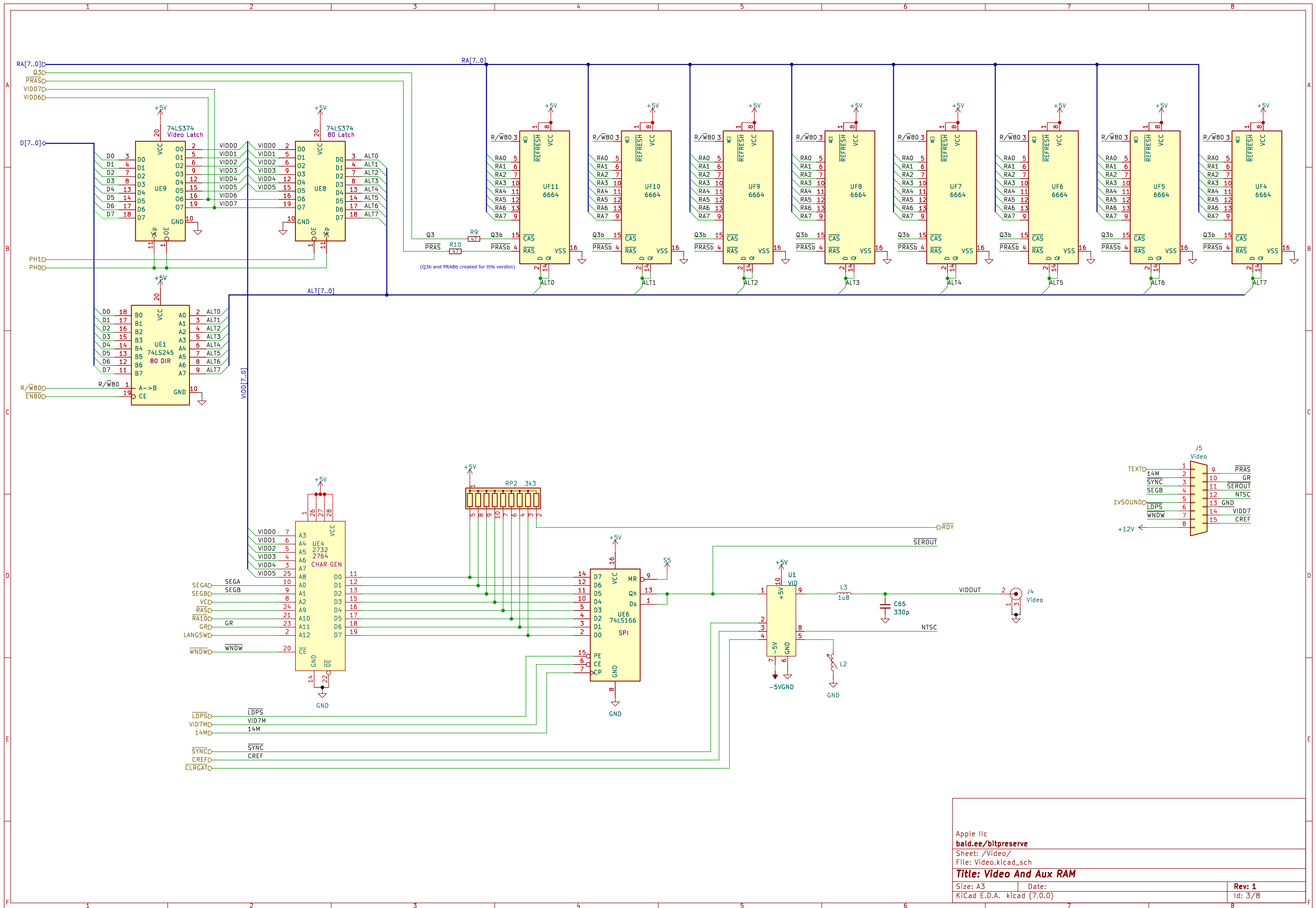
Apple IIc
bald.ee/bitpreserve
Sheet: /CPU MMU IOU RAM/
File: CPU_MMU_IOU_RAM.kicad_sch

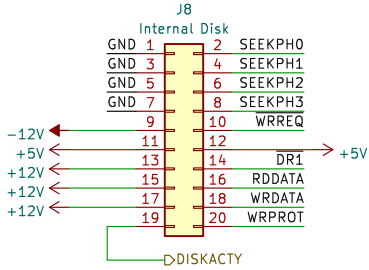
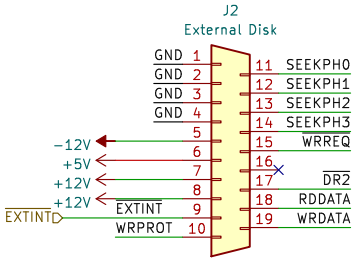
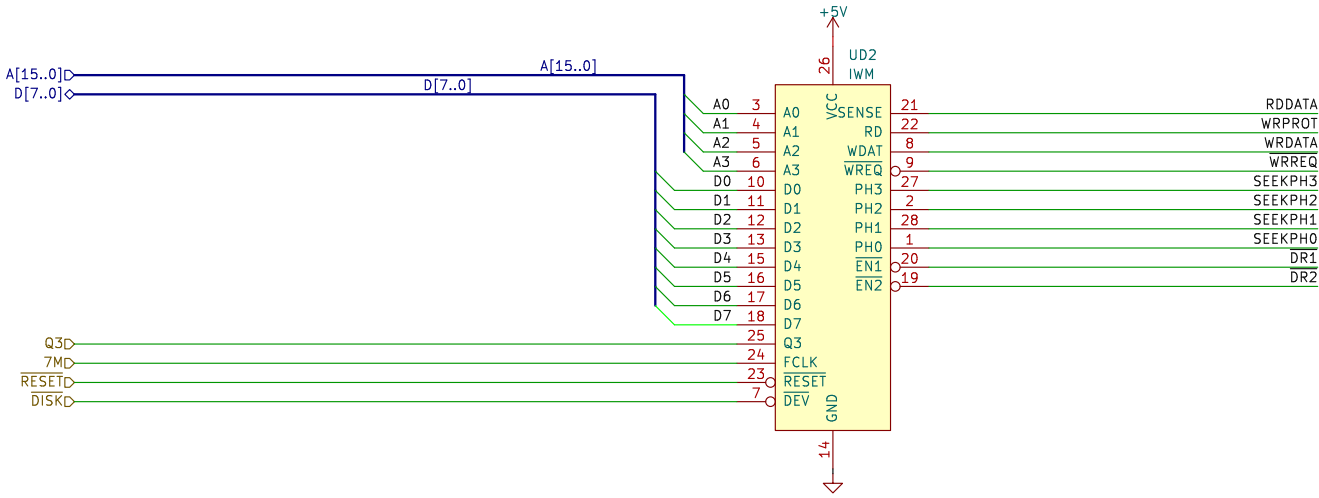
Title: CPU

Size: A3
KiCad E.D.A. kicad (7.0.0)

Date:
Id: 2/8

Rev: 1





OG Schematic had decouple capacitors on this page,
they have been moved to a separate sheet.

Apple IIc
bald.ee/bitpreserve
Sheet: /IWM/
File: IWM.kicad_sch

Title: IWM – Disk Drive

Size: A3	Date:	Rev: 1
KiCad E.D.A. kicad (7.0.0)		Id: 4/8

