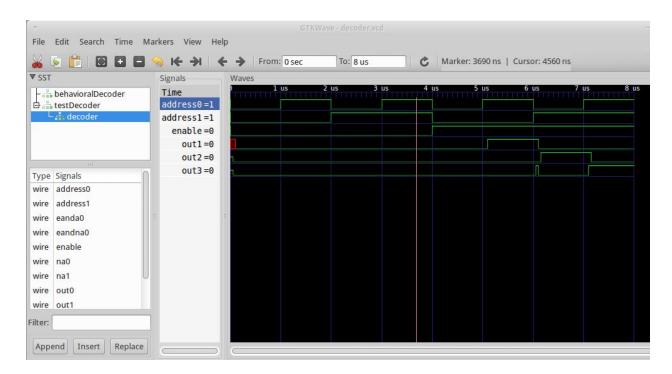
## Decoder

```
En A0 A1 | 00 01 02 03 | Expected Output
                      0 | All false
            0
                0
                   0
0
       0
                      0 | All false
0
   0
            0
               0
                   0
                      0 | All false
0
            0
               0
                   0
                      0 | All false
1
                      0 | 00 Only
1
            0
               1
                   0
                      0 | 01 Only
      0 |
1
   0
      1 |
            0
               0
                   1
                      0 | 02 Only
1
            0
                   0
                      1 | 03 Only
```

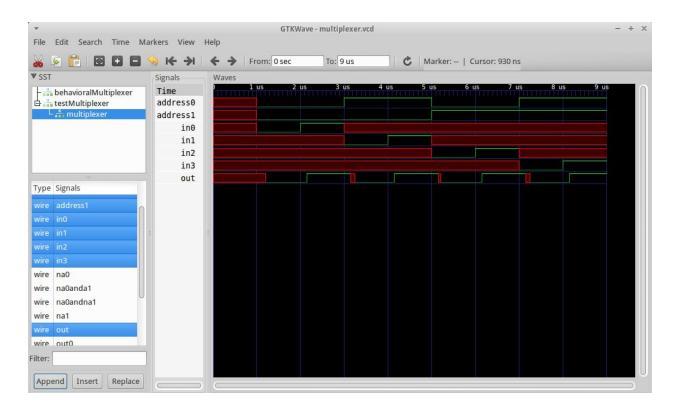


## Multiplexer

```
a0 a1 | in0 in1 in2 in3 | out | Expected Output
                                   1 0
0
   0
         0
                              0
              Х
                   Х
0
   0
       | 1
                              1
                                   | 1
              Х
                  Х
                       Х
       l x
              0
   0
                            1 0
                                   | 0
                  X
                       X
1
   0
              1
                            | 1
                                   | 1
0
                            | 0
   1
       | x
                  0
                                   | 0
              X
                       X
   1
       X
                            | 1
                                   | 1
```

## Shruti Iyer 09/22/16

```
1 1 | x x x 0 | 0 | 0
1 1 | x x x 1 | 1 | 1
```



## Adder

Α	В	Cin	Sum	Cout	Expe	ected	Output
0	0	0	0	0	0	0	
0	0	1	1	0	1	0	
0	1	0	1	0	1	0	
0	1	1	0	1	0	1	
1	0	0	1	0	1	0	
1	0	1	0	1	0	1	
1	1	0	0	1	0	1	
1	1	1	1	1	1	1	

