

Compare Midpoint Checkin

Manual Test Sequence

1. Press #Button 0* to reset the data in the shift register to 0x0
2. Ensure the LEDs light up to show: 10100101
3. Set #Switch 0* to 1
4. Toggle #Switch 1* high and low
5. Check LEDs to be 01001011
6. Set #Switch 0* to 0
7. Toggle #Switch 1* high and low
8. Check LEDs to be 10010110
9. Press #Button 0* to reset the data
10. Ensure LEDs are once again 10100101

We have now tested all of the functionality of the FPGA.

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