







\$./multiplexer

VCD info: dumpfile multiplexer.vcd opened for output.

A0	A1	I0	I1	I2	I3	Out
0	0	x	x	x	x	x
0	0	0	x	x	x	0
0	0	1	x	x	x	1
1	0	x	0	x	x	0
1	0	x	1	x	x	1
0	1	x	x	0	x	0
0	1	x	x	1	x	1
1	1	x	x	x	0	0
1	1	x	x	x	1	1

\$./decoder

VCD info: dumpfile decoder.vcd opened for output.

En	A0	A1	00	01	02	03	Expected Output
0	0	0	0	0	0	0	All false
0	1	0	0	0	0	0	All false
0	0	1	0	0	0	0	All false
0	1	1	0	0	0	0	All false
1	0	0	1	0	0	0	00 Only
1	1	0	0	1	0	0	01 Only
1	0	1	0	0	1	0	02 Only
1	1	1	0	0	0	1	03 Only

\$./adder

VCD info: dumpfile adder.vcd opened for output.

A	B	Cin	S	Cout
0	0	0	0	0
1	0	0	1	0
0	1	0	1	0
1	1	0	0	1
0	0	1	1	0
1	0	1	0	1
0	1	1	0	1
1	1	1	1	1