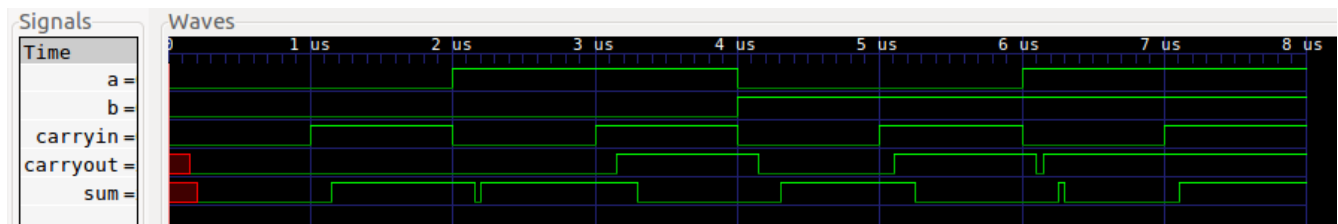


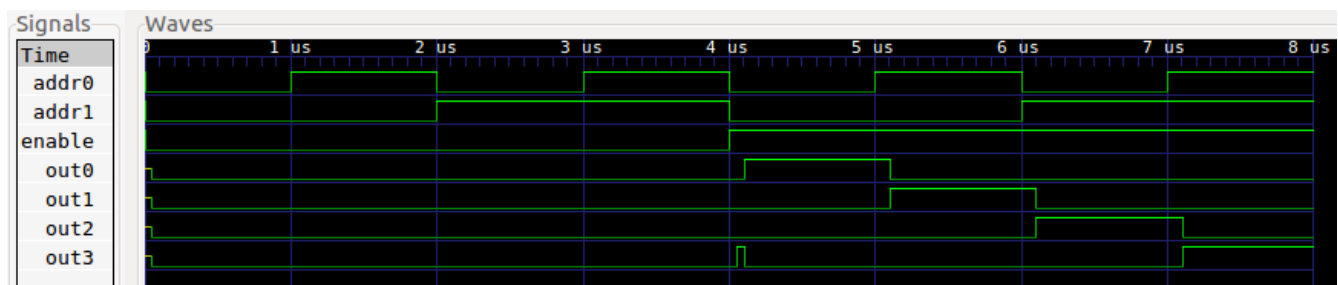
Adder:

```
apan@apan-Latitude-E5450:~/Documents/fall17/comparch/HW2$ ./adder
a b carryin | sum carryout | Expected Output
0 0 0 | 0 0 | 0 0
0 0 1 | 1 0 | 1 0
1 0 0 | 1 0 | 1 0
1 0 1 | 0 1 | 0 1
0 1 0 | 1 0 | 1 0
0 1 1 | 0 1 | 0 1
1 1 0 | 0 1 | 0 1
1 1 1 | 1 1 | 1 1
```



Decoder:

```
apan@apan-Latitude-E5450:~/Documents/fall17/comparch/HW2$ ./decoder
VCD info: dumpfile decoderTest.vcd opened for output.
En A0 A1 | 00 01 02 03 | Expected Output
0 0 0 | 0 0 0 0 | All false
0 1 0 | 0 0 0 0 | All false
0 0 1 | 0 0 0 0 | All false
0 1 1 | 0 0 0 0 | All false
1 0 0 | 1 0 0 0 | 00 Only
1 1 0 | 0 1 0 0 | 01 Only
1 0 1 | 0 0 1 0 | 02 Only
1 1 1 | 0 0 0 1 | 03 Only
```



Multiplexer:

```
apan@apan-Latitude-E5450:~/Documents/fall17/comparch/HW2$ ./multiplexer
A0 A1 i0 i1 i2 i3 | out | Expected Output
0 0 0 0 0 0 | 0 | False
0 0 1 0 0 0 | 1 | True
1 0 0 1 0 0 | 1 | True
0 1 0 0 1 0 | 1 | True
1 1 0 0 0 1 | 1 | True
```

