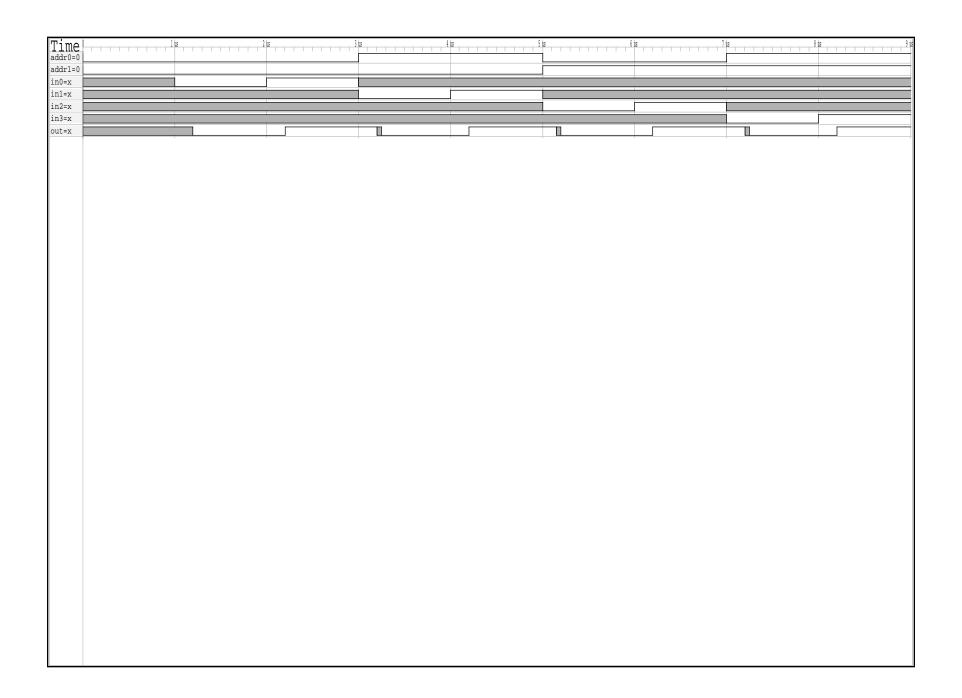
Time	1	us 2	us 3 us	4	is 5	is 6 ti	S 71	us 8 ius
a b								
carryin								
carryout								_
sum								

Time	1 ls	2 lus 3 lus	4	is 5 t	S 61	us 1	us 8 us
Time							
addr1							
enable							
out0							
out1							
out2							
out3				Л			



```
$ ./multiplexer
VCD info: dumpfile multiplexer.vcd opened for output.
A0 A1
      | I0 I1 I2 I3 |
                        Out
 0
    0
          x
             х
                 х
                    х
                           х
 0
    0
          0
                           0
             х
                 х
                    Х
    0
          1
                           1
             Х
                Х
                    Х
 1
    0
             0
                           0
          Х
                 Х
                    Х
 1
    0 I
             1
                           1
                 х
                    Х
          Х
 0
    1
                           0
          х
             Х
                 0
                    Х
 0
    1
                1
                           1
          х
             Х
                    хI
 1
    1
                    0
                           0
          х
             х
                 х
 1
    1
          Х
             х
                 х
                    1
                           1
$ ./decoder
VCD info: dumpfile decoder.vcd opened for output.
En A0 A1 | 00 01 02 03 | Expected Output
                      0 | All false
   0
      0 I
            0
               0
                   0
                      0 | All false
   1
      0 |
            0
               0
                   0
                      0 | All false
0
   0
      1
            0
                   0
               0
                      0 | All false
   1
      1
0
            0
               0
                  0
1
   0
      0
            1
               0
                   0
                      0 | 00 Only
                      0 | 01 Only
1
   1
      0
            0
               1
                   0
1
   0
               0
                   1
                      0 | 02 Only
      1
            0
1
                   0
                      1
   1
      1 I
            0
                          03 Only
$ ./adder
VCD info: dumpfile adder.vcd opened for output.
       Cin I
               S Cout
 Α
               0
 0
    0
          0
    0
 1
          0
               1
                     0
 0
    1
          0
               1
                     0
 1
    1
          0
               0
                     1
          1
 0
    0
               1
                     0
 1
    0
          1
               0
                     1
                     1
 0
    1
          1
               0
          1
                1
                     1
```