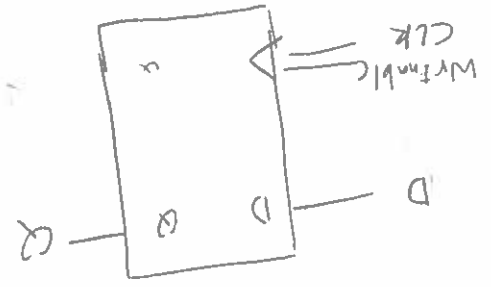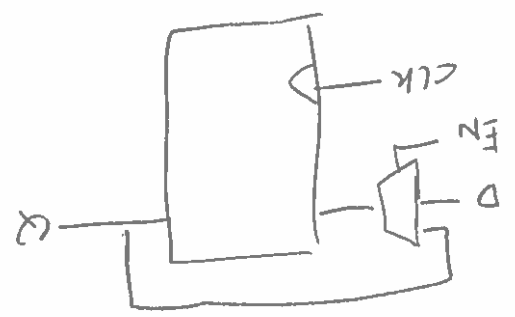## Deliverable 6

The module will bitshift(L) the enable bit by all of the address bit.
The range will match the output possibilities we can input w/ 5 bits
0-32.

Incorrect Register

Correct Register
Deliverable 1