CompArch HW4

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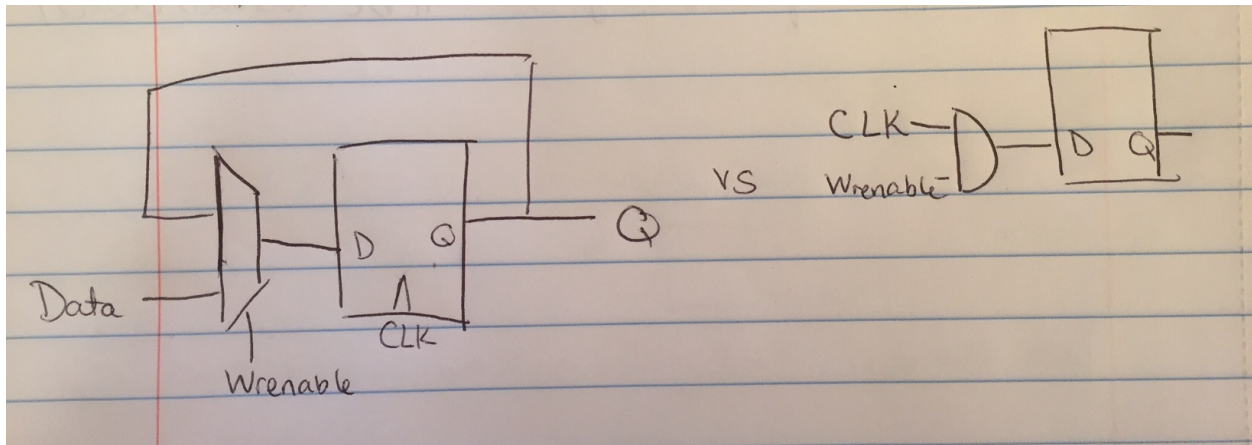
October 19, 2017

Deliverable 1

Figure 1

Deliverable 6

The "¡¡" operate denotes "shift x by y, when it is written $x << y$. Therefore, when it says "assign out = enable ¡¡ address", the enable bit is shifting by the value given by the address (which could be anything from 0 to 31). The enable bit is either 0 (do not write) or 1 (write). If enable is 1, then when it is shifted it will be a 1 at the location that should be written to. This value is then assigned to the output.