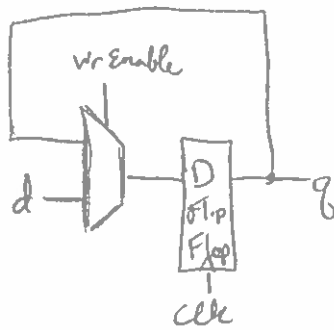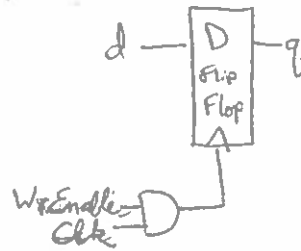Kaitlyn Keil
Comp Arch Fall '17
HW 4

1.

Good

Bad (Glitchy)

6. This module (decoder) shifts the 1 provided by Enable (if high) over address bits, meaning that one + only one of 32 wires is high, with that wire at the corresponding address. These wires can then be used as wr Enables to each register, so that at most only 1 register can be written to.