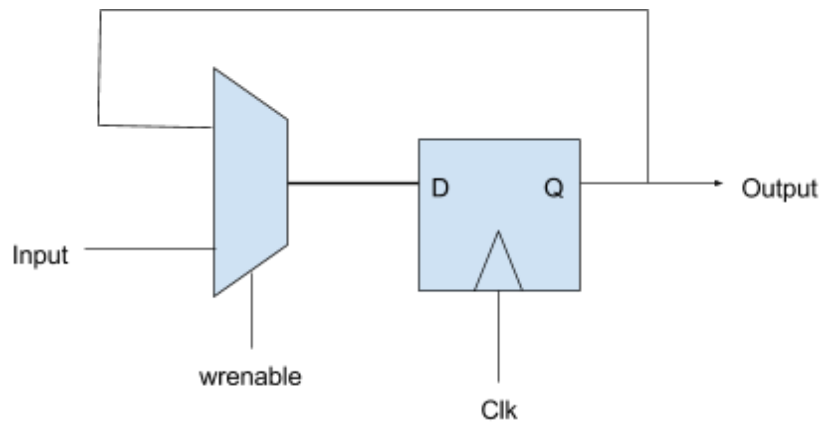


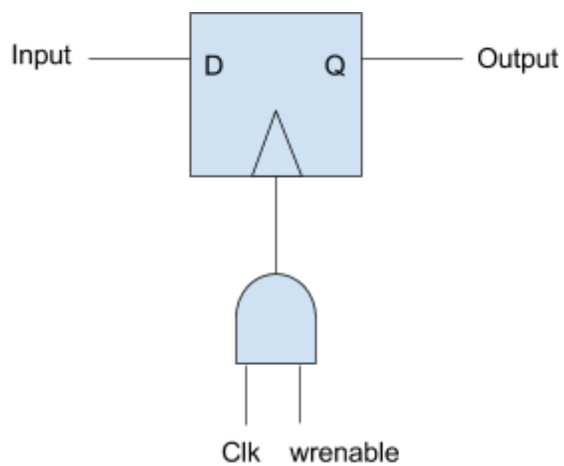
HW4 Report
Serena Chen

1)

Register 1:



Register 2 (Gated clock):



6) enable is always a single bit, either a 0 or a 1. When you shift it over using address, it moves the enable bit over address many digits, or moving it to position at index address. Therefore, all the bits will be zero, except the bit at address will take on the value of enable.

