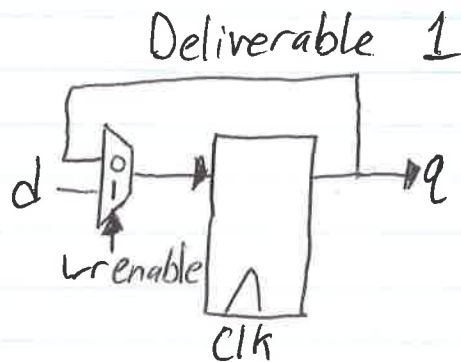
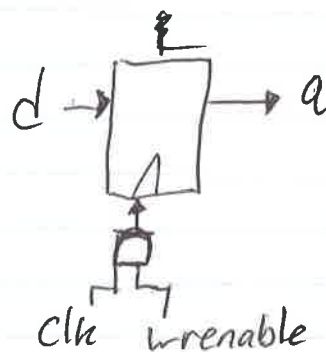


HW4

Eg 1.)



Eg 2.)



Deliverable 2

When enable is ~~active~~ ~~active~~ active, the address will bit shift, changing the output of the decoder to a new number that will align with a register from 0 to 31.