

**Dependable Distributed Systems**  
**Exercise week 2**  
**October 4<sup>th</sup>, 2023**

### Exercise 1

With reference to the synchronization of physical clocks, provide the definition of internal and external clock synchronization. In addition, consider a system composed of two processes  $p_1$  and  $p_2$  and one UTC server  $p_s$ . Let us assume that:

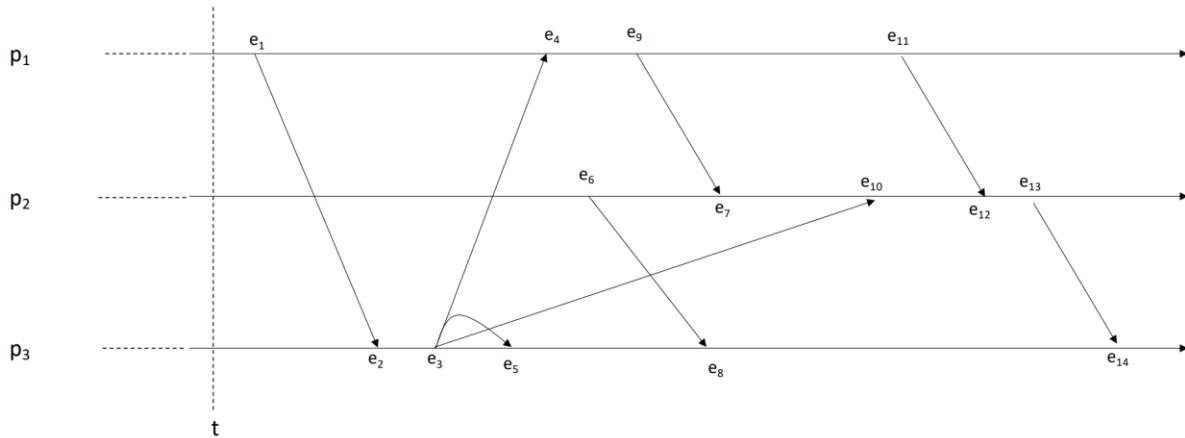
- $p_1$  and  $p_2$  communicate with  $p_s$  by using perfect point-to-point links;
- the maximum latency of the channel between  $p_s$  and  $p_1$  is 1 ms;
- the maximum latency of the channel between  $p_s$  and  $p_2$  is 2 ms.

In addition, let us assume that  $p_1$  and  $p_2$  start a clocks synchronization procedure at a certain time  $t$  by running the Christian algorithm. Answer to the following questions:

1. How much is the accuracy bound  $D_{ext}$  of the external synchronization obtained by  $p_1$  and  $p_2$  at the end of the synchronization?
2. Is the current system internally synchronized? If yes, determine the internal synchronization bound  $D_{int}$  obtained at the end of the procedure.

### Exercise 2

Let us consider the execution history depicted in the figure



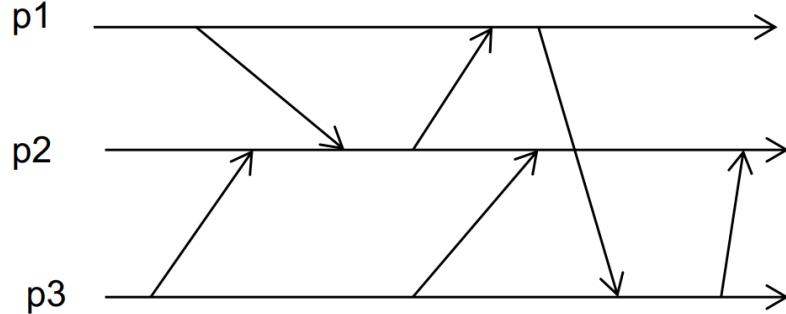
**Given the run depicted in the figure state the truthfulness of the following sentences:**

		T	F
a	According to the happened-before relation, $e_5 \rightarrow e_7$		
b	According to the happened-before relation, $e_4 \parallel e_5$		
c	Let $CK_i$ be the variable storing the scalar logical clock of process $p_i$ . Let us assume that at time $t$ , $CK_i = 0$ for each process $p_i$ . The logical clock $CK_2$ associated to $e_6$ is strictly larger than the logical clock $CK_1$ associated to $e_1$		
d	Let $CK_i$ be the variable storing the scalar logical clock of process $p_i$ . If at time $t$ $CK_3 = 0$ then the logical clock associated to $e_8$ is $CK_3=3$		
e	Let $CK_i$ be the variable storing the vector logical clock of process $p_i$ . If at time $t$ $CK_3 = [0, 0, 0]$ then the logical clock associated to $e_8$ is $CK_3=[3, 0, 3]$		

For each point, provide a justification for your answer

### Exercise 3

Describe timestamping techniques based on scalar logical clocks and vector logical clocks. In addition, considering the execution reported in Figure, answer to the following questions:



1. Apply the scalar clock timestamping technique to the execution assigning a timestamp to each event
2. Apply the vector clock timestamping technique to the execution assigning a timestamp to each event
3. List all pairs of concurrent events in the proposed execution

### Exercise 4

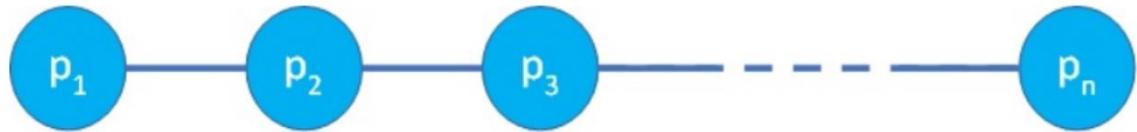
Consider an asynchronous message passing system that uses vectors clock to implement some causal consistency check. The message passing system is composed by 4 processes with IDs 1 to 4, and, as usual, the ID is used as displacement in the vector clock (i.e., the locations are  $(p1, p2, p3, p4)$ ). Vector clocks are updated increasing before send. Processes communicate by point2point links. You start debugging process  $p1$  (the process with id 1) in the middle of the algorithm execution. You see the following stream of messages exiting and entering the ethernet card of process  $p1$ :

- Time 00:00 - EXITING: Send Message [MSG CONTENT] Vector Clock:  $(1, 0, 0, 0)$
- Time 00:05 - ENTERING: Rcvd Message [MSG CONTENT] Vector Clock:  $(1, 2, 3, 1)$

1. Draw an execution that justifies the vectors clocks you are seeing. Is such an execution unique?
2. There exists an execution that justifies the vector clocks and where there exists at least a process that does not send any message? Justify your answer.

## Exercise 5

Let us consider a distributed system composed of  $N$  processes  $p_1, p_2, \dots, p_n$ , each one having a unique integer identifier. Processes are arranged in line topology as in the following figure, with consecutive identifiers.



Let us assume that there are no failures in the system (i.e., processes are always correct) and that topology links are implemented through perfect point-to-point links.

Write the pseudo-code of a distributed algorithm that builds the abstraction of a perfect point-to-point link between any pair of processes (i.e., also between those that are not directly connected, such as  $p_1$  and  $p_3$ ).

## Exercise 1

With reference to the synchronization of physical clocks, provide the definition of internal and external clock synchronization. In addition, consider a system composed of two processes p<sub>1</sub> and p<sub>2</sub> and one UTC server p<sub>s</sub>. Let us assume that:

- p<sub>1</sub> and p<sub>2</sub> communicate with p<sub>s</sub> by using perfect point-to-point links;
- the maximum latency of the channel between p<sub>s</sub> and p<sub>1</sub> is 1 ms;
- the maximum latency of the channel between p<sub>s</sub> and p<sub>2</sub> is 2 ms.

NO DUPLICATION  
↑ NO CREATION  
↓ RELIABLE DELIVERY

In addition, let us assume that p<sub>1</sub> and p<sub>2</sub> start a clocks synchronization procedure at a certain time t by running the Christian algorithm. Answer to the following questions:

1. How much is the accuracy bound D<sub>ext</sub> of the external synchronization obtained by p<sub>1</sub> and p<sub>2</sub> at the end of the synchronization?
2. Is the current system internally synchronized? If yes, determine the internal synchronization bound D<sub>int</sub> obtained at the end of the procedure.

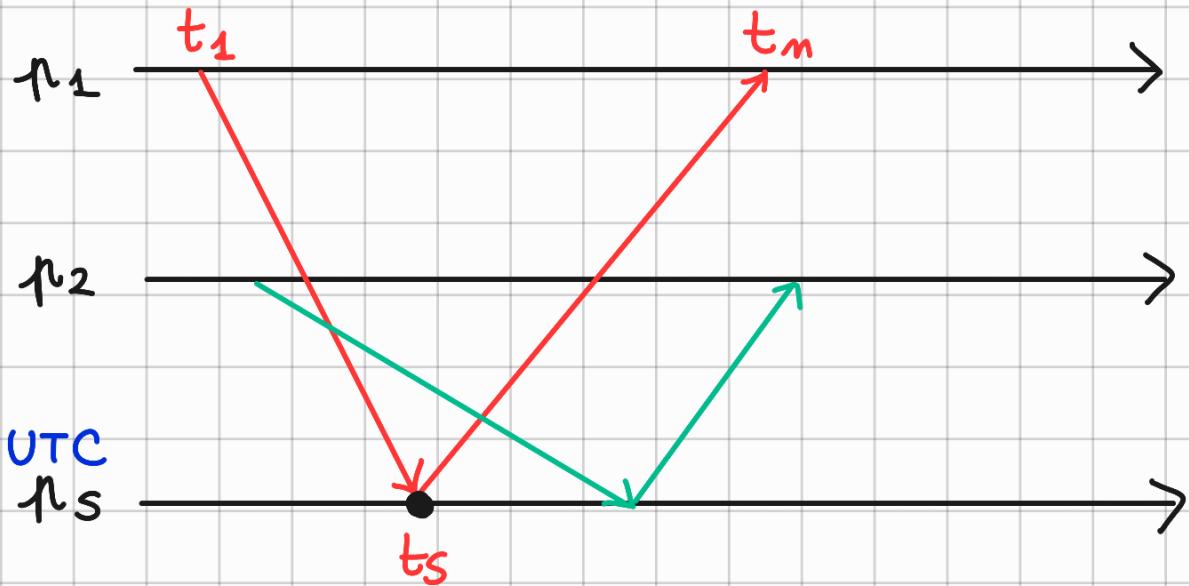
**EXTERNAL SYNCHRONIZATION:** means that the clock of all process are synchronized with an external source of time (UTC). The clocks C<sub>i</sub> are **external sy.** with a time source S(UTC) if for each time interval I :

$$|S(t) - C_i(t)| < D \quad \begin{array}{l} i = 1, \dots, N \\ D = \text{bound} \\ t = \text{real time in } I \end{array}$$

**INTERNAL SYNCHRONIZATION:** means that the clock of all process are synchronized between them. The clocks are **internal sy.** in a time interval I :

$$|C_i(t) - C_j(t)| < D \quad \begin{array}{l} i, j = 1, \dots, N \\ D = \text{bound} \\ t = \text{real time in } I \end{array}$$

clock of p<sub>i</sub>      ↗ clock of p<sub>j</sub>



①

$$\begin{aligned}
 n_1: D_{ext_1} &= \pm \frac{RTT}{2} - \text{min} \\
 &= \pm \frac{1(t_1-ts) + 1(ts-tm)}{2} - 0 \\
 &= \pm 1 \text{ ms}
 \end{aligned}$$

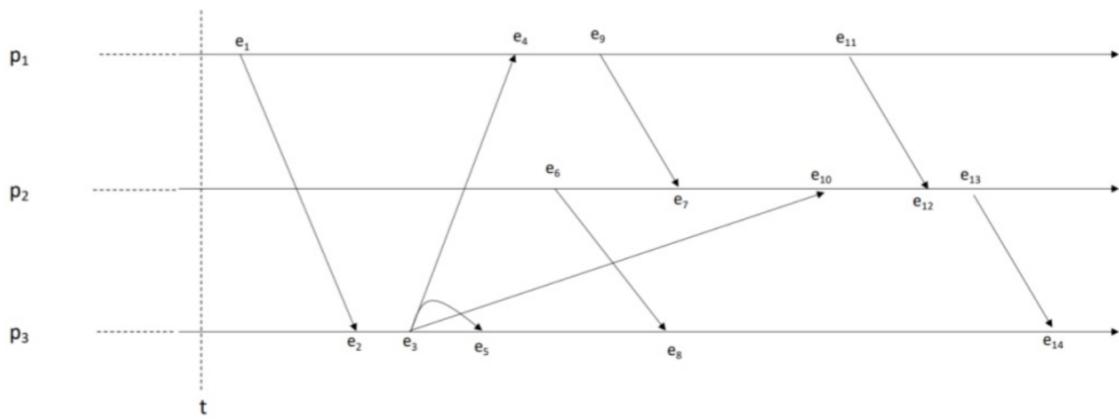
$$n_2: D_{ext_2} = \pm \frac{RTT}{2} - \text{min} = \pm \frac{4}{2} - 0 = \pm 2 \text{ ms}$$

② EXT → INT  $D_{int} = |(+2) - (-1)| = 3 \text{ ms}$

Worst  
extreme

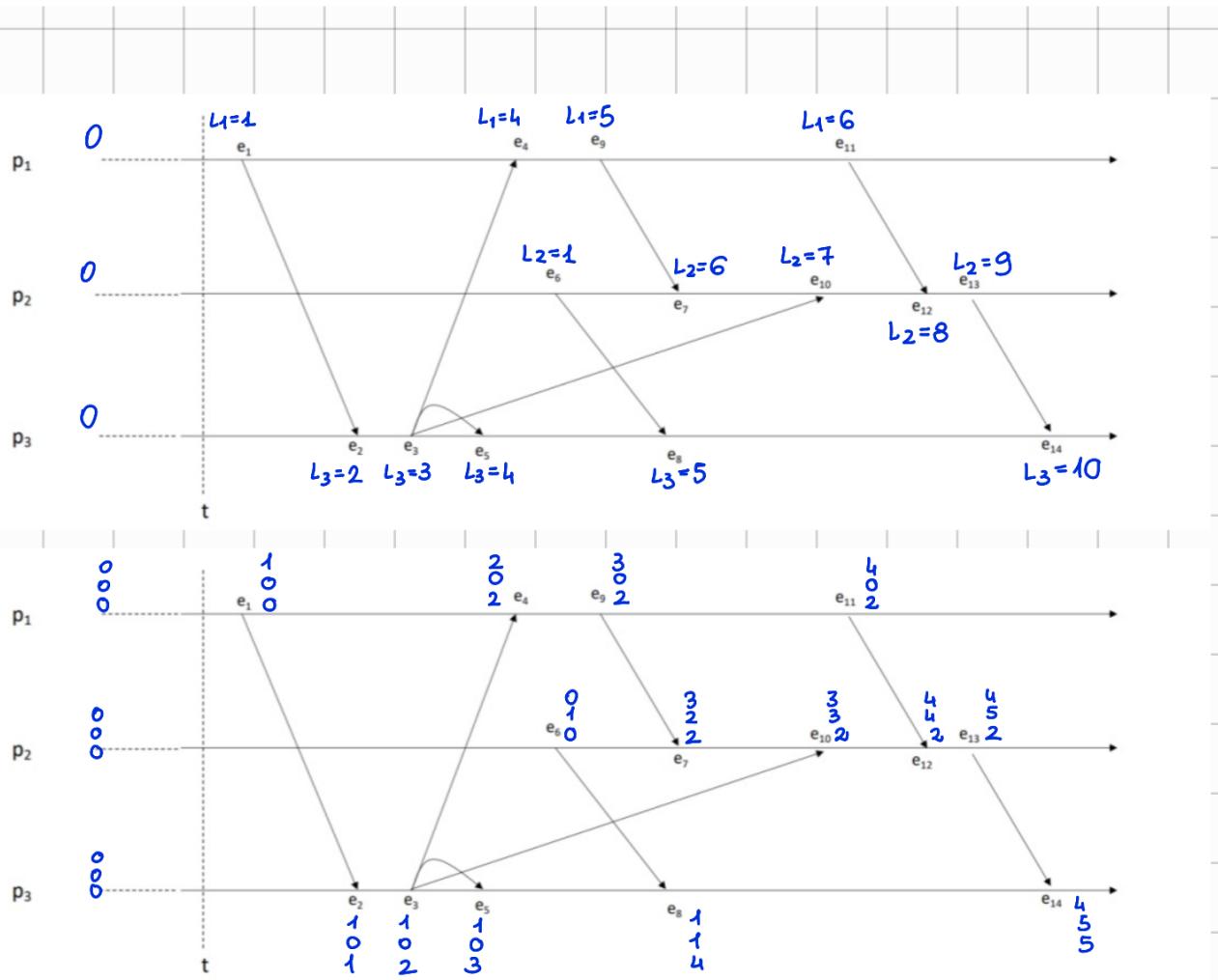
## Exercise 2

Let us consider the execution history depicted in the figure



Given the run depicted in the figure state the truthfulness of the following sentences:		
a	According to the happened-before relation, $e_5 \rightarrow e_7$	T <input checked="" type="radio"/> F <input type="radio"/>
b	According to the happened-before relation, $e_4 \parallel e_5$	T <input checked="" type="radio"/> F
c	Let $CK_i$ be the variable storing the scalar logical clock of process $p_i$ . Let us assume that at time $t$ , $CK_i = 0$ for each process $p_i$ . The logical clock $CK_2$ associated to $e_6$ is strictly larger than the logical clock $CK_1$ associated to $e_1$	T <input checked="" type="radio"/> F <input type="radio"/>
d	Let $CK_i$ be the variable storing the scalar logical clock of process $p_i$ . If at time $t$ $CK_3 = 0$ then the logical clock associated to $e_8$ is $CK_3=3$	T <input checked="" type="radio"/> F <input type="radio"/>
e	Let $CK_i$ be the variable storing the vector logical clock of process $p_i$ . If at time $t$ $CK_3 = [0, 0, 0]$ then the logical clock associated to $e_8$ is $CK_3=[3, 0, 3]$	T <input checked="" type="radio"/> F <input type="radio"/>

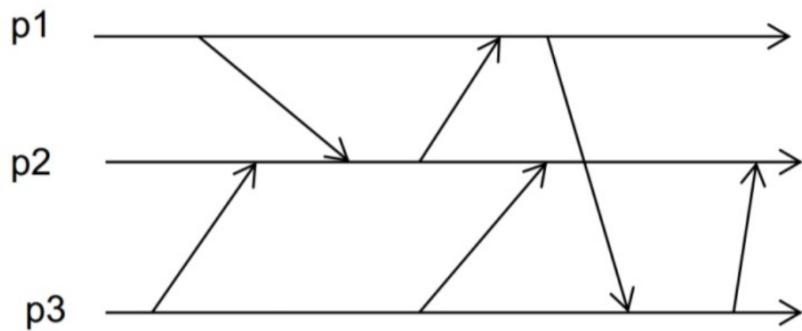
For each point, provide a justification for your answer



- a) F, the events are concurrent (happened-before relation violated) and they are in a different local history
- b) T,  $e_4$  and  $e_5$  are concurrent because they are in a different local history but have the same logical clock (4)
- c) F, they have the same logical clock (1)
- d) F,  $e_8$  have a  $L_3 = 5$
- e) F,  $e_8$  have a vector clock equal to  $\begin{bmatrix} 1 \\ 1 \\ 4 \end{bmatrix}$

### Exercise 3

Describe timestamping techniques based on scalar logical clocks and vector logical clocks. In addition, considering the execution reported in Figure, answer to the following questions:

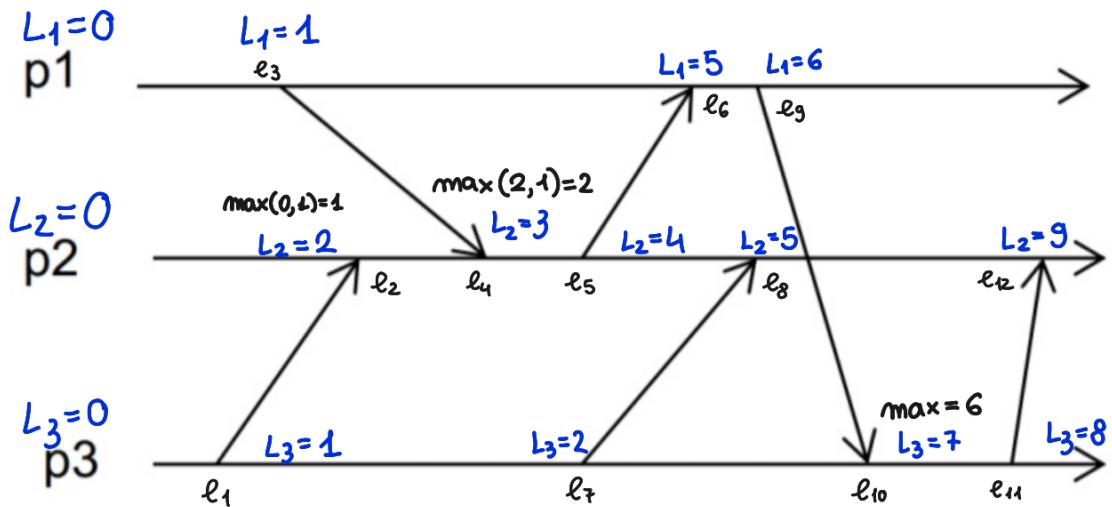


1. Apply the **scalar clock** timestamping technique to the execution assigning a timestamp to each event
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3. List all pairs of **concurrent events** in the proposed execution

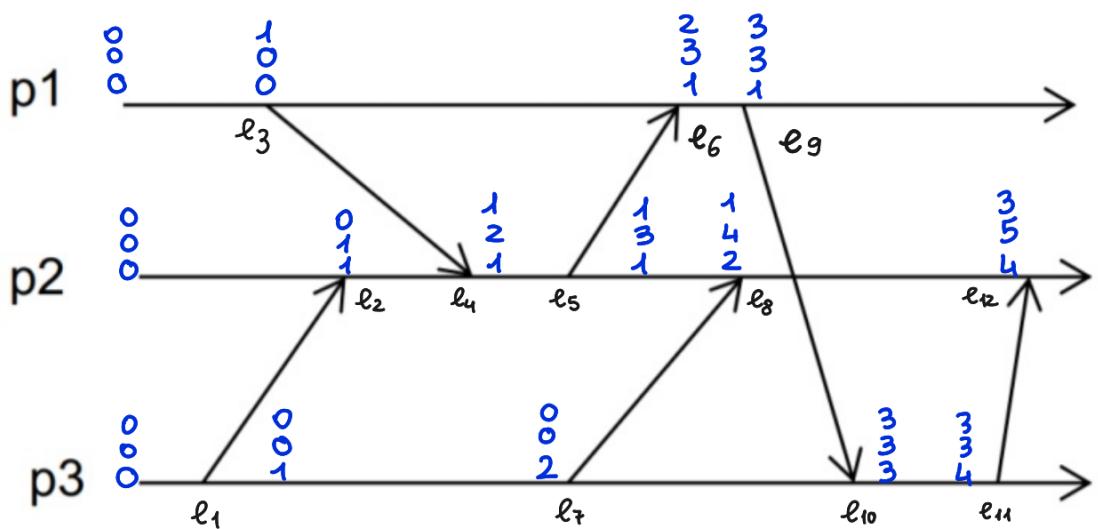
In the **scalar logical clocks technique**, each process  $p_i$  initializes its logical clock  $L_i = 0$  and increases  $L_i$  of 1 when it generates an event ( $L_i = L_i + 1$ ). In particular, when  $p_i$  sends a message  $m$ , it increases  $L_i$  and timestamp  $m$  with  $ts = L_i$ . While when  $p_i$  receives a message the logical clock is updated  $L_i = \max(ts, L_i)$ .

In the **vector clocks technique** for a set of  $n$  processes the vector clock is composed by an array of  $n$  integers. So, each process  $p_i$  maintains a vector clock  $V_i$  and timestamps events by mean of its vector clock.

1



2



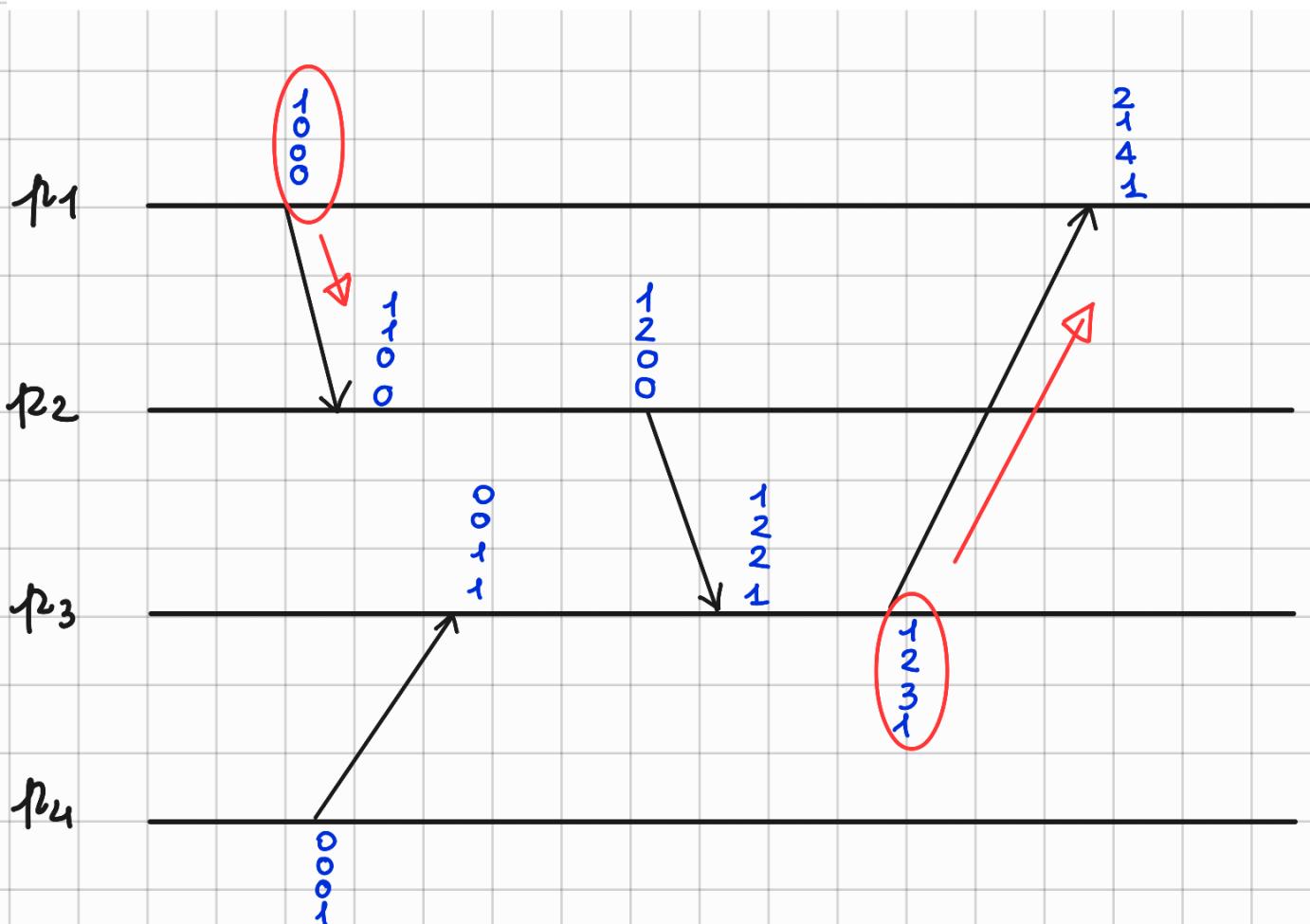
Concurrence:  $l_2 \parallel l_7$ ,  $l_1 \parallel l_3$ ,  $l_8 \parallel l_6$ ,  $l_2 \parallel l_3$ ,  $l_8 \parallel l_{10}$

## Exercise 4

Consider an asynchronous message passing system that uses vectors clock to implement some causal consistency check. The message passing system is composed by 4 processes with IDs 1 to 4, and, as usual, the ID is used as displacement in the vector clock (i.e., the locations are  $(p_1, p_2, p_3, p_4)$ ). Vector clocks are updated increasing before send. Processes communicate by point2point links. You start debugging process  $p_1$  (the process with id 1) in the middle of the algorithm execution. You see the following stream of messages exiting and entering the ethernet card of process  $p_1$ :

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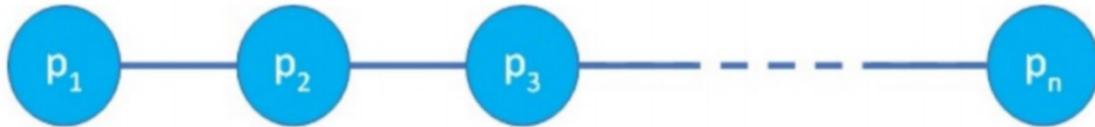


① No, it's not unique because we can design another execution

② No, because  $p_1$  received a message that is increased at least 1 time by each process

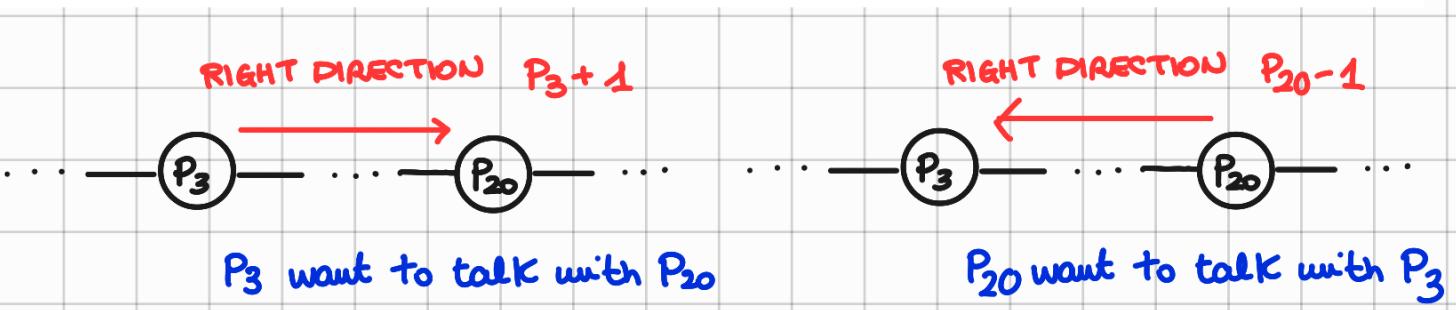
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**PROCEDURE INIT :**

*id*

// SELF

*perfect link  
global*

*to who*  
↓  
*what PAYLOAD*  
↓

UPON EVENT < PLG, SEND | DEST, m > DO

IF DEST > SELF THEN

TRIGGER < PP2PL, SEND | SELF+1, < SELF, DEST, m > >

ELSE IF DEST < SELF THEN

TRIGGER < PP2PL, SEND | SELF-1, < SELF, DEST, m > >

ELSE

TRIGGER < PP2PL, SEND | SELF, < SELF, DEST, m > >

*myself*

UPON EVENT  $\langle \text{PP2PL}, \text{DELIVER} \mid Q, \langle \text{SOURCE}, \text{DEST}, m \rangle \rangle$

IF DEST == SELF THEN

TRIGGER  $\langle \text{PLG}, \text{DELIVER} \mid \text{SOURCE}, m \rangle$

my self



ELSE IF DEST > SELF THEN

TRIGGER  $\langle \text{PP2PL}, \text{SEND} \mid \text{SELF} + 1, \langle \text{SOURCE}, \text{DEST}, m \rangle \rangle$

ELSE DEST < SELF THEN

TRIGGER  $\langle \text{PP2PL}, \text{SEND} \mid \text{SELF} - 1, \langle \text{SOURCE}, \text{DEST}, m \rangle \rangle$