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# Machine Language (Part 1)

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# Lab Project

- Project objectives: to have a taste of
  - Low-level programming
  - Hack assembly language
  - Hack hardware

# CPU Emulator

File View Run Help

The CPU Emulator interface includes a menu bar (File, View, Run, Help) and a toolbar with icons for file operations, execution (single step, break, reset), and speed control (Slow, Fast). It also features dropdown menus for animation (Program flow), view (Screen), and format (Decimal).

**ROM**

Address	Instruction
0	@0
1	D=M
2	@1
3	D=D+M
4	@2
5	M=D
6	@6
7	0; JMP
8-28	

**RAM**

Address	Value
0	0
1-28	0

**PC** 0 **A** 0

**ALU**

D Input : 0  
M/A Input : 0  
ALU output : 0

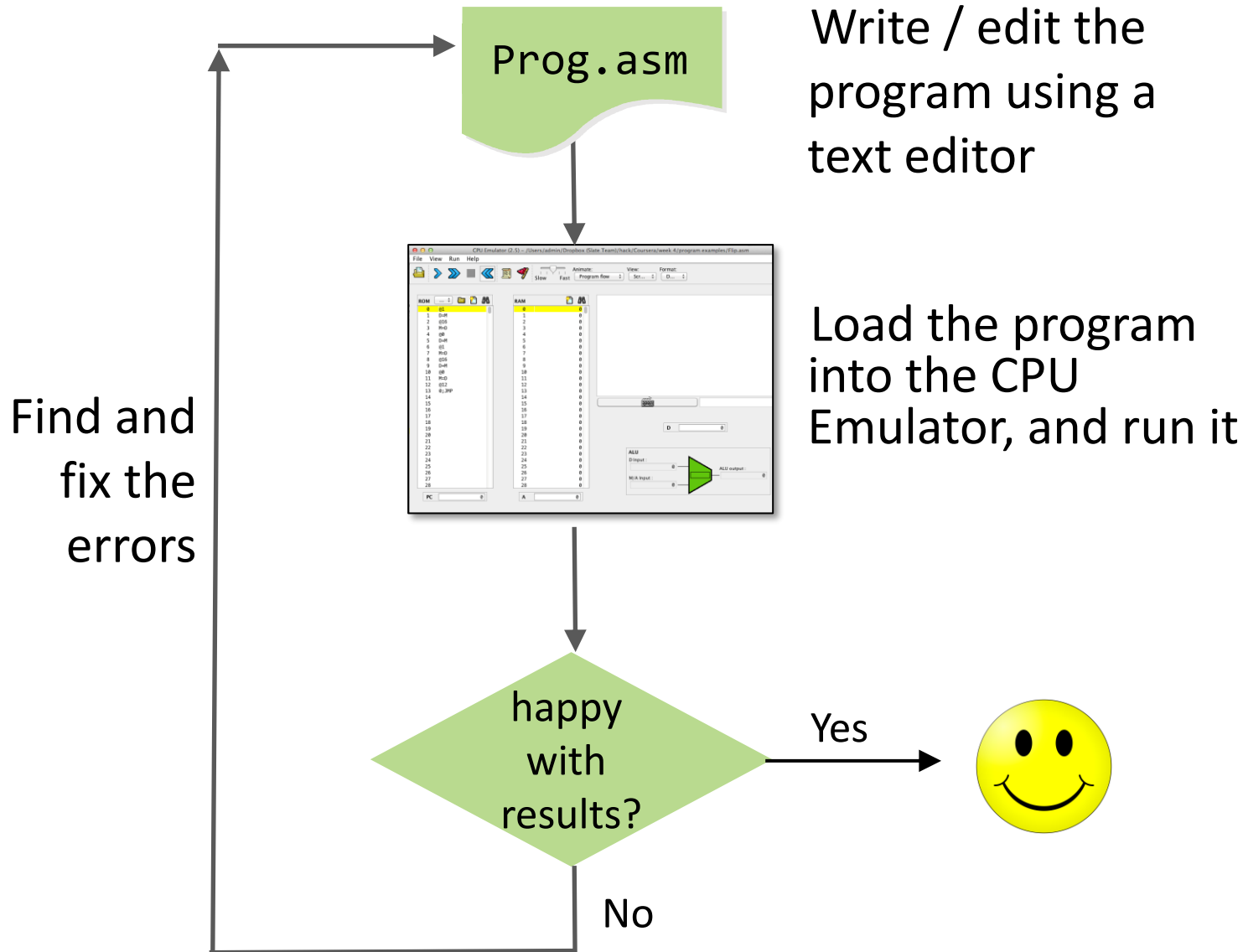
# CPU Emulator

- **Slow ... Fast** (change the speed of emulation).
- **Animation**
  - **Program flow**: show how the program proceeds.
  - **Program & data flow**: show how the program proceeds and the data vary.
  - **No animation**: no animation will be shown.
- **View**
  - **Script**: show script.
  - **Output**: show running results.
  - **Compare**: not in used for this lab.
  - **Screen**: show Hack Computer screen. (256×512, B/W)
- **Format**: show numbers in decimal, hexamal or binary.

# Misc

- File suffix
  - Binary code (.hack) files.
  - Assembly language (.asm) files.
- Test procedures
  - Open CPU Emulator.
  - Load xxx.asm into ROM, symbolic code without symbols (e.g. predefined symbols, labels, variables).
  - Load xxx.tst test script, run the test script.

# Program development process



# Best practice

## Well-written low-level code is

- Short
- Efficient
- Elegant
- Self-describing

## Technical tips

- Use symbolic variables and labels
- Use sensible variable and label names
- Variables: lower-case
- Labels: upper-case
- Use indentation
- Start with pseudo code.

# Task 1: add two numbers

- Input: RAM[0] and RAM[1].
- Output:  $\text{RAM}[2] = \text{RAM}[0] + \text{RAM}[1]$ .
- add2.asm



## Task 2: swap two numbers

- Set  $\text{RAM}[0] = 50$ ,  $\text{RAM}[1] = 100$ , then swap the value of  $\text{RAM}[0]$  and  $\text{RAM}[1]$ . You may use  $\text{RAM}[16]$  as the temporary variable.

# Task 3: signum

- Implement signum.asm to achieve the following function.

```
// Program: signum.asm
// Computes:
// if RAM[0]>0
//     RAM[1]=1
// else
//     RAM[1]=0
// Usage: put a value in RAM[0],
//     run and inspect RAM[1].
```

# Task 4: sgn function

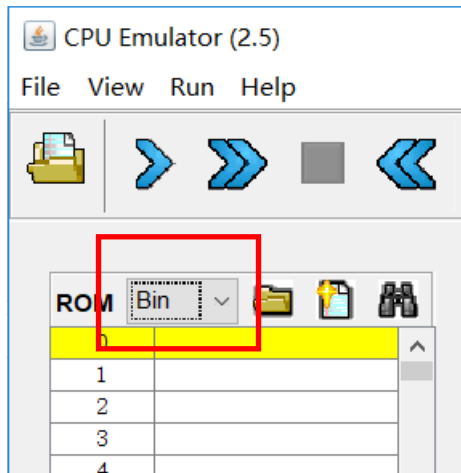
- Implement sgn function as follow.

$$\text{sgn}(x) := \begin{cases} -1 & \text{if } x < 0, \\ 0 & \text{if } x = 0, \\ 1 & \text{if } x > 0. \end{cases}$$

- You may assume that  $x$  is stored in RAM[0], and the returned value of function  $\text{sgn}(x)$  is stored in RAM[1].
- You should use R0, R1.
- You should use labels such as NEGATIVE, ZERO and END.

# Task 5: assembly to binary

- For task 1-4, choose one of them, translate the assembly code to binary code. Compare your translation with the translation by CPU Emulator.



# Acknowledgement

- This set of lecture notes are based on the lecture notes provided by Noam Nisam / Shimon Schocken.
- You may find more information on:  
[www.nand2tetris.org](http://www.nand2tetris.org).