

APPENDIX B
SYSTEM ARCHITECTURE

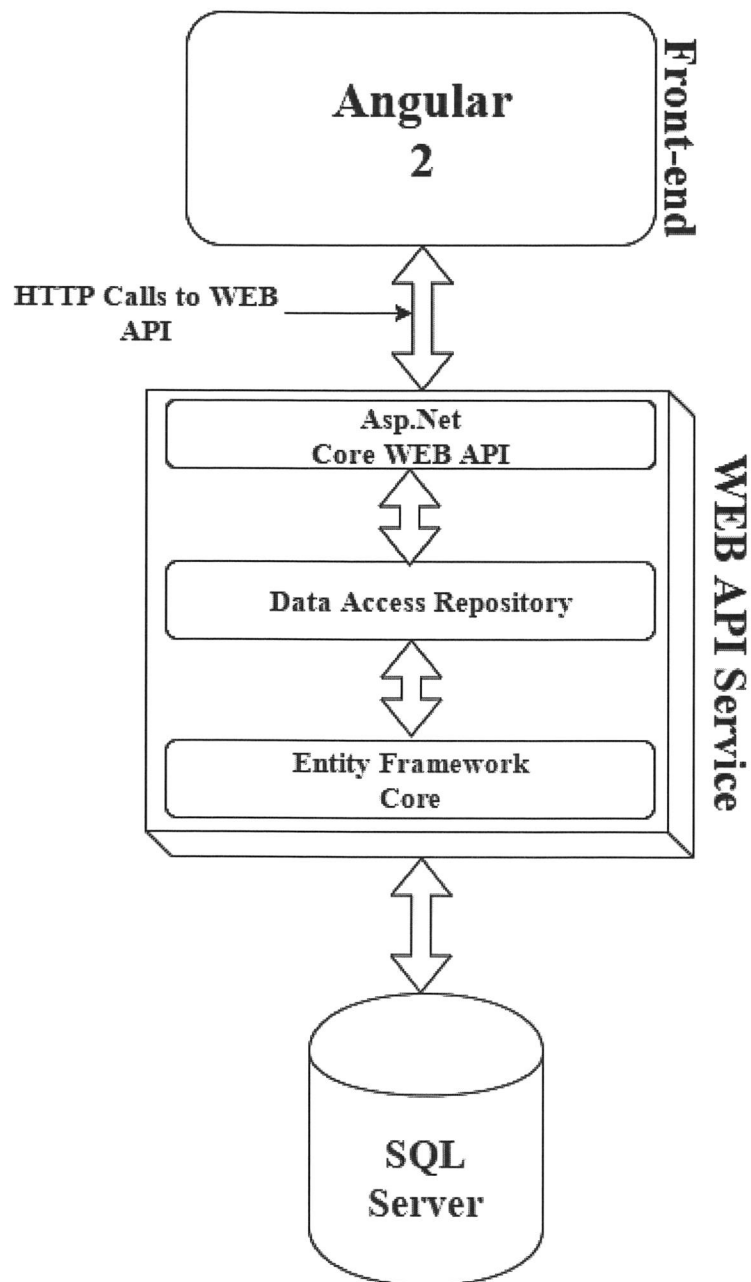


Fig. 3. System Architecture